

Marketing Bulletin

DATE: December 27th, 2006

TO: All Sales Personnel

FROM: Mark Stoner

RE: Product Termination

To all concerned parties,

This bulletin is to notify all customers of the discontinuation of the following Ecliptek series effective December 27th, 2006:

SeriesDescriptionEC165V 14 pin DIP Oscillator

Recommended Replacement EB51F3 or EB51F5

In compliance with our End of Life (EOL) policy, this will serve as advanced notice of product termination. New orders will not be accepted after March 31st, 2007, with delivery to conclude by June 30th 2007.

If there are any questions pertaining to this bulletin, please fell free to contact me. Thank you again for your cooperation.

Best Regards,

Mark W Summer

Mark W. Stoner Vice President of Marketing Ecliptek Corporation

	l <mark>6 Se</mark> r	ioc	Rot	13 P6			
 RoHS HCM0 5.0V 14 pi Stab Custo 	S Compliant (F OS/TTL output supply voltag in DIP package ility to ±5ppm om lead lengt ving option	Pb-free) t e	•	ЕС16 Н 8.0 L 20.8 W 13.2	OSCILLA	TOR	
LECTRICAL	SPECIFICAT ON	OBS	OLE	TE			
Frequency Rang	ge (MHz)			1.000MH;	z to 46.000MHz		
Operating Tem				Per Table			
Storage Temper				-55°C to 125°C			
Supply Voltage				5.0V _{DC} ±10			
Input Current	(-00)	1.000MHz to 20.0	000MHz		15mA Maximum		
Input current		20.001MHz to 46			40mA Maximum		
Frequency Tolerance / Stability		vs. Operating Temperature Range		Per Table 1			
		vs. Input Voltage ($V_{DD} \pm 5\%$)		±2.0ppm Maximum			
		vs. Load (±2pF ±2		±1.0ppm1			
Internal Trim (Top of (an)	v3: Loud (ILbi I.		±5ppm Mi			
Internal Trim (Top of Can) Output Voltage Logic High (V _{0H})		w/TTL Load		2.4V _{pc} Min		I _{он} = -16mA	
output vottage		w/HCMOS Load			Minimum	I _{он} = -16mA	
Output Voltage Logic Low (V _o)		w/TTL Load		0.4 V _{DC} Ma	-	$I_{0H} = -10 mA$ $I_{0L} = +16 mA$	
output vollage		w/HCMOS Load		0.4 V _{DC} Max 0.5V _{DC} Max		$I_{0L} = +10mA$ $I_{0L} = +16mA$	
Dico Timo / Fall	Timo	,	orm w/HCMOS Load or	50	ds Maximum	$I_{0L} = \pm 10 \text{ mA}$	
Rise Time / Fall Time				0 11500110			
Duty Cycle		at 1.4V _{DC} w/TTL L		E0 : 10/0/) (Standard)		
Duly Lycle		at 1.4V _{DC} w/HCMOS Load or w/TTL Load 10% to 90% of Waveform w/HCMOS Load		50 ±10(%) (Standard)			
			,		(Ontional)		
Load Drive Cap	abilita.	or $0.4V_{DC}$ to $2.4V_{C}$	_c w/ TTL LOdu		(Optional)		
·	-	V . No Connectio	-		10TTL Load or 50pF HCMOS Load Enables Output		
Tri-State Input Voltage		V_{IH} : No Connection		Enables Output			
		$V_{IH}: \geq 2.2 V_{DC}$				1	
Anima /at 25 96	<u></u>	$V_{IL}: \leq 0.8V_{DC}$			Output: High Imped	lance	
Aging (at 25°C	.)		±1ppm / year Maximum 10 mSeconds Maximum				
Start Up Time	harded a		±100pSeconds Maximum				
Period Jitter: A				±25pSeconds Maximum			
Period Jitter: 0	me sigma			±25pSeC0	iius maximum		
MANUFACTURER	CATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV - DATE	

PART NUMBERING GUIDE

EC16 07 A R T TS - 24.000M - CL125

BSOLETE

FREQUENCY STABILITY

2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE 1 Letter Code Per Table 1

INTERNAL TRIM OPTIONS Blank=No Internal Trim

R=±5ppm Minimum (Top of Can)

DUTY CYCLE Blank=50±10(%) (Standard) T=50 ±5(%)

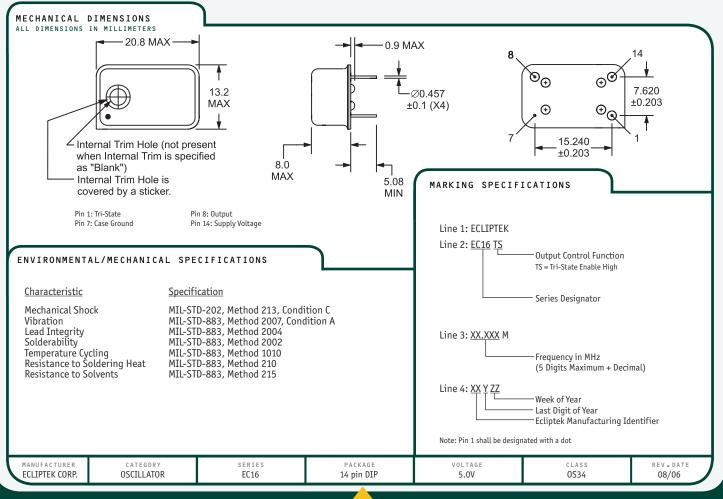
AVAILABLE OPTIONS

Blank=None (Standard) CLXXX=Custom Lead Length (See Page 133) G=Full Size Gull Wing (See Page 132)

FREQUENCY

OUTPUT CONTROL FUNCTION TS=Tri-State Enable High

TABLE 1: PART NUMBERING CODES										
Range		Frequency Stability X = Availability with Internal Trim Option "Blank" Y = Availability with Internal Trim Option "R"								
			±5ppm	±7ppm	±10ppm	±15ppm	±20ppm			
Temperature		Code	05	07	10	15	20			
Tem	0°C to +50°C	А	Y	Χ, Υ	Χ, Υ	Χ, Υ	Χ, Υ			
	-10°C to +60°C	В		Χ, Υ	Χ, Υ	Χ, Υ	Χ, Υ			
Operating	-20°C to +70°C	С			Χ, Υ	Χ, Υ	Χ, Υ			
ő	-40°C to +85°C	D					Χ, Υ			



800-ECLIPTEK www.ecliptek.com for latest revision

Specifications subject to change without notice.