



Parameter	Rating	Units
Blocking Voltage	70	V _p
Load Current	150	mA
Max On-Resistance	16	Ω
LED Current to Operate	1	mA

Transient Protection Characteristics

Peak Pulse Power	V _{WM}
600W	40.2V

Features

- 100% Solid State
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- 3750V_{rms} Input/Output Isolation
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable

Applications

- Security
- Sensor Circuitry
- Instrumentation
- Multiplexers
- Data Acquisition
- Electronic Switching
- I/O Subsystems
- Aerospace
- Industrial Controls

Description

The CPC1317 is a single-pole normally open (1-Form-A) solid state relay with bi-directional transient voltage suppressor (TVS) relay protection, which is designed to meet the requirements of EN50130-4 (installation class 3).

The relay output is constructed with efficient MOSFET switches and photovoltaic die that use Clare's patented OptoMOS architecture. The input, a highly efficient GaAlAs infrared LED, controls the optically coupled output.

The CPC1317 is available in an 8-pin, space-saving surface-mount package.

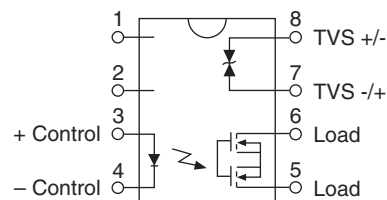
Approvals

- UL Recognized Component: File # E76270
- EN/IEC 60950 Compliant
- CSA Certified Component: Certificate # 1172007

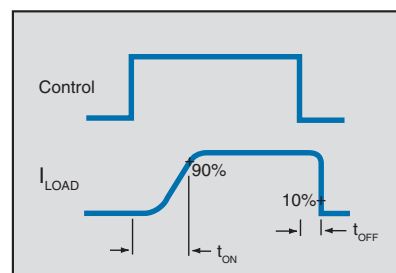
Ordering Information

Part #	Description
CPC1317P	8-Pin Flatpack (50/tube)
CPC1317PTR	8-Pin Flatpack (1000/reel)

Pin Configuration



Switching Characteristics of Normally Open (Form A) Devices



Absolute Maximum Ratings

Parameter	Ratings	Units
SSR Output Blocking Voltage	70	V _P
TVS Working Voltage, Maximum	40.2	V
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	150	mW
SSR Output Power Dissipation ²	400	mW
TVS Peak Pulse Power (I _{pp} =9.3A, 10/1000μs pulse)	600	W
Isolation Voltage Input to Output	3750	V _{rms}
Operating Temperature	-40 to +85	°C
Storage Temperature	-40 to +125	°C

¹ Derate Linearly 1.33 mw / °C

² Derate Linearly 6.67 mw / °C

Electrical absolute maximum ratings are at 25°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

SSR Electrical Characteristics

Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Load Current						
Continuous	-	I _L	-	-	150	mA
Peak	t=10ms	I _{LPK}	-	-	400	
On-Resistance ¹	I _L =150mA, I _F =1mA	R _{ON}	-	7	16	Ω
Off-State Leakage Current	V _L =70V	I _{LEAK}	-	-	1	μA
Switching Speeds						
Turn-On	I _F =5mA, V _L =10V	T _{ON}	-	-	2.5	ms
Turn-Off		T _{OFF}	-	-	2.5	
Output Capacitance	50V; f=1MHz	C _{OUT}	-	25	-	pF
Input Characteristics @ 25°C						
Input Control Current ²	I _L =150mA	I _F	-	-	1	mA
Input Dropout Current	-	I _F	0.1	-	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Common Characteristics @ 25°C						
Capacitance Input to Output	-	C _{I/O}	-	3	-	pF

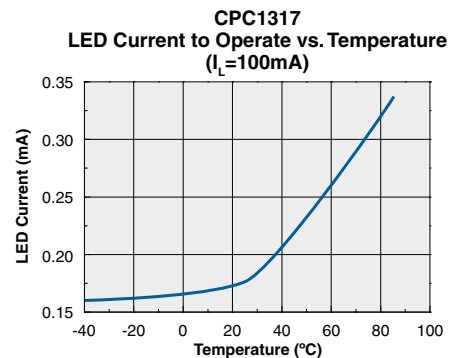
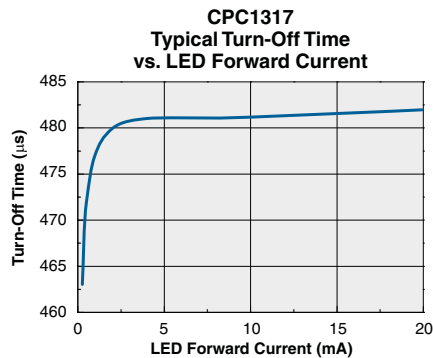
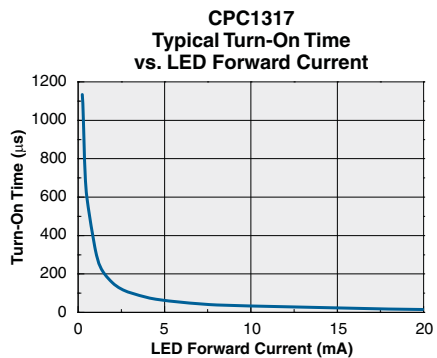
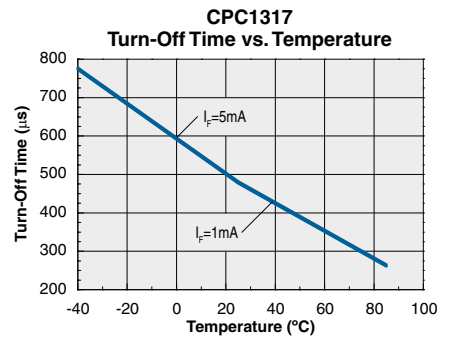
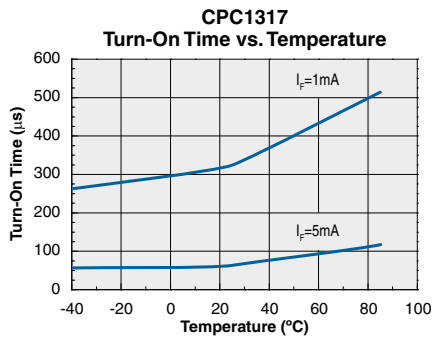
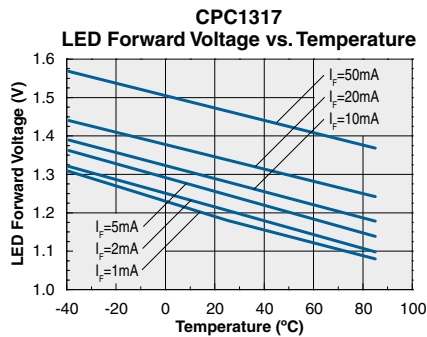
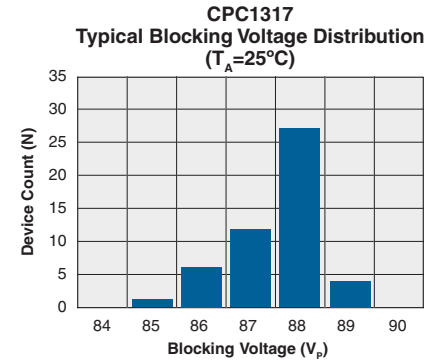
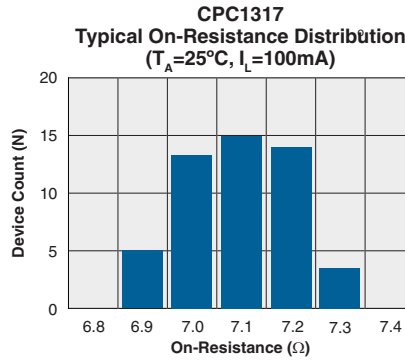
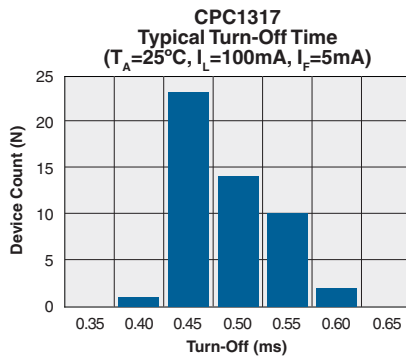
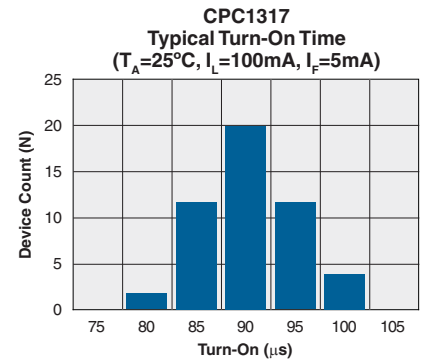
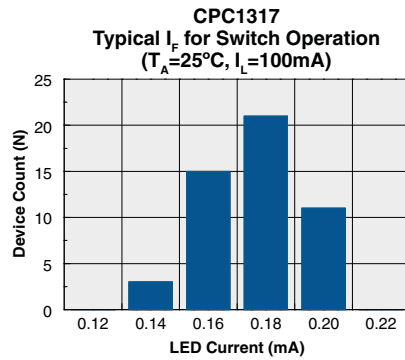
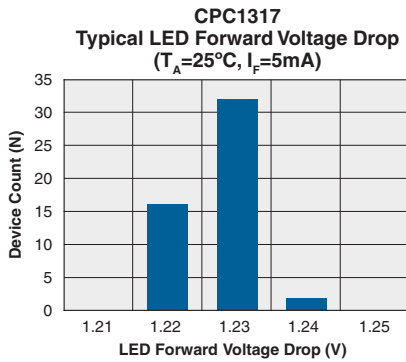
¹ Measurement taken within 1 second of turn-on time.

² For applications requiring high temperature operation (> 60°C) a minimum LED drive current of 3mA is required.

TVS Electrical Characteristics

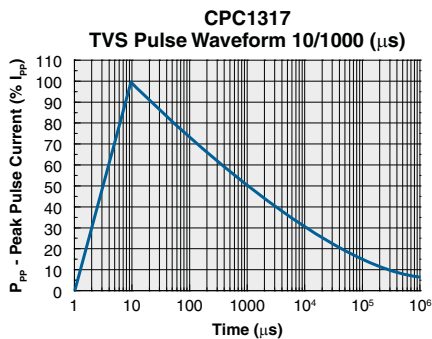
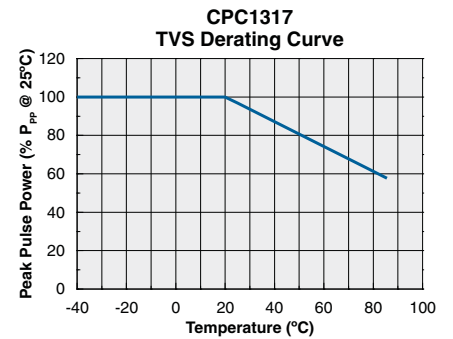
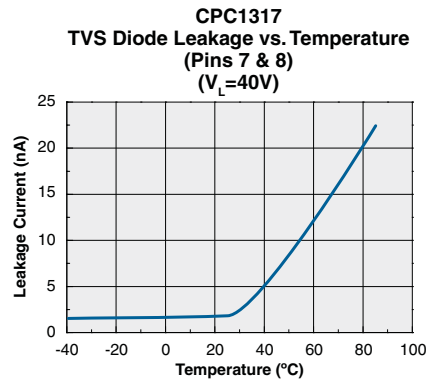
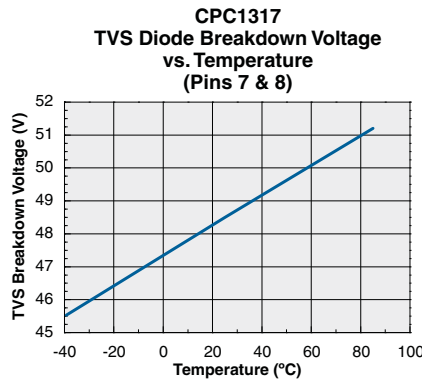
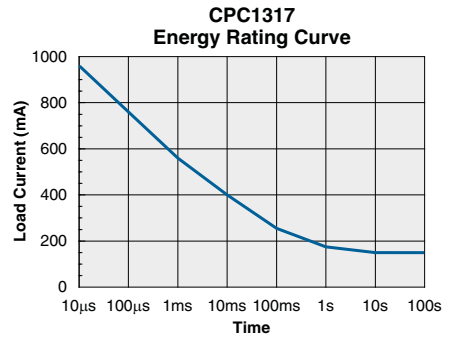
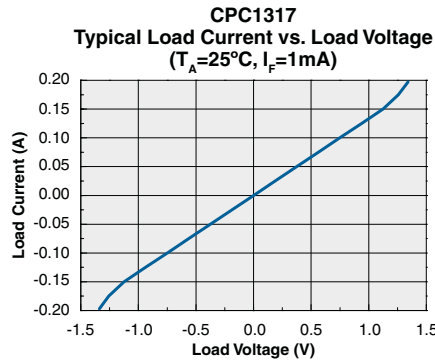
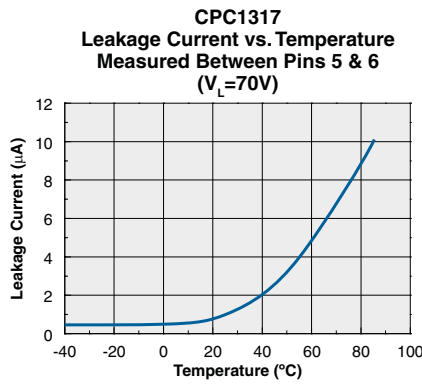
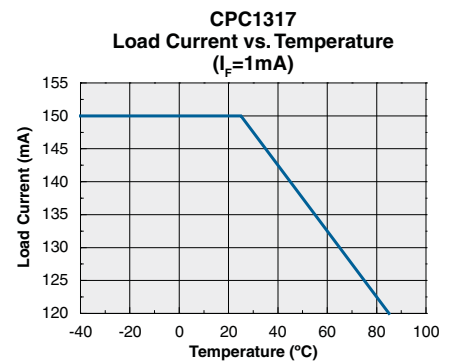
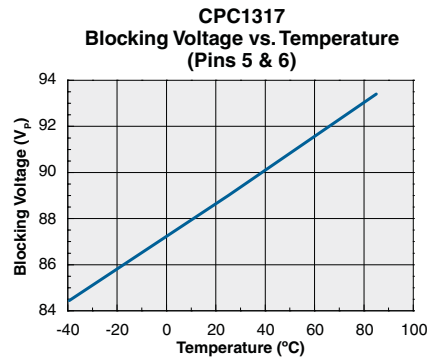
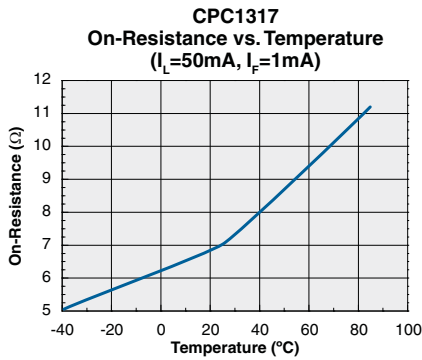
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Clamping Voltage	I _{pp} =9.3A	V _C	-	-	66.5	V
Reverse Breakdown Voltage	I=1mA	V _{BR}	44.4	-	-	V
Reverse Leakage Current	V _{WM} =40.2V	I _L	-	-	5	μA

PERFORMANCE DATA*



*The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

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MANUFACTURING INFORMATION

Soldering

For proper assembly, the component must be processed in accordance with the current revision of IPC/JEDEC standard J-STD-020. Failure to follow the recommended guidelines may cause permanent damage to the device resulting in impaired performance and/or a reduced lifetime expectancy.

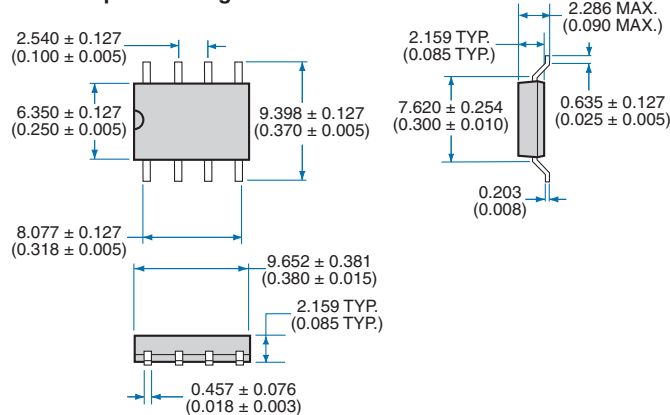
Washing

Clare does not recommend ultrasonic cleaning or the use of chlorinated solvents.

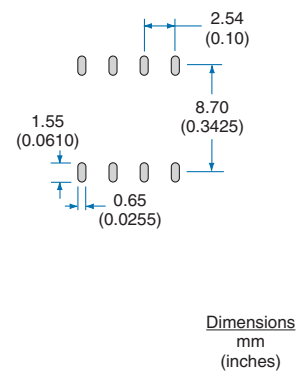


MECHANICAL DIMENSIONS

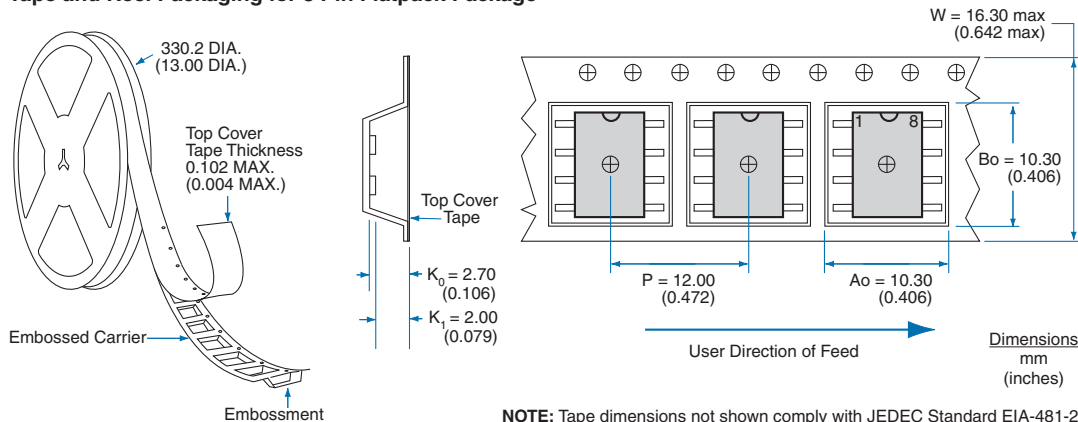
8 Pin Flatpack Package



Recommended PCB Land Pattern



Tape and Reel Packaging for 8 Pin Flatpack Package



NOTE: Tape dimensions not shown comply with JEDEC Standard EIA-481-2

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