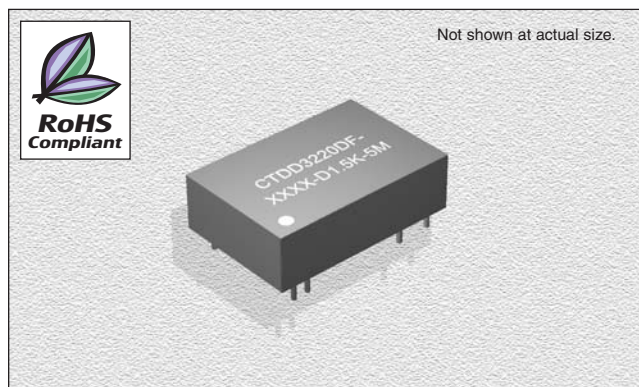


CTDD3220DF-D1.5K-5M

Series

Wide Input Isolated & Regulated
5W Output, Dual Output



FEATURES

- Efficiency:** To 85%
- Temperature Range:** -40°C to +85°C
- Isolation:** 1.5KVDC
- Miscellaneous:** Wide (2:1) Input Range. Dual Output. No heat sink required. Metal Shielding Package. 24PIN Industry standard pinout. Continuous Short-Circuit Protection.
- Samples available.**

CHARACTERISTICS

- Output Short Circuit Protection:** Continuous 1 Hour
- Temperature Rise at Full Load:** 40°C (Typ.)
- Cooling:** Free air convection
- Operating Temperature Range:** -40°C to +85°C
- Storage Temperature Range:** -55°C to +125°C
- Lead Temperature:** 300°C (1.5mm from case for 10 seconds)
- Storage Humidity Range:** ≤ 95%
- Case Material:** Metal
- MTBF:** >1,000,000 hours
- Miscellaneous:** RoHS Compliant.

SPECIFICATIONS

Part Number	Vin Nom. (VDC)	Input Range (VDC)	Vin Max.* (VDC)	Vout (VDC)	Iout Max. (mA)	Iout Min.** (mA)	Effi. Typ. (%)
CTDD3220DF-1205-D1.5K-5M	12	9-18	20	±5	±500	±50	75
CTDD3220DF-1212-D1.5K-5M	12	9-18	20	±12	±210	±21	77
CTDD3220DF-1215-D1.5K-5M	12	9-18	20	±15	±166	±17	79
CTDD3220DF-1224-D1.5K-5M	15	9-18	20	±24	±105	±11	81
CTDD3220DF-2405-D1.5K-5M	24	18-36	40	±5	±500	±50	75
CTDD3220DF-2412-D1.5K-5M	24	18-36	40	±12	±210	±21	78
CTDD3220DF-2415-D1.5K-5M	24	18-36	40	±15	±166	±17	82
CTDD3220DF-2424-D1.5K-5M	24	18-36	40	±24	±105	±11	82
CTDD3220DF-4805-D1.5K-5M	48	36-72	80	±5	±500	±50	78
CTDD3220DF-4812-D1.5K-5M	48	36-72	80	±12	±210	±21	81
CTDD3220DF-4815-D1.5K-5M	48	36-72	80	±15	±166	±17	84
CTDD3220DF-4824-D1.5K-5M	48	36-72	80	±24	±105	±11	85

*If Input voltage above specified may cause permanent damage to the device
**The load shouldn't be less than 10%, otherwise ripple will increase dramatically

ISOLATION SPECIFICATIONS

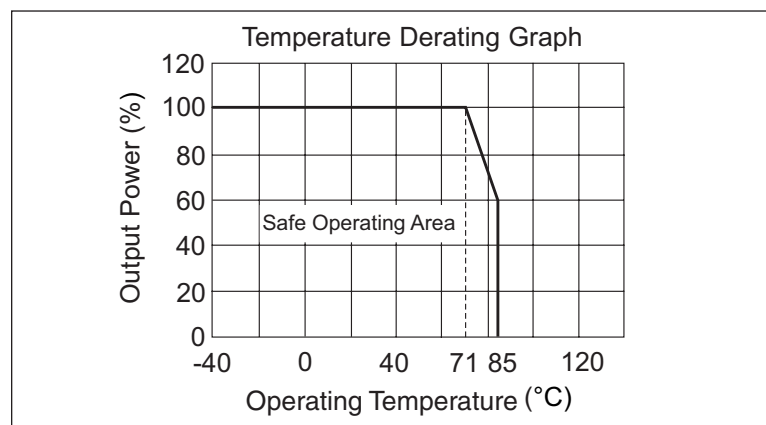
Item	Test Condition	MIN	TYP	MAX	Units
Isolation Voltage	Flash tested for 1 minute	1500			VDC
Isolation Resistance	Test at 500VDC	1000			MΩ

OUTPUT SPECIFICATIONS

Item	Test Condition	MIN	TYP	MAX	Units
5W Output Power				5	W
Positive voltage accuracy	Refer to Recommended Circuit		±1	±3	%
Negative voltage accuracy	Refer to Recommended Circuit		±3	±5	%
Load regulation	From 10% to 100% load		±0.5	±1	%
Line regulation	Input Voltage from low to high		±0.2	±0.5	%
Temperature drift (Vout)	Refer to Temp. Derating Graph		0.02		%/°C
Ripple	20Hz to 300KHz Bandwidth		30	50	mVp-p
Noise	DC-20MHz Bandwidth		100	300	mVp-p
Switching frequency	100% load, nominal input voltage		300		KHz

Note:
1. All specifications measured at TA=25°C, humidity<75%, nominal input voltage and rated output unless otherwise specified.

TYPICAL CHARACTERISTICS

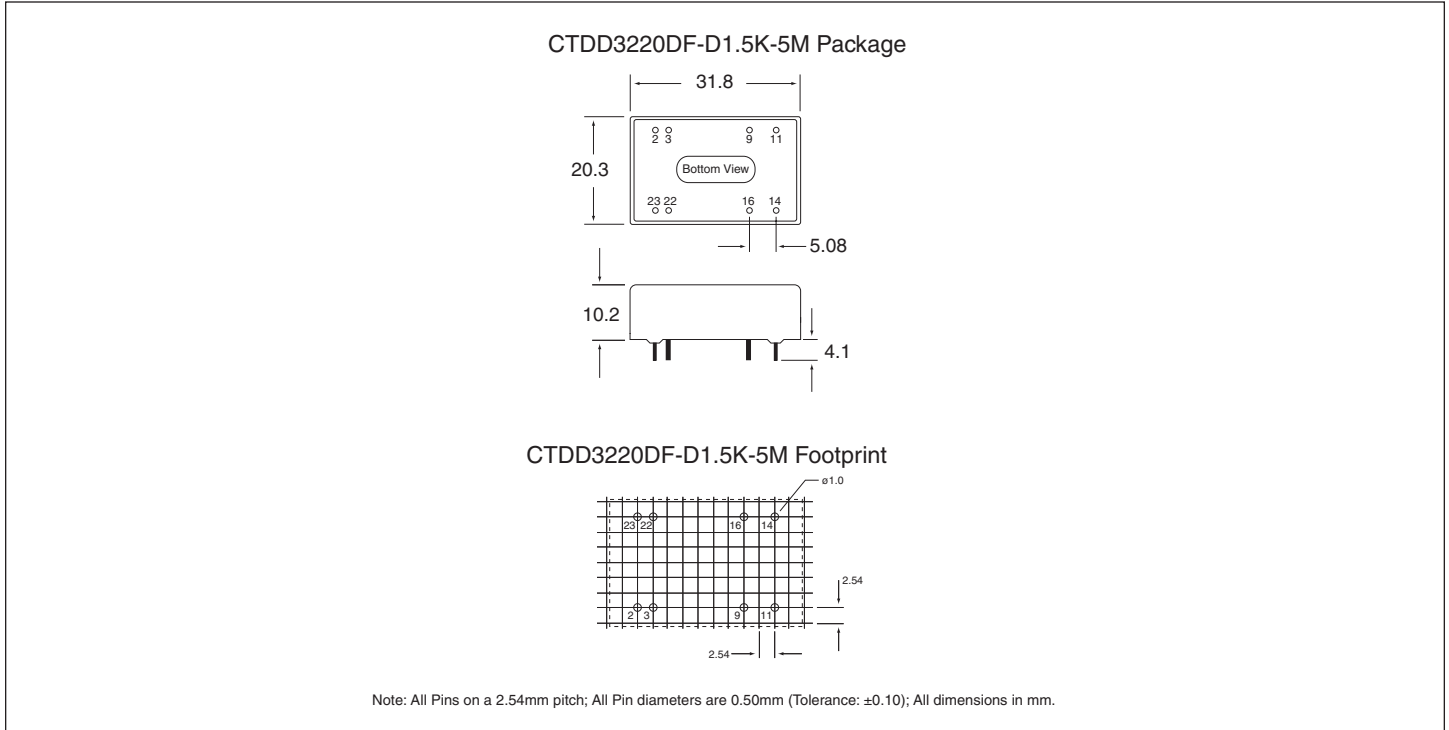


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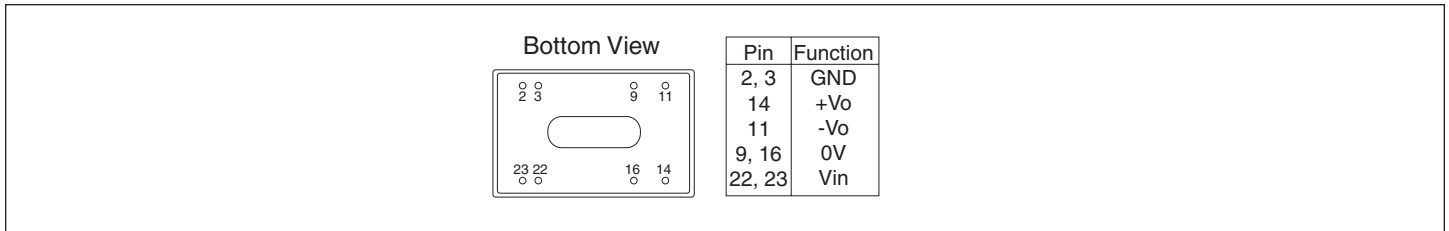
CTDD3220DF-D1.5K-5M Series

Wide Input Isolated & Regulated
5W Output, Dual Output

OUTLINE DIMENSIONS & RECOMMENDED FOOTPRINT DETAILS



PIN CONNECTIONS



RECOMMENDED CIRCUIT

All the CTDD3220DF-D1.5K-5M Series have been tested according to the following recommended testing circuit before leaving factory. This series should be tested under load. Never be tested under no load (See figure 1 & 2). If you want to further decrease the input/output ripple, you can increase capacitance properly or choose capacitors with low ESR. However, the capacitance should not be too high. (See table 1)

External Capacitor Table (table 1)

Vin	Cin	Vout	Cout
12V	100µF	5V	100µF each 1A Current
24V	100µF	12V	
48V	100µF	15V	
		24V	

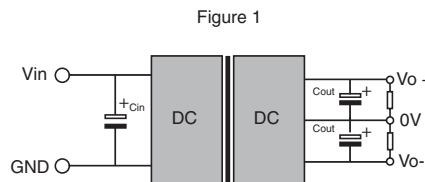
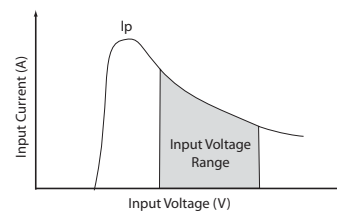


Figure 2



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