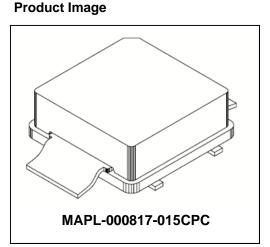


### LDMOS RF Line Power FET Transistor 15 W , 800-1700 MHz, 26V

M/A-COM Products Released - Rev. 07.08

Designed for broadband commercial applications up to 1.7GHz

- High gain, high efficiency and high linearity
- Aluminum-Copper Metallization for high reliability
- RoHS Compliant
- Typical P1dB performance at 960MHz, 26Vdc, CW Typical power output: 16.5W Gain: 17.0dB Efficiency: 50% 10:1 VSWR ruggedness at 15W, 26Vdc, 960MHz



#### MAXIMUM RATINGS

Parameter	Symbol	Rating	Units	
Drain—Source Voltage	V <sub>DSS</sub>	65	V <sub>dc</sub>	
Gate—Source Voltage	V <sub>GS</sub>	+20, -20	V <sub>dc</sub>	
Total Power Dissipation @ $T_c = 25 \ ^{\circ}C$	PD	31	W	
Storage Temperature	Т <sub>stg</sub>	-65 to +150	°C	
Junction Temperature	TJ	200	°C	

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R <sub>øjc</sub>	4	°C/W

NOTE—CAUTION—MOS devices are susceptible to damage from electrostatic charge. Precautions in handling and packaging MOS devices should be observed.

- 1
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Characteristic	Symbol	Min	Тур	Max	Unit
DC CHARACTERISTICS @ 25°C				•	
Drain-Source Breakdown Voltage (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 20 µAdc)	V <sub>(BR)DSS</sub>	65	_	_	Vdc
Gate Quiescent Voltage (V <sub>ds</sub> = 26 Vdc, I <sub>d</sub> = 100 mA)	V <sub>DS(Q)</sub>	3	_	5	Vdc
Drain-Source On-Voltage ( $V_{gs} = 10 \text{ Vdc}, I_d = 1 \text{ A}$ )	V <sub>DS(on)</sub>		0.25	-	Vdc
RF FUNCTIONAL TESTS @ 25ºC (In M/A-COM Test Fixture) (1)			I	I	1
Common Source Amplifier Gain $(V_{DD} = 26 \text{ Vdc}, I_{DQ} = 150 \text{ mA}, f = 960 \text{ MHz}, P_{OUT} = 15 \text{ W})$	G <sub>P</sub>	_	17	_	dB
Drain Efficiency $(V_{DD} = 26 \text{ Vdc}, I_{DQ} = 150 \text{ mA}, f = 960 \text{ MHz}, P_{OUT} = 15 \text{ W})$	EFF (ŋ)		50	-	%
Input Return Loss $(V_{DD} = 26 \text{ Vdc}, I_{DQ} = 150 \text{ mA}, f = 960 \text{ MHz}, P_{OUT} = 15 \text{ W})$	IRL		-10	-	dB
Output VSWR Tolerance (V <sub>DD</sub> = 26 Vdc, I <sub>DQ</sub> = 150 mA, f = 960 MHz, P <sub>OUT</sub> = 15 W, VSWR = 10:1, All Phase Angles at Frequency of Tests)	Ψ	No Degradation In Output Power Before and After Test			
Common Source Amplifier Gain ( $V_{DD}$ = 26 Vdc, $I_{DQ}$ = 150 mA, f = 1670 MHz, $P_{OUT}$ = 15 W)	G <sub>P</sub>	13.0	15	_	dB
Drain Efficiency $(V_{DD} = 26 \text{ Vdc}, I_{DQ} = 150 \text{ mA}, f = 1670 \text{ MHz}, P_{OUT} = 15 \text{ W})$	EFF (ŋ)	45	50	-	%
Input Return Loss $(V_{DD} = 26 \text{ Vdc}, I_{DQ} = 150 \text{ mA}, f = 1670 \text{ MHz}, P_{OUT} = 15 \text{ W})$	IRL	_	-10	-8	dB

(1) Device specifications obtained on a Production Test Fixture.

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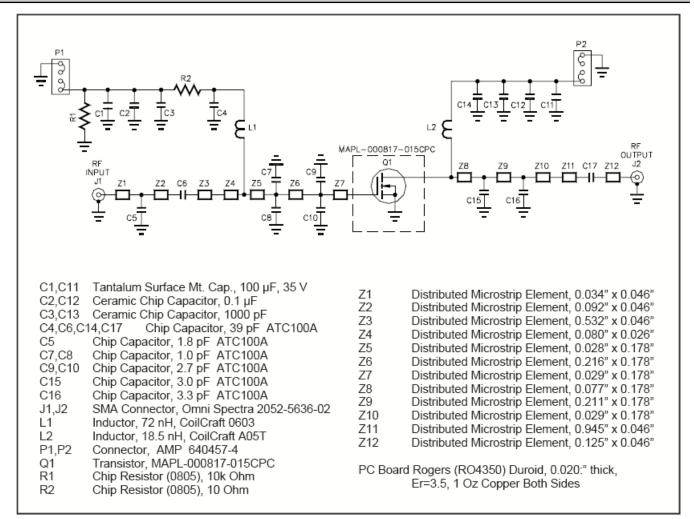


Figure 1. 1620-1670 MHz Test Fixture Schematic

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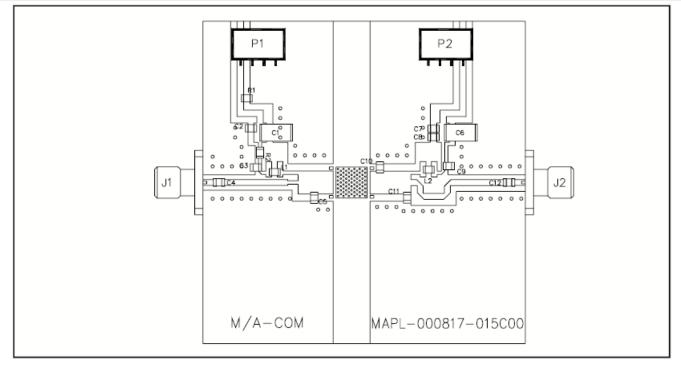


Figure 2. 1620—1670 MHz Test Fixture Component Layout

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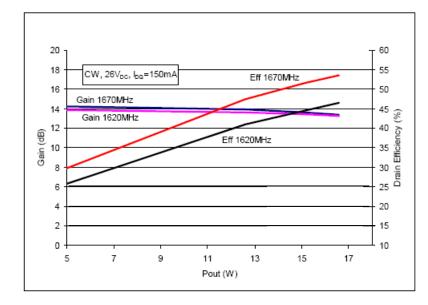
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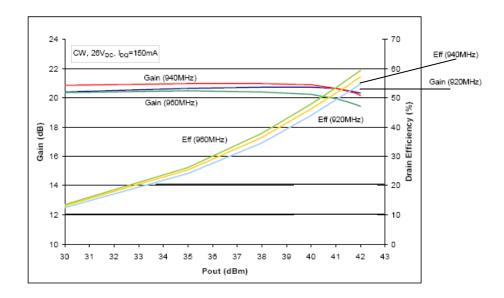


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Graph 1. 1620, 1670MHz: CW Power Gain and Drain Efficiency vs. Output Power



Graph 2. 920, 940, 960MHz: CW Power Gain and Drain Efficiency vs. Output Power

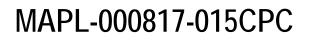
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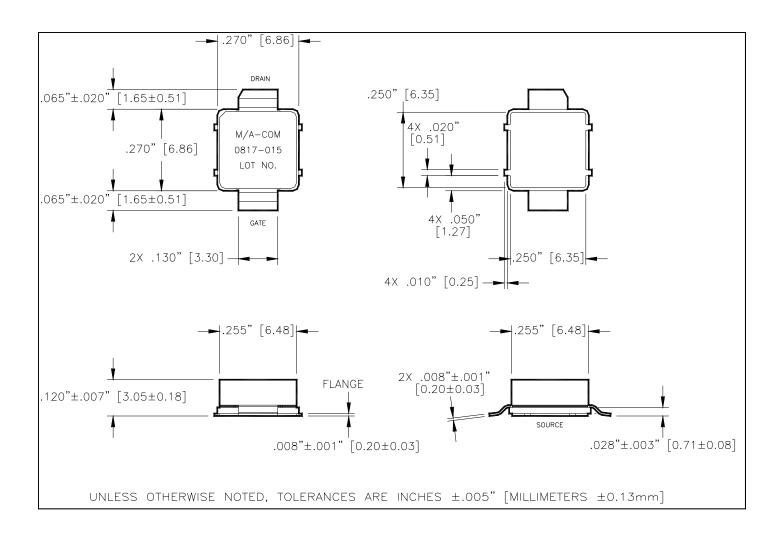




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#### PACKAGE DIMENSIONS



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