### 0.01 to 3.0 GHz SPDT SWITCH

## DESCRIPTION

The $\mu$ PG2406TK is a GaAs MMIC for L, S-band SPDT (Single Pole Double Throw) switch which were designed for mobile phone and another L, S-band application.

This device can operate 2 control switching by control voltage 1.8 to 5.3 V . This device can operate frequency from 0.01 to 3.0 GHz , having the low insertion loss and high isolation.

This device is housed in a 6-pin lead-less minimold package. And this package is able to high-density surface mounting.

## FEATURES

- Switch control voltage

$$
\begin{aligned}
& : V_{\text {cont }(H)}=1.8 \text { to } 5.3 \mathrm{~V}(2.7 \mathrm{~V} \text { TYP. }) \\
& : V_{\text {cont }(L)}=-0.2 \text { to }+0.2 \mathrm{~V}(0 \mathrm{VYP} .)
\end{aligned}
$$

- Low insertion loss
: Lins $=0.40 \mathrm{~dB}$ TYP. @ $\mathrm{f}=1.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=2.7 \mathrm{~V}, \mathrm{~V}_{\text {cont }(L)}=0 \mathrm{~V}$ $:$ Lins $=0.47 \mathrm{~dB}$ TYP. $@ \mathrm{f}=2.5 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=2.7 \mathrm{~V}, \mathrm{~V}_{\text {cont }(L)}=0 \mathrm{~V}$
- High isolation
$: I S L=27 \mathrm{~dB}$ TYP. @ $\mathrm{f}=1.0 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=2.7 \mathrm{~V}, \mathrm{~V}_{\text {cont }(L)}=0 \mathrm{~V}$
$: I S L=17 \mathrm{~dB}$ TYP. @ $\mathrm{f}=2.5 \mathrm{GHz}, \mathrm{V}_{\text {cont }(H)}=2.7 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}$
- Handling power : Pin $(0.1 \mathrm{~dB})=+29.0 \mathrm{dBm}$ TYP. @ $\mathrm{f}=2.0 / 2.5 \mathrm{GHz}, \mathrm{V}$ cont $(H)=2.7 \mathrm{~V}, \mathrm{~V}$ cont $(\mathrm{L})=0 \mathrm{~V}$

$$
\left.: \operatorname{Pin}(1 \mathrm{~dB})=+30.5 \mathrm{dBm} \text { TYP. @ } \mathrm{f}=0.5 \text { to } 3.0 \mathrm{GHz}, \mathrm{~V}_{\text {cont }(H)}=2.7 \mathrm{~V}, \mathrm{~V}_{\text {cont }(\mathrm{L}}\right)=0 \mathrm{~V}
$$

- High-density surface mounting : 6-pin lead-less minimold package ( $1.5 \times 1.1 \times 0.55 \mathrm{~mm}$ )


## APPLICATIONS

- L, S-band digital cellular or cordless telephone
- W-LAN, WLL and Bluetooth ${ }^{\text {TM }}$ etc.


## ORDERING INFORMATION

| Part Number | Order Number | Package | Marking | Supplying Form |
| :---: | :---: | :---: | :---: | :---: |
| $\mu$ PG2406TK-E2 | $\mu$ PG2406TK-E2-A | 6-pin lead-less minimold <br> (1511 PKG) (Pb-Free) | G5K | • Embossed tape 8 mm wide <br> $\bullet$ Pin 1, 6 face the perforation side of the tape <br> $\bullet$ Qty $5 \mathrm{kpcs} /$ reel |

Remark To order evaluation samples, contact your nearby sales office.
Part number for sample order: $\mu$ PG2406TK

Caution Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

[^0]PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM


## SW TRUTH TABLE

| ON Path | V $_{\text {cont1 }}$ | V cont2 |
| :--- | :--- | :--- |
| RFC-RF1 | High | Low |
| RFC-RF2 | Low | High |

ABSOLUTE MAXIMUM RATINGS ( $\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}$, unless otherwise specified)

| Parameter |  | Symbol | Ratings | Unit |
| :---: | :---: | :---: | :---: | :---: |
| Switch Control Voltage |  | $\mathrm{V}_{\text {cont }}$ | $+6.0^{\text {Note }}$ | V |
| Input Power | $\mathrm{f}=0.01$ to 0.5 GHz | Pin 1 | +24.0 | dBm |
|  | $\mathrm{f}=0.5$ to 3.0 GHz | Pin2 | +31.0 |  |
| Operating Ambient Temperature |  | $\mathrm{T}_{\mathrm{A}}$ | -45 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature |  | $\mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

Note $\left|V_{\text {cont1 }}-V_{\text {cont2 }}\right| \leq 6.0 \mathrm{~V}$

RECOMMENDED OPERATING RANGE ( $\mathrm{T}_{\mathrm{A}}=\boldsymbol{+ 2 5}^{\circ} \mathrm{C}$, unless otherwise specified)

| Parameter | Symbol | MIN. | TYP. | MAX. | Unit |
| :--- | :---: | :---: | :---: | :---: | :---: |
| Switch Control Voltage (H) | $\mathrm{V}_{\text {cont }(H)}$ | 1.8 | 2.7 | 5.3 | V |
| Switch Control Voltage (L) | $\mathrm{V}_{\text {cont }(\mathrm{L})}$ | -0.2 | 0 | 0.2 | V |

ELECTRICAL CHARACTERISTICS
$\left(\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\text {cont }}(\mathrm{H})=2.7 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}\right.$, DC blocking capacitors $=56 \mathrm{pF}$, unless otherwise specified)

| Parameter | Symbol | Test Conditions | MIN. | TYP. | MAX. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss 1 | Lins 1 | $\mathrm{f}=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 0.40 | - | dB |
| Insertion Loss 2 | Lins2 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}{ }^{\text {Note } 2}$ | - | 0.40 | 0.45 | dB |
| Insertion Loss 3 | Lins3 | $\mathrm{f}=0.5$ to 1.0 GHz | - | 0.40 | 0.45 | dB |
| Insertion Loss 4 | Lins4 | $\mathrm{f}=1.0$ to 2.0 GHz | - | 0.45 | 0.50 | dB |
| Insertion Loss 5 | Lins5 | $\mathrm{f}=2.0$ to 2.5 GHz | - | 0.47 | 0.55 | dB |
| Insertion Loss 6 | Lins6 | $\mathrm{f}=2.5$ to 3.0 GHz | - | 0.53 | 0.60 | dB |
| Isolation 1 | ISL1 | $\mathrm{f}=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 27 | - | dB |
| Isolation 2 | ISL2 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}^{\text {Note } 2}$ | 23 | 27 | - | dB |
| Isolation 3 | ISL3 | $\mathrm{f}=0.5$ to 1.0 GHz | 23 | 27 | - | dB |
| Isolation 4 | ISL4 | $\mathrm{f}=1.0$ to 2.0 GHz | 16 | 19 | - | dB |
| Isolation 5 | ISL5 | $\mathrm{f}=2.0$ to 2.5 GHz | 14 | 17 | - | dB |
| Isolation 6 | ISL6 | $\mathrm{f}=2.5$ to 3.0 GHz | 14 | 17 | - | dB |
| Input Return Loss 1 | RLin1 | $f=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 20 | - | dB |
| Input Return Loss 2 | RLin2 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}^{\text {Note } 2}$ | 15 | 20 | - | dB |
| Input Return Loss 3 | RLin3 | $\mathrm{f}=0.5$ to 3.0 GHz | 15 | 20 | - | dB |
| Output Return Loss 1 | RLout1 | $\mathrm{f}=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 20 | - | dB |
| Output Return Loss 2 | RLout2 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}^{\text {Note } 2}$ | 15 | 20 | - | dB |
| Output Return Loss 3 | RLout3 | $\mathrm{f}=0.5$ to 3.0 GHz | 15 | 20 | - | dB |
| 0.1 dB Loss Compression | Pin ( 0.1 dB ) | $f=2.0 / 2.5 \mathrm{GHz}$ | +26.0 | +29.0 | - | dBm |
| Input Power Note 3 |  | $\mathrm{f}=0.5$ to 3.0 GHz | - | +29.0 | - | dBm |
| 1 dB Loss Compression Input Power ${ }^{\text {Note } 4}$ | Pin (1 dB) | $\mathrm{f}=0.5$ to 3.0 GHz | - | +30.5 | - | dBm |
| 2nd Harmonics | $2 f_{0}$ | $\mathrm{f}=2.0 / 2.5 \mathrm{GHz}, \mathrm{P}_{\text {in }}=+20 \mathrm{dBm}$ | 65 | 75 | - | dBc |
| 3rd Harmonics | $3 f_{0}$ | $\mathrm{f}=2.0 / 2.5 \mathrm{GHz}, \mathrm{P}_{\mathrm{in}}=+20 \mathrm{dBm}$ | 65 | 75 | - | dBc |
| Intermodulation Intercept Point | $11 \mathrm{P}_{3}$ | $\mathrm{f}=0.5$ to 3.0 GHz , 2 tone, <br> 5 MHz spicing | - | +60 | - | dBm |
| Switch Control Current | I cont | No RF input | - | 0.2 | 20 | $\mu \mathrm{A}$ |
| Switch Control Speed | tsw | 50\% CTL to 90/10\% RF | - | 50 | 500 | ns |

Notes 1. DC blocking capacitors $=10000 \mathrm{pF}$ at $\mathrm{f}=0.01$ to 0.05 GHz
2. DC blocking capacitors $=1000 \mathrm{pF}$ at $\mathrm{f}=0.05$ to 0.5 GHz
3. Pin ( 0.1 dB ) is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.
4. $\operatorname{Pin}(1 \mathrm{~dB})$ is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

Caution This device is used it is necessary to use DC blocking capacitors.
The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC blocking capacitor value is less than 56 pF .

ELECTRICAL CHARACTERISTICS
$\left(\mathrm{T}_{\mathrm{A}}=+25^{\circ} \mathrm{C}, \mathrm{V}_{\text {cont }}(\mathrm{H})=1.8 \mathrm{~V}, \mathrm{~V}_{\text {cont }}(\mathrm{L})=0 \mathrm{~V}\right.$, DC blocking capacitors $=56 \mathrm{pF}$, unless otherwise specified)

| Parameter | Symbol | Test Conditions | MIN. | TYP. | MAX. | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Insertion Loss 7 | Lins7 | $\mathrm{f}=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 0.40 | - | dB |
| Insertion Loss 8 | Lins8 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}^{\text {Note } 2}$ | - | 0.40 | 0.46 | dB |
| Insertion Loss 9 | Lins9 | $\mathrm{f}=0.5$ to 1.0 GHz | - | 0.40 | 0.47 | dB |
| Insertion Loss 10 | Lins 10 | $\mathrm{f}=1.0$ to 2.0 GHz | - | 0.46 | 0.52 | dB |
| Insertion Loss 11 | Lins 11 | $\mathrm{f}=2.0$ to 2.5 GHz | - | 0.48 | 0.57 | dB |
| Insertion Loss 12 | Lins 12 | $\mathrm{f}=2.5$ to 3.0 GHz | - | 0.54 | 0.62 | dB |
| Isolation 7 | ISL7 | $\mathrm{f}=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 27 | - | dB |
| Isolation 8 | ISL8 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}^{\text {Note } 2}$ | 23 | 27 | - | dB |
| Isolation 9 | ISL9 | $\mathrm{f}=0.5$ to 1.0 GHz | 23 | 27 | - | dB |
| Isolation 10 | ISL10 | $\mathrm{f}=1.0$ to 2.0 GHz | 16 | 19 | - | dB |
| Isolation 11 | ISL11 | $\mathrm{f}=2.0$ to 2.5 GHz | 14 | 17 | - | dB |
| Isolation 12 | ISL12 | $\mathrm{f}=2.5$ to 3.0 GHz | 14 | 17 | - | dB |
| Input Return Loss 4 | RLin4 | $\mathrm{f}=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 20 | - | dB |
| Input Return Loss 5 | RLin5 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}^{\text {Note } 2}$ | 15 | 20 | - | dB |
| Input Return Loss 6 | RLin6 | $\mathrm{f}=0.5$ to 3.0 GHz | 15 | 20 | - | dB |
| Output Return Loss 4 | RLout4 | $\mathrm{f}=0.01$ to $0.05 \mathrm{GHz}^{\text {Note } 1}$ | - | 20 | - | dB |
| Output Return Loss 5 | RLout5 | $\mathrm{f}=0.05$ to $0.5 \mathrm{GHz}^{\text {Note } 2}$ | 15 | 20 | - | dB |
| Output Return Loss 6 | RLout6 | $f=0.5$ to 3.0 GHz | 15 | 20 | - | dB |
| 0.1 dB Loss Compression | Pin (0.1 dB) | $\mathrm{f}=2.0 / 2.5 \mathrm{GHz}$ | +19.0 | +22.0 | - | dBm |
| Input Power ${ }^{\text {Note } 3}$ |  | $\mathrm{f}=0.5$ to 3.0 GHz | - | +22.0 | - | dBm |
| 1 dB Loss Compression Input Power ${ }^{\text {Note } 4}$ | Pin (1dB) | $\mathrm{f}=0.5$ to 3.0 GHz | - | +25.0 | - | dBm |
| Switch Control Current | Icont | No RF input | - | 0.2 | 20 | $\mu \mathrm{A}$ |
| Switch Control Speed | tsw | 50\% CTL to 90/10\% RF | - | 50 | 500 | ns |

Notes 1. DC blocking capacitors $=10000 \mathrm{pF}$ at $\mathrm{f}=0.01$ to 0.05 GHz
2. DC blocking capacitors $=1000 \mathrm{pF}$ at $\mathrm{f}=0.05$ to 0.5 GHz
3. Pin ( 0.1 dB ) is measured the input power level when the insertion loss increases more 0.1 dB than that of linear range.
4. $\operatorname{Pin}(1 \mathrm{~dB})$ is measured the input power level when the insertion loss increases more 1 dB than that of linear range.

## Caution This device is used it is necessary to use DC blocking capacitors.

The value of DC blocking capacitors should be chosen to accommodate the frequency of operation, bandwidth, switching speed and the condition with actual board of your system. The range of recommended DC blocking capacitor value is less than 56 pF .

## EVALUATION CIRCUIT



Note C1: 0.01 to 0.05 GHz 10000 pF
: 0.05 to $0.5 \mathrm{GHz} \quad 1000 \mathrm{pF}$
: 0.5 to $3.0 \mathrm{GHz} \quad 56 \mathrm{pF}$
C2 : 1000 pF

The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

## APPLICATION INFORMATION



- Lesd provides a means to increase the ESD protection on a specific RF port, typically the port attached to the antenna.
- The value may be tailored to provide specific electrical responses.
- The RF ground connections should be kept as short as possible and connected to directly to a good RF ground for best performance.


## ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



USING THE NEC EVALUATION BOARD

| Symbol | Test Conditions | Values |
| :--- | :---: | :---: |
| C 1 | $\mathrm{f}=0.01$ to 0.05 GHz | 10000 pF |
|  | $\mathrm{f}=0.05$ to 0.5 GHz | 1000 pF |
|  | $\mathrm{f}=0.5$ to 3.0 GHz | 56 pF |
| C2 |  | 1000 pF |

TYPICAL CHARACTERISTICS ( $\mathrm{T}_{\mathrm{A}}=+\mathbf{2 5}^{\circ} \mathrm{C}$, DC blocking capacitors $=56 \mathrm{pF}$, unless otherwise specified)

RFC-RF1/RF2
INSERTION LOSS vs. FREQUENCY


RFC-RF1/RF2
INPUT RETURN LOSS vs. FREQUENCY


RFC-RF1/RF2 INSERTION LOSS,
Icont vs. SWITCH CONTROL VOLTAGE (H)


RFC-RF1/RF2
ISOLATION vs. FREQUENCY


RFC-RF1/RF2
OUTPUT RETURN LOSS vs. FREQUENCY


RFC-RF1/RF2 ISOLATION vs. SWITCH CONTROL VOLTAGE (H)


Remark The graphs indicate nominal characteristics.

RFC-RF1/RF2 INPUT RETURN LOSS vs. SWITCH CONTROL VOLTAGE (H)


Switch Control Voltage (H) $\mathrm{V}_{\text {cont (H) }}(\mathrm{V})$
RFC-RF1/RF2 INSERTION LOSS, Icont vs. INPUT POWER


RFC-RF1/RF2 INPUT POWER vs. SWITCH CONTROL VOLTAGE (H)


RFC-RF1/RF2 OUTPUT RETURN LOSS vs SWITCH CONTROL VOLTAGE (H)


RFC-RF1/RF2 INSERTION LOSS, Icont vs. INPUT POWER


RFC-RF1/RF2
INPUT POWER vs. FREQUENCY


Remark The graphs indicate nominal characteristics.

## MOUNTING PAD LAYOUT DIMENSIONS

## 6-PIN LEAD-LESS MINIMOLD (1511 PKG) (UNIT: mm)



Remark The mounting pad layout in this document is for reference only.

## PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (1511 PKG) (UNIT: mm)
(Top View) (Bottom View)


Remark Dimension ${ }^{4}$ is bigger than dimension ${ }^{+2}$ (dimension ${ }^{* 2}=\mathbf{a}+\mathbf{b}+\mathbf{c}$ ).

## RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

| Soldering Method |  | Coldering Conditions |  |
| :--- | :--- | :--- | :--- |
| Infrared Reflow | Peak temperature (package surface temperature) | $: 260^{\circ} \mathrm{C}$ or below | Condition Symbol |
|  | Time at peak temperature | $: 10$ seconds or less |  |
|  | Time at temperature of $220^{\circ} \mathrm{C}$ or higher | $: 60$ seconds or less |  |
|  | Preheating time at 120 to $180^{\circ} \mathrm{C}$ | $: 120 \pm 30$ seconds |  |
|  | Maximum number of reflow processes | $: 3$ times | $: 0.2 \%($ Wt.) or below |

Caution Do not use different soldering methods together (except for partial heating).

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| :---: | :--- | :--- |


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