

April 2009

FJD3305H1 NPN Silicon Transistor

High Voltage Switch Mode Application

- Fast Speed Switching
- Wide Safe Operating Area
- Suitable for Electronic Ballast Application



1. Base 2. Collector 3. Emitter

Absolute Maximum Ratings * T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	700	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	9	V
I _C	Collector Current (DC)	4	А
I _{CP}	Collector Current (Pulse)	8	А
I_B	Base Current	2	Α
P _C	Collector Dissipation, $T_a = 25$ °C $T_c = 25$ °C	1.1 50	W W
T _J	Junction Temperature	150	°C
T_{STG}	Storage Temperature	-65 ~ 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics Ta=25°C unless otherwise noted

Symbol	Parameter	Value	Units
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	110	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	2.0	°C/W

^{*} Device mounted on minimum pad size

Ordering Information

Part Number Marking		Package	Packing Method	Remarks
FJD3305H1TM	J3305H1	D-PAK	Tape & Reel	

Electrical Characteristics * $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
BV _{CBO}	Collector-Base Breakdwon Voltage	$I_C = 500 \mu A, I_E = 0$	700			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 5mA, I_B = 0$	400			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 500 \mu A, I_C = 0$	9			V
I _{CBO}	Collector Cut-off Current	V _{CB} = 700V, I _E = 0			1	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 9V, I_{C} = 0$			1	μА
h _{FE1} h _{FE2}	DC Current Gain *	V _{CE} = 5V, I _C = 1A V _{CE} = 5V, I _C = 2A	19 8		28 40	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 1A, I_B = 0.2A$ $I_C = 2A, I_B = 0.5A$ $I_C = 4A, I_B = 1A$			0.5 0.6 1.0	V V V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 1A, I_B = 0.2A$ $I_C = 2A, I_B = 0.5A$			1.2 1.6	V V
f _T	Current Gain Bandwidth Product	$V_{CE} = 10V, I_{C} = 0.5A$	4			MHz
C _{ob}	Output Capacitance	V _{CB} = 10V, f = 1MHz		65		pF
t _{ON}	Turn On Time	V _{CC} = 125V, I _C = 2A			0.8	μS
t _{STG}	Storage Time	$I_{B1} = -I_{B2} = 0.4A$ $R_1 = 62.5\Omega$			4.0	μS
t _F	Fall Time	- IVL - 02.032			0.9	μS

^{*} Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

Figure 1. Static Characteristic

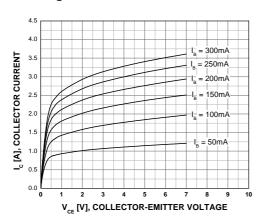


Figure 2. DC Current Gain

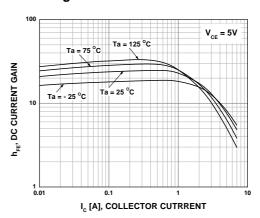


Figure 3. Collector- Emitter Saturation Voltage

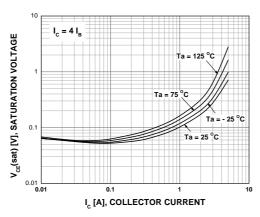


Figure 4. Base - EmitterSaturation Voltage

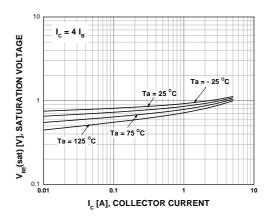


Figure 5. Switching Time

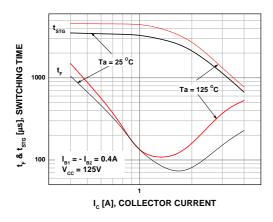


Figure 6. Capacitance

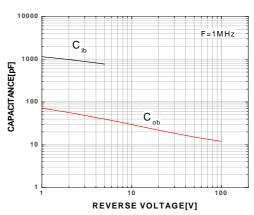


Figure 7. Reverse Biased Safe Operating Area



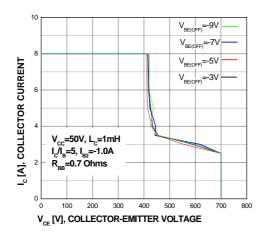


Figure 9. RBSOA Turn-on Pulse Width vs Collector Current

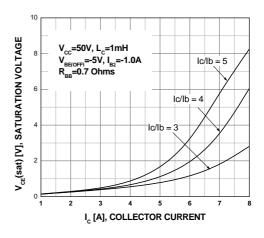
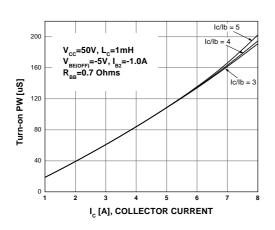


Figure 10. Power Derating



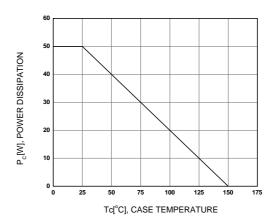
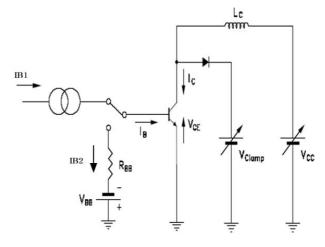
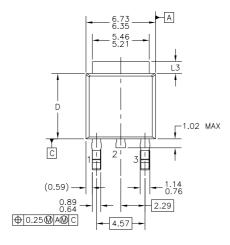


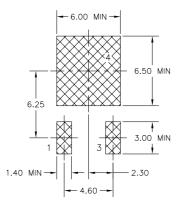
Figure 11. RBSOA Test Circuit



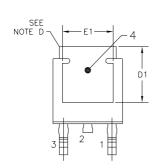
Mechanical Dimensions

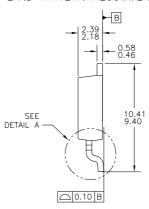
D-PAK

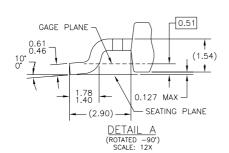




LAND PATTERN RECOMMENDATION







- NOTES: UNLESS OTHERWISE SPECIFIED

 A) ALL DIMENSIONS ARE IN MILLIMETERS.

 B) THIS PACKAGE CONFORMS TO JEDEC, TO-252, ISSUE C, VARIATION AA & AB, DATED NOV. 1999.

 C) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.

 D) HEAT SINK TOP EOGE COULD BE IN CHAMFERED CORNERS OR EDGE PROTRUSION.

 E) DIMENSIONS L3,D,E1&D1 TABLE:

 [OPTION AA | OPTION AB]

	OPTION AA	OPTION AB	
L3	0.89-1.27	1.52-2.03	
D	5.97-6.22	5.33-5.59	
E1	4.32 MIN	3.81 MIN	
D1	5.21 MIN	4.57 MIN	

PRESENCE OF TRIMMED CENTER LEAD IS OPTIONAL.

Dimensions in Millimeters





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Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
		Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete Not In Production		Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 140