

CHC Series

- Superior TaNFilm® resistors on ceramic substrate
- High density networks on a reduced footprint
- Excellent high frequency performance
- Standard tolerances to ±1%
- RoHS compliant terminations available

High purity alumina substrate Ball grid terminations

IRC's Chipscale on ceramic CHC offers high performance terminal solutions in a small surface mount package. Sn/Pb solder balls placed on a ceramic substrate permit very low parasitic inductance and

capacitance. This improves speeds, lowers propagation delays, and reduces ground bounce. IRC's proven tantalum nitride thin film technology can handle the most demanding applications.

For all of your high density, small footprint termination needs, use IRC's CHC Termination arrays.

Electrical Data

Package	Resistance Range (Ω)	Absolute Tolerances	Absolute TCR	Package Power Rating 70°C*	Element Power Rating 70°C*	Operating Temperature
CB0565A	10R to 4.7K	±1%, ±2%		0.6W	0.1W	-40°C to +85°C
	10R to 10.0K	±5%	. 100 /80			
OBASSED	10R to 2.2K	±1%, ±2%	±100ppm/°C			
CB0565B	10R to 4.7K	±5%				
CD0865A	10R to 4.7K	±1%, ±2%	±100ppm/°C	1.2W		
CD0865A	10R to 10.0K	±5%				
CD0865B	10R to 2.2K	±1%, ±2%				
CD0805B	10R to 4.7K	±5%				
CD1065A	10R to 4.7K	±1%, ±2%	±100ppm/°C	1.6W		
CD1065A	10R to 10.0K	±5%				
CD1065B	10R to 2.2K	±1%, ±2%	±100ppm/ C			
CD1065B	10R to 4.7K	±5%				
CC0910A	10R to 4.7K	±1%, ±2%		1.2W		
CCOSTUA	10R to 10.0K	±5%	100 /00			
0000108	10R to 2.2K	±1%, ±2%	±100ppm/°C			
CC0910B	10R to 4.7K	±5%				
0000104	10R to 4.7K	±1%, ±2%	100	1.2W		
CD0910A	10R to 10.0K	±5%				
CD0910B	10R to 2.2K	±1%, ±2%	±100ppm/°C			
	10R to 4.7K	±5%]			

*Rated power is from 0°C to 70°C derated linearly to 0W at 85°C.

General Note IRC reserves the right to make changes in product specification without notice or liability.

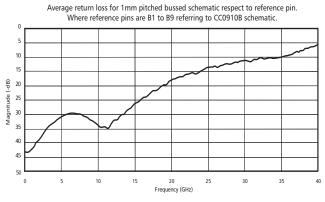
All information is subject to IRC's own data and is considered accurate at time of going to print.

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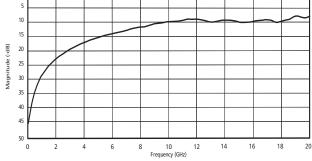


Return Loss Data (50Ω Nominal)



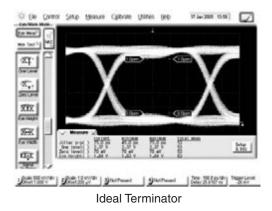
Typical Return Loss For CC0910B-01-50R0-F

Average return loss for 0.65mm pitched bussed schematic for elements away from reference pins. For example: CB0565B, reference pins are A3 or B3



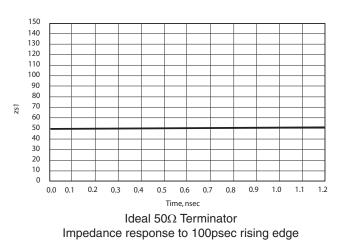
Typical Return Loss For CD1065B-01-50R0-F

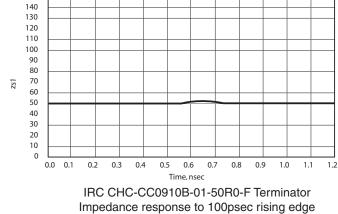
Eye Diagram Data



The Great Sette Heave Cathons Dates Hit 17 Jan-2008. 12-54 The Mass? Min Text 1 ar! 640 x_{i} Sec. 1 200 414 590 222 ITTE | Date ALC: NO 1 5/40 (Sector () () () IRC CHC-CC0910B-01-50R0-F Terminator

Impedance Response Data





150

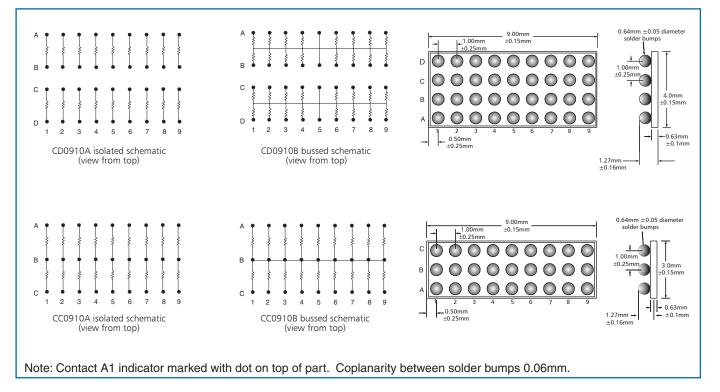
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Environmental Data

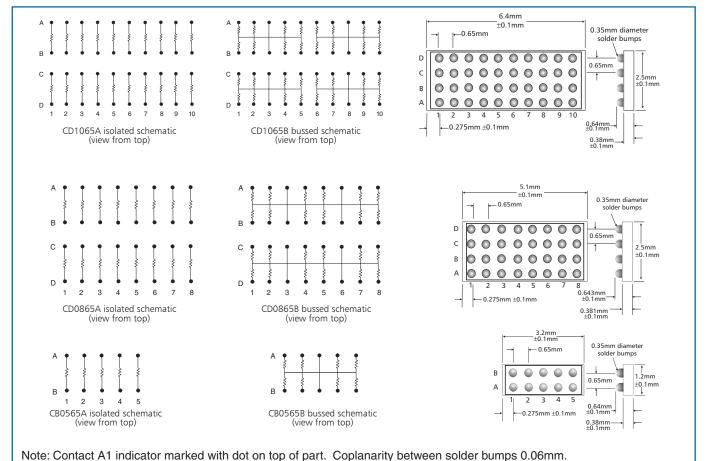
Environmental Test	Specification	Typical	Maximum	
Thermal shock	MIL-PRF-83401	±0.01%	±0.02%	
Low temperature operation	MIL-PRF-83401	±0.01%	±0.05%	
Short time overload	MIL-PRF-83401	±0.01%	±0.05%	
High temperature exposure	MIL-PRF-83401	±0.03%	±0.05%	
Effects of solder	MIL-PRF-83401	±0.01%	±0.05%	
Moisture resistance	MIL-STD-202, Method 206 65°C, 45% RH, with bias	±0.02%	±0.01%	
Life	MIL-PRF-83401	±0.01%	±0.02%	

Physical Data and Schematic Diagrams for 1.0mm Pitch Series





Physical Data and Schematic Diagrams for 0.65mm Pitch Series



The contact AT indicator marked with dot on top of part. Copiananty between solder bump

Ordering Data

Prefix CHC - CD0865	A -	01 - 51R	1 - J
Model · · · · · · · · · · · · · · · · · · ·	÷	÷ ÷	
CB0565 = 2 × 5, 0.65mm pitch array	•	÷ ÷	•
CD0865 = 4×8 , 0.65mm pitch array	:		:
CD1065 = 4 × 10, 0.65mm pitch array	:	: :	:
CC0910 = 3 × 9, 1.0mm pitch array	:	: :	:
CD0910 = 4 × 9, 1.0mm pitch array	•		
	:	4 C	:
Schematic	:	: :	:
A = Isolated schematic with 60/40 Sn/Pb (0.65mm pitch) or 90/10 Sn/Pb (1.0mm pitch) terminations		1 I I	:
ALF = Isolated schematic with RoHS compliant terminations		: :	•
B = Bussed schematic with 60/40 Sn/Pb (0.65mm pitch) or 90/10 Sn/Pb (1.0mm pitch) terminations			:
BLF = Bussed schematic with RoHS compliant terminations		4 - E	:
		÷ :	:
Absolute TCR Code	• • • •	• •	
01 = ±100ppm/°C		:	
Four Digit Resistance Code			:
	••••	•••••	:
Standard resistance values $10R0 = 10\Omega$; $15R0 = 15\Omega$; $22R0 = 22\Omega$; $33R0 = 33\Omega$; $47R0 = 47\Omega$; $50R0 = 50\Omega$; $51R1 = 51.1\Omega$;			:
$75R_0 = 75\Omega_1 + 10\Omega_2$ (2010) $= 1.00\Omega_2$ (2010) $= 1.00\Omega_2$ (2010) $= 2.20\Omega_2$ (2010) $= 2.20\Omega_2$ (2010) $= 1.01\Omega_2$			
			:
Absolute Tolerance Code . $J = \pm 5\%; G = \pm 2\%; F = \pm 1\%$		• • • • • • • •	•••••