TOSHIBA Transistor Silicon NPN · PNP Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN47A6

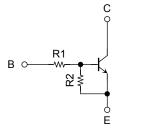
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

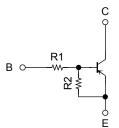
- Two devices are incorporated into an Ultra-Super-Mini (5 pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly cost.

Equivalent Circuit and Bias Resistor Values

Q1

Q2





Q1

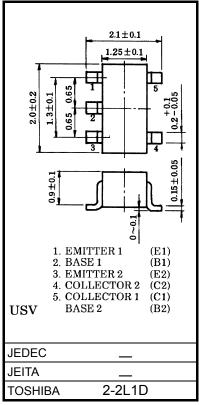
R1: 100 k Ω , R2: 100 k Ω

Q2

R1: 100 k Ω , R2: 100 k Ω

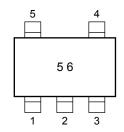
Q1: RN1130F Q2: RN2130F

Unit: mm

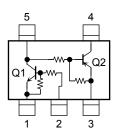


Weight: 0.0062g (typ.)

Marking



Equivalent Circuit (top view)



Absolute Maximum Ratings (Ta = 25°C) (Q1)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	50	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	10	V
Collector current	IC	100	mA

Absolute Maximum Ratings (Ta = 25°C) (Q2)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	-50	V
Collector-emitter voltage	V _{CEO}	-50	V
Emitter-base voltage	V _{EBO}	-10	V
Collector current	IC	-100	mA

Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Collector power dissipation	P _C (Note 1)	200	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	−55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Total rating

Electrical Characteristics (Ta = 25°C) (Q1)

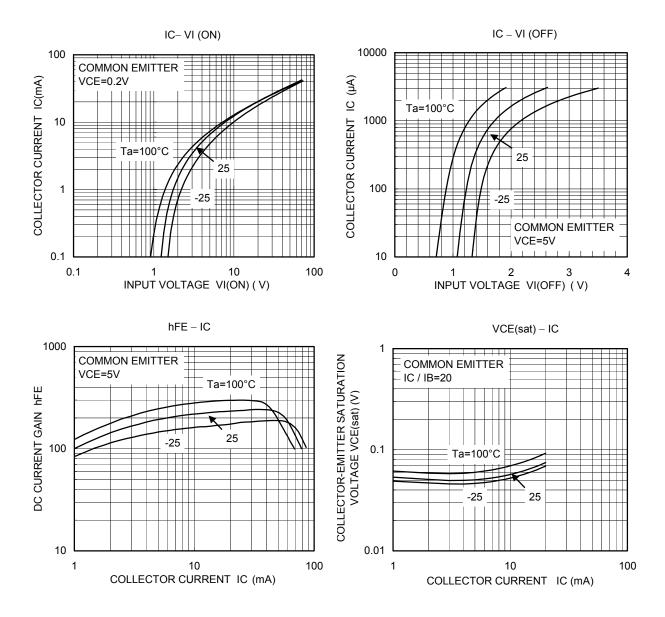
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Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = 50 \text{ V}, I_{E} = 0$	_	_	100	nA
Collector cut-on current	I _{CEO}	$V_{CE} = 50 \text{ V}, I_{B} = 0$	_	_	500	IIA
Emitter cut-off current	I _{EBO}	V _{EB} = 10 V, I _C = 0	0.038	_	0.072	mA
DC current gain	h _{FE}	$V_{CE} = 5 \text{ V}, I_{C} = 10 \text{ mA}$	100	_	_	
Collector-emitter saturation voltage	V _{CE} (sat)	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$	_	0.1	0.3	V
Input voltage (ON)	V _{I (ON)}	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.7	_	8.2	V
Input voltage (OFF)	V _{I (OFF)}	V _{CE} = 5 V, I _C = 0.1 mA	1.0	_	1.6	V
Transition frequency	f _T	V _{CE} = 10 V, I _C = 5 mA	_	250	_	MHz
Collector output capacitance	C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	3	_	pF
Input resistor	R1	_	70	100	130	kΩ
Resistor ratio	R1/R2	_	0.8	1.0	1.2	

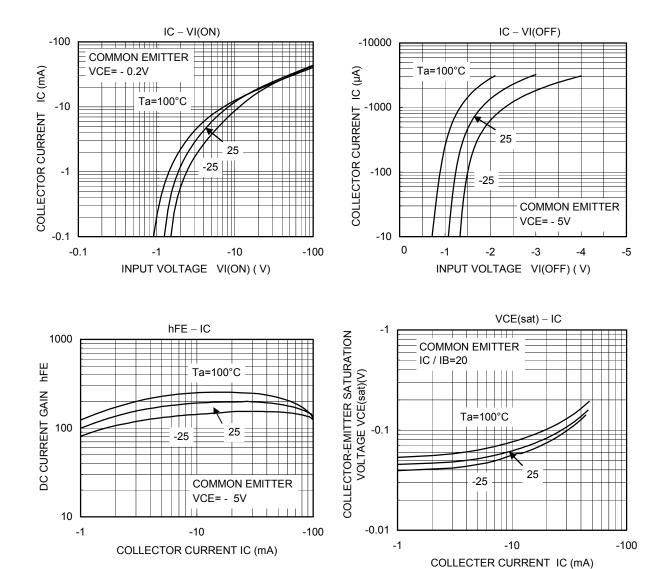
Electrical Characteristics (Ta = 25°C) (Q2)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I _{CBO}	$V_{CB} = -50 \text{ V}, I_E = 0$	_	_	-100	nA
	I _{CEO}	$V_{CE} = -50 \text{ V}, I_B = 0$	_	_	-500	IIA
Emitter cut-off current	I _{EBO}	$V_{EB} = -10 \text{ V}, I_{C} = 0$	-0.038	_	-0.072	mA
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, I_{C} = -10 \text{ mA}$	100	_	_	
Collector-emitter saturation voltage	V _{CE} (sat)	$I_C = -5 \text{ mA}, I_B = -0.25 \text{ mA}$	_	-0.1	-0.3	V
Input voltage (ON)	V _{I (ON)}	$V_{CE} = -0.2 \text{ V}, I_{C} = -5 \text{ mA}$	-1.7	_	-8.2	V
Input voltage (OFF)	V _{I (OFF)}	$V_{CE} = -5 \text{ V}, I_{C} = -0.1 \text{ mA}$	-1.0	_	-1.6	V
Transition frequency	f _T	$V_{CE} = -10 \text{ V}, I_{C} = -5 \text{ mA}$	_	200	_	MHz
Collector output capacitance	C _{ob}	V _{CB} = -10 V, I _E = 0, f = 1 MHz	_	3	_	pF
Input resistor	R1	_	70	100	130	kΩ
Resistor ratio	R1/R2	_	0.8	1.0	1.2	

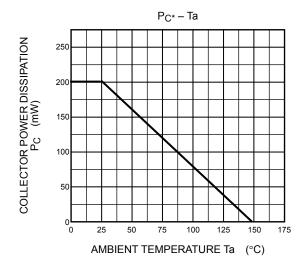
Q1



Q2



Q1, Q2 Common



*Total Rating.

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RESTRICTIONS ON PRODUCT USE

20070701-EN GENERAL

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