

RJK0397DPA

Silicon N Channel Power MOS FET Power Switching

REJ03G1788-0210 Rev.2.10 Apr 03, 2009

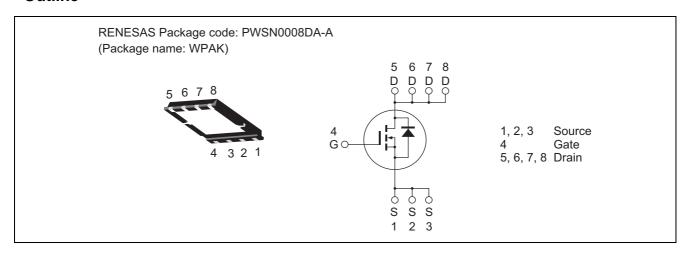
Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance

 $R_{DS(on)}\!=7.8~m\Omega$ typ. (at $V_{GS}\!=10~V)$

- Pb-free
- Halogen-free

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	30	A
Drain peak current	I _{D(pulse)} Note1	120	A
Body-drain diode reverse drain current	I _{DR}	30	A
Avalanche current	I _{AP} Note 2	8	A
Avalanche energy	E _{AR} Note 2	6.4	mJ
Channel dissipation	Pch Note3	25	W
Channel to case thermal impedance	θch-c Note3	5	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. $Tc = 25^{\circ}C$

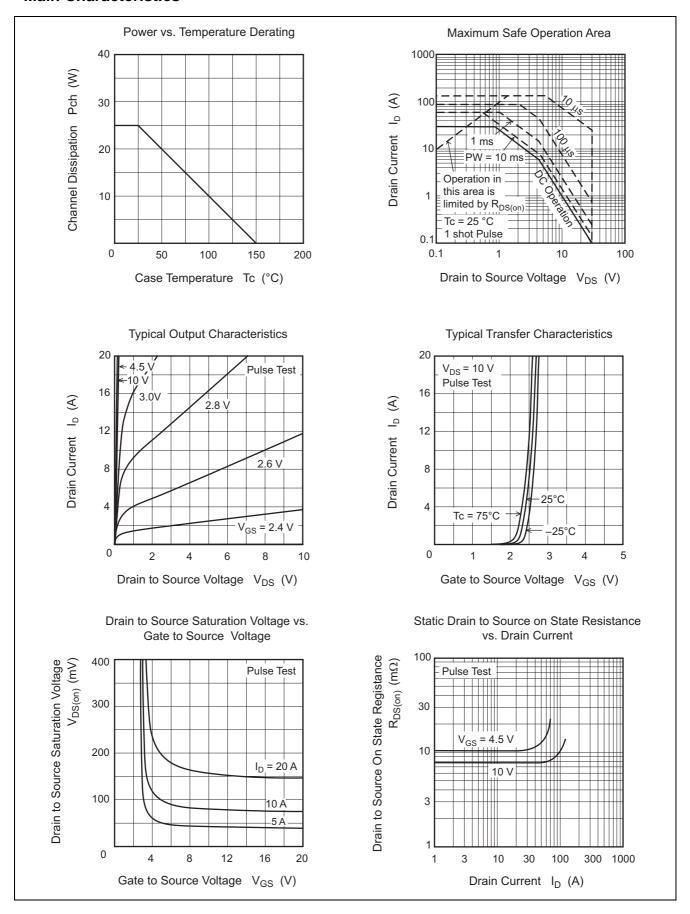
Electrical Characteristics

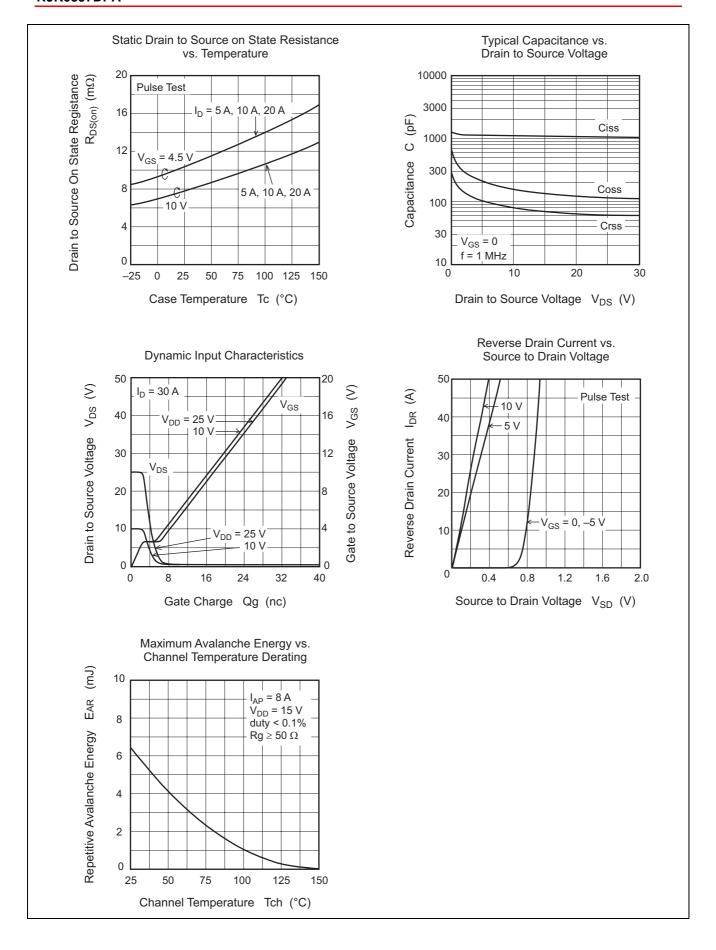
 $(Ta = 25^{\circ}C)$

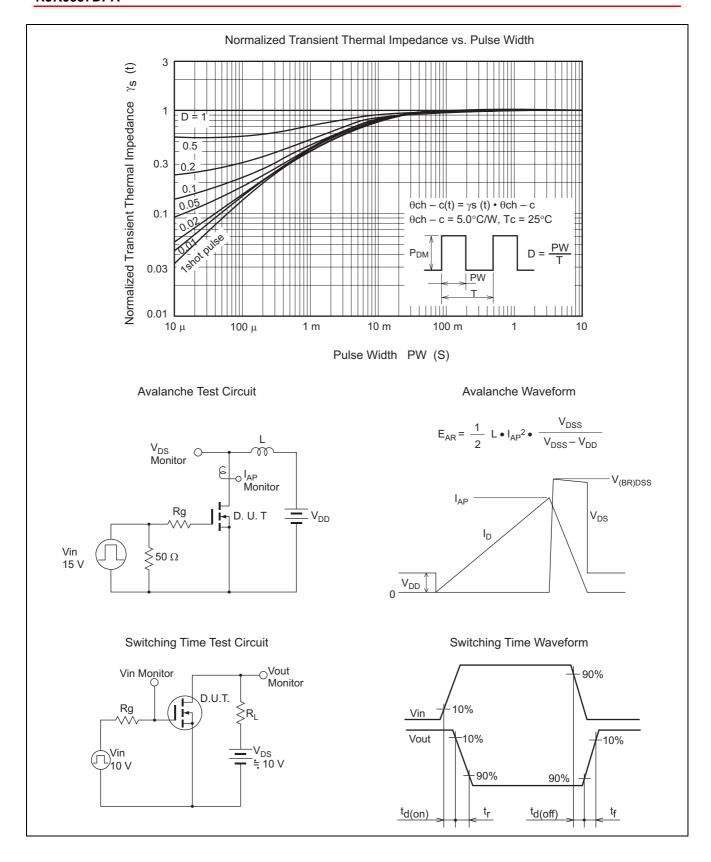
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I_{GSS}	_	_	± 0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS(off)}	1.2	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	7.8	10.1	mΩ	$I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	10.4	14.6	mΩ	$I_D = 15 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	_	65	_	S	$I_D = 15 \text{ A}, V_{DS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	1110	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	160	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	80	_	pF	f = 1 MHz
Gate Resistance	Rg	_	2.5	_	Ω	
Total gate charge	Qg	_	7.4	_	nC	V _{DD} = 10 V
Gate to source charge	Qgs	_	3.2	_	nC	$V_{GS} = 4.5 \text{ V}$
Gate to drain charge	Qgd	_	1.9	_	nC	$I_D = 30 \text{ A}$
Turn-on delay time	$t_{d(on)}$	_	8.8	_	ns	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A}$
Rise time	t _r	_	4	_	ns	$V_{DD} \cong 10 \text{ V}$
Turn-off delay time	$t_{d(off)}$	_	36	_	ns	$R_L = 0.67 \Omega$
Fall time	t _f	_	5.1	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	V_{DF}	_	0.88	1.15	V	$I_F = 30 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	t _{rr}	_	13	_	ns	$I_F = 30 \text{ A}, V_{GS} = 0$ $di_F / dt = 100 \text{ A} / \mu \text{s}$

Notes: 4. Pulse test

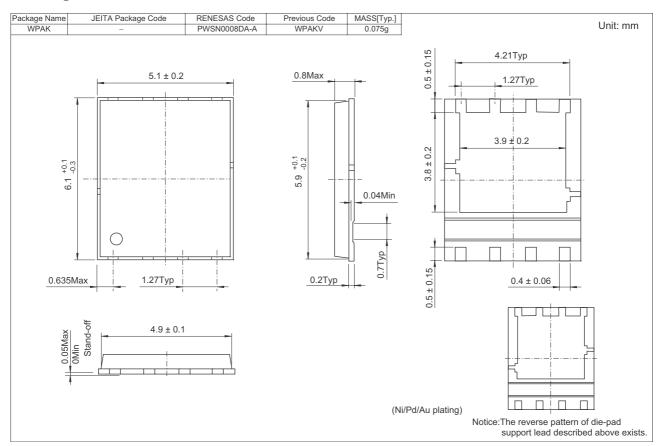
Main Characteristics







Package Dimensions



Ordering Information

Part No.	Quantity	Shipping Container
RJK0397DPA-00-J53	3000 pcs	Taping

Renesas Technology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

- Renesas lechnology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Notes:

 1. This document is provided for reference purposes only so that Renesas customers may select the appropriate Renesas products for their use. Renesas neither makes warrantes or representations with respect to the accuracy or completeness of the information in this document nor grants any license to any intellectual property girbs to any other rights of representations with respect to the information in this document in this document of the purpose of the respect of the information in this document in the product data, diagrams, charts, programs, algorithms, and application circuit examples.

 3. You should not use the products of the technology described in this document for the purpose of military use. When exporting the products or technology described herein, you should follow the applicable export control laws and regulations, and procedures required by such laws and regulations, and procedures required to change without any plan protein. Before purchasing or using any Renesas products listed in this document, in the development is satisfied. The procedure is such as the development of the dev



RENESAS SALES OFFICES

http://www.renesas.com

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510