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## PRELIMINARY

## Y/C MIX

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### ■ FEATURE

1. Y/C mix circuit
2. Output and Chrominance muting function
3. Output protection function
4. SAG correction function
5. A load sufficient for driving two circuits
6. Low power consumption, 60mw typical
7. Internal voltage clamp circuit

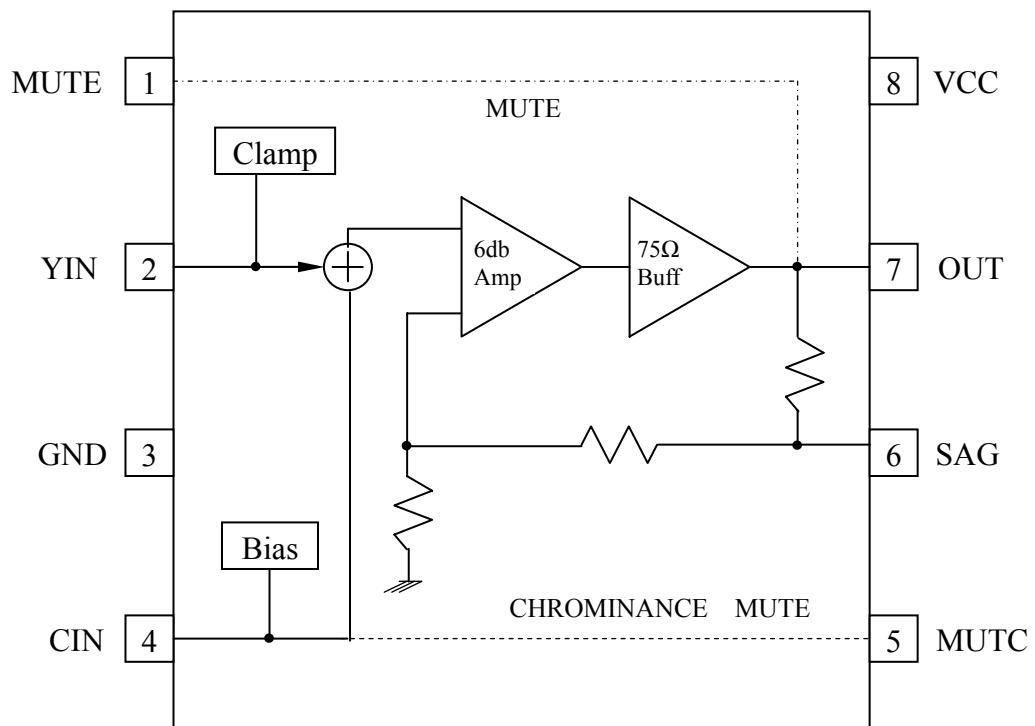
### ■ APPLICATION

- VCR
- Video Camera
- TV
- Video Player

### ■ DESCRIPTION

AA8600AP is a Y/C mix with 6db gain,  $75\Omega$  output buffer and SAG correction. The  $75\Omega$  buffer is capable of driving two circuits. The SAG could reduce the capacitance of output coupling capacitor. The Y signal is clamped input, while the C signal is biased input. The power saving circuit provides the output and chrominance muting function. The IC is available in 8-pin TSSOP.

### ■ BLOCK DIAGRAM & PIN ASSIGNMENT





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■ **PIN DESCRIPTION**

<b>Symbol</b>	<b>Pin-No</b>	<b>Type</b>	<b>Function</b>
<b>MUTE</b>	1	I	Output is muted as this pin is set to high
<b>YIN</b>	2	I	Composite Y input signal is clamped input
<b>GND</b>	3		Ground
<b>CIN</b>	4	I	Chrominance C input signal is bias input
<b>MUTC</b>	5	I	Only chrominance output signal is muted as this pin is set to high
<b>SAG</b>	6	O	SAG correction to reduce the capacitance of output coupling capacitor
<b>OUT</b>	7	O	Y/C mix output. As it is less than 0.6V, the protect circuit is triggered to enable the power saving function
<b>VCC</b>	8		Power supply

■ **ABSOLUTE MAXIMUM RATING**

Ta = 25

<b>Parameter</b>	<b>Symbol</b>	<b>Rating</b>			<b>UNIT</b>	<b>CONDITION</b>
		<b>MIN</b>	<b>TYP</b>	<b>MAX</b>		
<b>Supply Voltage</b>	<b>Vcc</b>	-0.3		8	V	
<b>Operating Ambient Temperature</b>	<b>Ta</b>	-25		75		
<b>Storage Temperature</b>	<b>Ts</b>	-55		125		

NOTE : Stress above those listed under "Absolute Maximum Rating" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for the extended periods of time may affect device reliability.



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**■ ELECTRICAL CHARACTERISTICS**

T<sub>a</sub> = 25 °C, V<sub>CC</sub> = 5V

Parameter	Symbol	Min	Typ	Max	Unit	Condition
<b>Supply Current</b>	I <sub>CC</sub>		12.6	20	mA	No input Signal
<b>Voltage Gain</b>	G <sub>V</sub>		-0.3		dB	Input Signal Freq = 4.43MHz, 1Vp-p, measure V <sub>O1</sub>
<b>Frequency Characteristic</b>	G <sub>F</sub>	-1.5	-0.5	0.5	dB	Input Signal Freq = 7MHz/1MHz, 1Vp-p, measure V <sub>O1</sub>
<b>Muting Attenuation</b>	M <sub>T</sub>		-64		dB	Input Signal Freq = 4.43MHz, 1Vp-p, measure V <sub>O1</sub>
<b>Muting High Level</b>	V <sub>MH</sub>	2.2		V <sub>CC</sub>	V	
<b>Muting Low Level</b>	V <sub>ML</sub>	GND		0.8	V	
<b>Input Impedance</b>	Z <sub>IN</sub>	15	20	25	KΩ	CIN, 50uA
<b>Muted Current</b>	I <sub>M</sub>		1	2.5	mA	MUTE = 'H'
<b>SAG-Terminal Gain</b>	G <sub>SAG</sub>		48		dB	
<b>Differential Gain</b>	D <sub>G</sub>		1	2	%	V <sub>IN</sub> = 1Vp-p, refer staircase signal
<b>Differential Phase</b>	D <sub>P</sub>		1	2.5	deg	V <sub>IN</sub> = 1Vp-p, refer staircase signal

**■ MUTE MODE**

Function	MUTE	MUTC
<b>Output Muting</b>	H	NA
<b>Chrominance Muting</b>	L	H



Agamem Microelectronics Inc. **AA8600AP**

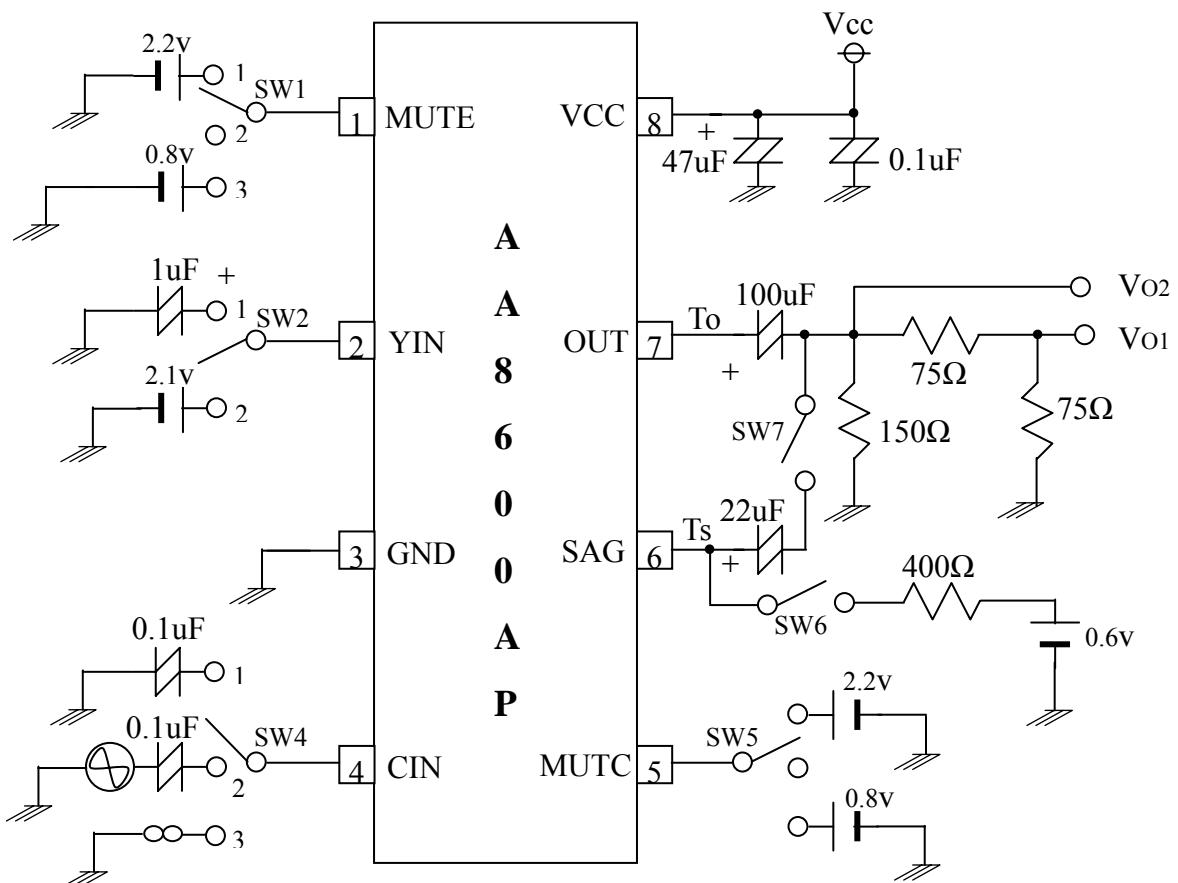
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**■ MEASUREMENT CIRCUIT**



**PRELIMINARY****Y/C MIX****■ MEASUREMENT CONDITIONS**

Parameter	Symbol	SW Condition						Measurement Method
		1	2	4	5	6	7	
Supply Current	I <sub>cc</sub>	2	1	1	2	O	C	No signal input
Voltage Gain	G <sub>v</sub>	3	2	2	3	O	C	Input a 4.43MHz, 1Vp-p sine wave from CIN and then measure the output level from V <sub>O1</sub>
Frequency Characteristic	G <sub>f</sub>	3	1	2	3	O	C	G <sub>V1M</sub> : voltage gain at input frequency=1MHz G <sub>V7M</sub> : voltage gain at input frequency=7MHz G <sub>F</sub> =G <sub>V7M</sub> -G <sub>V1M</sub>
Output Mute	M <sub>T</sub>	1	1	2	3	O	C	Input a 4.43MHz, 1Vp-p sine wave and then measure the output level from V <sub>O1</sub>
Chrominance Mute	M <sub>c</sub>	3	2	2	1	O	C	Same as output muting
Input Impedance	Z <sub>IN</sub>	3	1	3	3	O	C	Measure the input voltage V <sub>IS</sub> & the open voltage V <sub>IO</sub> when a 50uA is introduced. The Z <sub>IN</sub> =  V <sub>IS</sub> -V <sub>IO</sub>   / 50uA
Muted Current	I <sub>M</sub>	1	1	1	2	O	C	Measure the current of power supply when mute is set at high
AG Terminal Gain	G <sub>SAG</sub>	3	2	2	3	O	C	Measure the value of T <sub>s</sub> and T <sub>o</sub> at the first as V <sub>S1</sub> & V <sub>O1</sub> and the value at second condition as V <sub>S2</sub> & V <sub>O2</sub> , respectively.
		3	2	2	3	C	O	G <sub>SAG</sub> = 20log{(V <sub>S2</sub> - V <sub>S1</sub> ) / (V <sub>O2</sub> - V <sub>O1</sub> )}

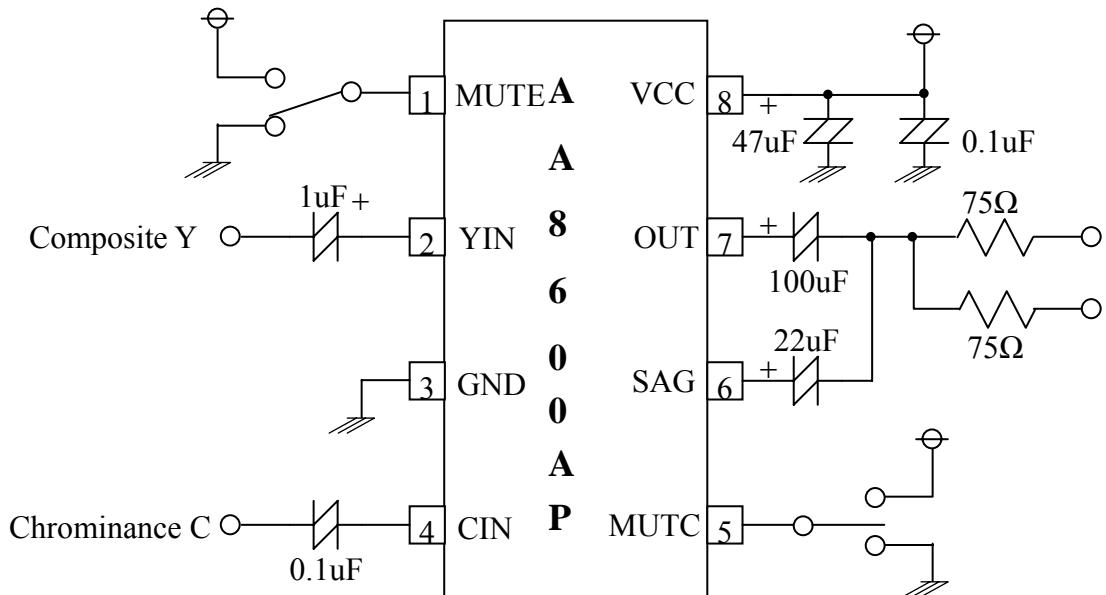


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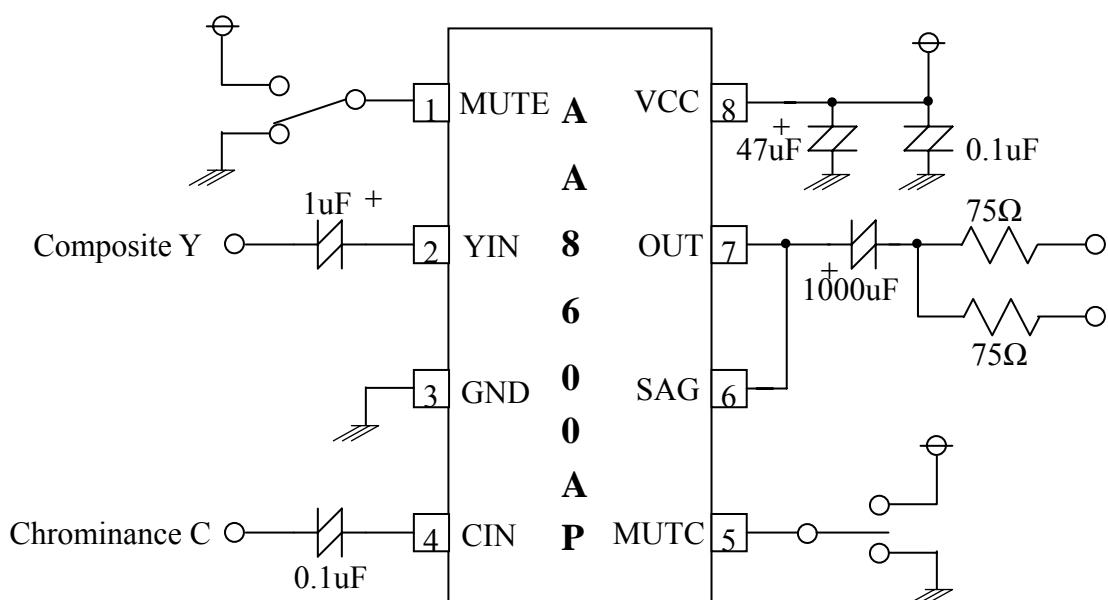
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■ APPLICATION

➤ With SAG



➤ Without SAG

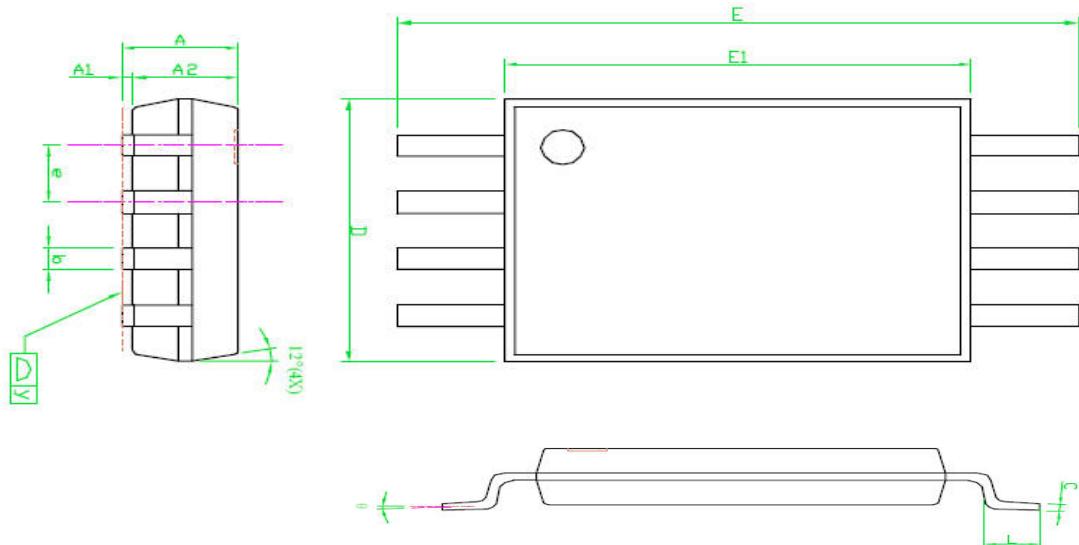




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**■ TSSOP 8L PACKAGE DIMENSION**



**NOTES:**

1. Package body sizes exclude mold flash protrusions or gate burrs
2. Tolerance  $\pm 0.1 \text{ mm}$  (4 mil) unless otherwise specified
3. Coplanarity: 0.1 mm
4. Controlling dimension is millimeter converted inch dimensions are not necessarily exact
5. Followed from jedec mo-153

SYMBOLS	DIMENSIONS IN MILLIMETERS			DIMENSIONS IN INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	---	---	1.20	---	---	0.048
A1	0.05	---	0.15	0.002	---	0.006
A2	0.80	1.00	1.05	0.031	0.039	0.041
b	0.19	---	0.30	0.007	---	0.012
C	0.09	---	0.20	0.004	---	0.008
D	2.90	3.00	3.10	0.114	0.118	0.122
E	6.20	6.40	6.60	0.244	0.252	0.260
E1	4.30	4.40	4.50	0.169	0.173	0.177
e	---	0.65	---	---	0.026	---
L	0.45	0.60	0.75	0.018	0.024	0.030
y	---	---	0.10	---	---	0.004
	0°	---	8°	0°	---	8°