



SELECTION GUIDE									
Model Number	Power		Output C	Current		Production Status	ROHS		
		5V	3.3V	12V	-12V		2002/95/EC		
PCI500D-1	500W	74A ²	60A ²	10A	2.5A	Consult Factory	No		
PCI500D-1C	500W	74A ²	60A ²	10A	2.5A	Active	Yes		

INPUT CHARACTERISTICS					
Parameter	Conditions	Min	Тур	Max	Units
Input Operating Voltage		36		72	Vdc
Input Voltage Withstand		34		75	Vdc
Inrush Current	36Vdc input, cold start			25	A _{pk}
illiusii Current	72Vdc inpro, cold start			50	A _{pk}

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N+n redundant configurable	
No minimum load	
Active current sharing	
Status indicator LEDs	
Synchronous startup control	OF
DC OK & Fan Alarm signals	יע
PCI output voltage(+5/+3.3/+12/-12)	Co

DESCRIPTION

UL, cUL, VDE, CE marked

500W power

Hot-swap capable

Widerange 36-72Vdc input range

The PCI500-4D is a modular, hot-swap, 500W, quad output power supply with wide range 48Vdc input. This unit was designed specifically for redundant applications with active current sharing, synchronous starting, and output isolation diodes.

The PCI500-4D incorporates a unique architecture that supports migration of low-voltage requirements between the 5V and 3.3V rails by deriving both of these outputs from a common transformer winding.

The PCI500-4D is ideal for networking equipment, communications, and computer equipment where fault-tolerance is a necessity. All outputs have remote sense and are individually protected against overloads and short circuits. With UL/cUL approval to UL1950, VDE approval to EN60950, and the CE Mark, the PCI500-4D provides a truly global power solution for your PCI requirements..

OLE for Replace State Factory for Replace VS	ont Mod	E				
ETE de	cemel Voltage	Output	t Current		Total Regulation ¹	
Cor Repic	Nominal voltage	Min	Ma	ıx	Total n	eguiation
tory 10.	+5.0Vdc	0A	74	A ²	±	:2%
actface	+3.3Vdc	0A	60	A ²	±	:2%
V3	+12Vdc	0A	16	A	±	:2%
V4	-12Vdc	0A	2.5	iΑ	±	:2%
Parameter	Conditions		Min	Тур	Max	Units
Temperature Coefficient	After 30-minute warmu	р			0.02	%/°C
PARD (V1 & V2)	20MHz bandwidth				60	$mV_{_{p-p}}$
PARD (V3 & V4)	20MHz bandwidth				120	mV _{p-p}
Output Power	40°C ambient		0		500	W
Output Power	50°C ambient		0		350	W
Transient Response	ΔV, 25% load step			±5	%V _{nom}	
Tutiolotti Hooponoo	Settling time				400	μsec
Output Voltage Adjustment	All outputs			±5		%V _{nom}
Over-Voltage Protection	Output V1, latching		6.0	6.5	7.0	Vdc
Over voltage i rotection	Output V2, latching		3.9	4.3	4.7	Vdc
Minimum Load			0			Α
Remote Sense Compensation	All outputs	All outputs				mV
Current Share Tolerance	V1-V4; full load				±10	%
	Pri-Sec		3			Vdc
Isolation	Pri-Chassis		1.5			Vdc
	Sec-Chassis		500			Vdc









Notes:

1. Total regulation includes line, load, and cross regulation.

2. Combined current output of V1 & V2 not to exceed 83A total. Outputs V1, V2, and V3 share a common return. Outputs V4 has an isolated return.



GENERAL CHARACTERISTICS	GENERAL CHARACTERISTICS						
Parameter	Conditions	Min	Тур	Max	Units		
Efficiency	48Vdc input, 500W load (dependent upon load profile)		65		%		
Switching Frequency			72		kHz		
MTBF	Calculated per MIL-HDBK-217F, 25°C, ground benign	84			khrs		
Weight	Unpackaged		3.6		kg		

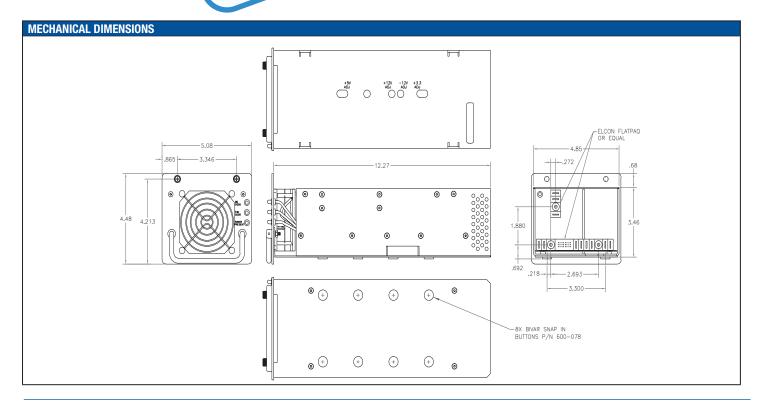
PROTECTION							
Parameter	Conditions/Response	Inception					
raiailletei	Conditions/nesponse	Min	Тур	Max	Units		
Thermal Shutdown	Automatic recovery upon restoration to operational temperatures		90		°C		
Output Power Limit	Automatic recovery		530		W		
Input Protection	Internal line fuse, Littlefuse BLN 25P ROHS or equivalent			25	Α		
Over Veltage Protection	Output V1, latching			6.5	Vdc		
Over-Voltage Protection	Output V2, latching			4.3	Vdc		
Parameter	Conditions/Response						
Output Overload Protection	Outputs are individually protected against our leads are making short circular.						
Hot-Swap Capability	Output V1, latching Output V2, latching Output S2, Latching Output S2, Latching Output S2, Latching Output S2, Latching Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are indexime short circular with automatic recovery upon removal of the fault condition. Design Verification Testing Outputs are individually protected against one treats are individually vide of the fault condition.						
Output Fault Isolation	Output, solation devices are present in all outputs to isolate faults within a failed power supply.						

STATUS & CONTROL SIGNALS &	STATUS & CONTROL SIGNALS & INDICATORS						
Name	Description						
Hot-Swap Enable	Short pin on connector will enable the outputs when the mating pin is tied to DC GND. Supply will not power up until this pin is engaged to its mate in the backplane. Unit output will be inhibited as pin is disengaged from the mating connector.						
Output Inhibit	Secondary referenced; active low, TTL compatible. Logic "0" or short circuit to DC GND inhibits all outputs.						
DC Good	Secondary referenced. Open collector signal with an internal 2.2k pull-up resistor is connected to the +5V output. TTL signal will transition high when all outputs are between 90% and 110% of their nominal voltage.						
Remote Sense	Connection of the sense leads across the load at the desired point of regulation will compensate for voltage distribution drops up to 700mV between the output terminals of the power supply and the point of connection. The unit reverts to local sensing if the sense lines are opened for any reason. The output is protected against shorted or open leads. Applies to all outputs.						
Fan Alarm	Secondary referenced; TTL compatible; active low. Signal transitions to a Logic 0 denotes a thermal warning.						
Power Present Indicator LED	A green LED will be illuminated when the input voltage is present and above the minimum requirement.						
DC Good Indicator LED	A green LED will be illuminated when the output voltages are within 90-110% of specification, coincident with assertion of the DC Good signal. This LED will be extinguished if any of the output voltages is outside of this range.						
Fan Good Indicator LED	A green LED will be illuminated when the fan is operational, coincident with de-assertion of the Fan Alarm signal. This LED will be extinguished in the event of a fan failure.						
Sync Start	A power supply generated signal used to simultaneously start power supplies connected in parallel when the load on any output exceeds a single power supply's capacity for that output. These pins must be bused together at the backplane in parallel/redundant applications (N+n) when N>1. In simple redundant (1+1) or non-parallel applications (1+0), the pin can be ignored.						
Power Supply Present	This pin presents a DC ground signal to the mating pin in the backplane. It is intended to be used by the system to detect the presence of a power supply when the supply is mated into an available position.						

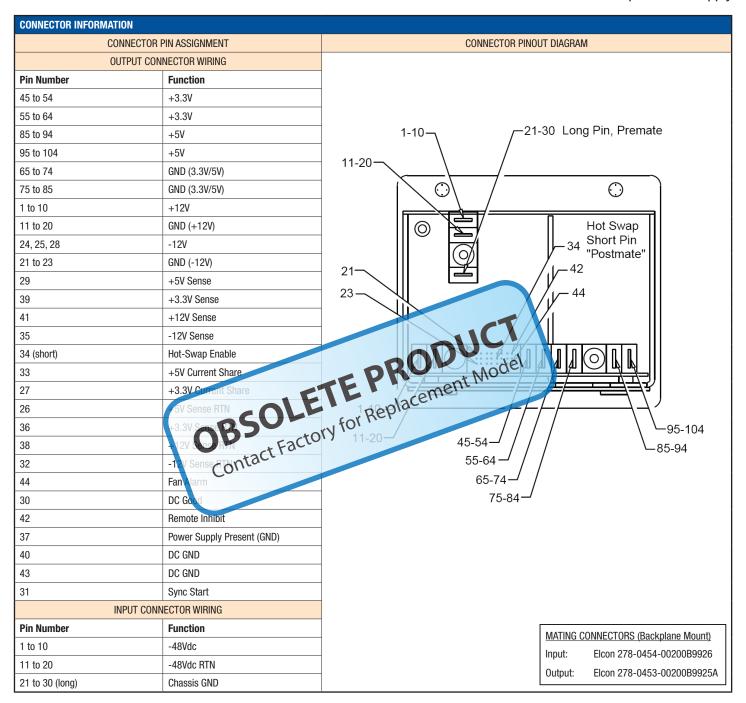


ENVIRONMENTAL CHARACTERISTICS						
Parameter	Conditions	Min	Тур	Max	Units	
AmbientOperatingTemperature	De-rate output power linearly above 40°C to 250W at 60°C.	0		60	°C	
Ambient Storage Temperature		-25		+125	°C	
Humidity	Operating; non-condensing	10		95	%	
numuity	Storage; non-condensing	5		95	%	
Altitudo	Operating. De-rate ambient temperature by 2C° per 1000ft above 5000ft.	-200		10000	ft	
Altitude	Storage	-200		40000	ft	
Cooling	Self-cooled by internal fan					

CERTIFICATIONS		
Agency/Characteristic		Standard
UL		UL1950
CSA		CSA950 (per cUL)
VDE		EN60950
CE		Self-certified
RoHS		EN Directive 2002/95/EC, self-certified; see Selection Guide table for specific model compliance
SELV		Certified
Vibration		will-STD-810, Method 5-14. Procedure 1: self-certifie
Shock		MIL-STD-813, MSP-01 103, Procedure + sN Certified
ELECTROMAGNETIC COMPATABILITY (EMC		lacemen
Conducted Emissions	-601	1 500 386 NF Re Pose
Electrostatic Discharge (ESD)	OBS	FNO(N)-4-2, Level 3, Criteria B
Radiated Immunity	tact Fa	EN61000-4-3, Level 3, Criteria A
Conducted Immunity	Conta	EN61006 4-4, Level 3, Criteria A
Line Voltage Surge		Certified MIL-STD-810, Method State To state The Self-certified MIL-STD-820, Machan









SAFETY AGENCY RATINGS						
Input Voltage	36-72Vdc					
Input Current	25-12.5Adc					

For further information, please visit www.cd4power.com/rohs

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