



**CHENMKO ENTERPRISE CO.,LTD**

*Lead free devices*

**SURFACE MOUNT  
NPN Digital Silicon Transistor**

**VOLTAGE 30 Volts CURRENT 600 mAmpere**

**CHDTC363TKPT**

**APPLICATION**

- \* Switching circuit, Inverter, Interface circuit, Driver circuit.

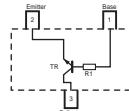
**FEATURE**

- \* Small surface mounting type. (SOT-23)
- \* High current gain.
- \* Suitable for high packing density.
- \* Low collector-emitter saturation( $V_{CE(sat)}$ )=40mV at  $I_C/I_B=50\text{mA}/2.5\text{mA}$ .
- \* High Collector current ( $I_{C(\text{Max.})}=600\text{mA}$ ).
- \* Internal isolated NPN transistors in one package.
- \* Built in single resistor( $R_1=6.8\text{k}\Omega$ , Typ. )

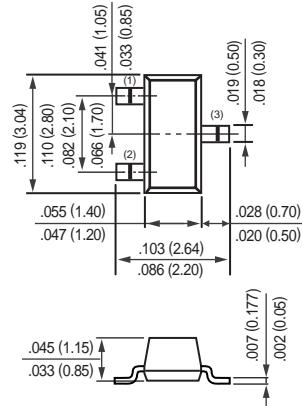
**CONSTRUCTION**

- \* One NPN transistors and bias of thin-film resistors in one package.

**CIRCUIT**



**SOT-23**



Dimensions in inches and (millimeters)

**SOT-23**

**LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-Base voltage		30	V
$V_{CEO}$	Collector-Emitter voltage		15	V
$V_{EBO}$	Emitter-Base voltage		5	V
$I_{C(\text{Max.})}$	Collector current		600	mA
$P_D$	Power dissipation	$T_{\text{amb}} \leq 25^\circ\text{C}$ , Note 1	200	mW
$T_{\text{STG}}$	Storage temperature		-55 +150	°C
$T_J$	Junction temperature		+150	°C
$R_{\theta J-S}$	Thermal resistance , Note 1	junction - soldering point	140	°C/W

**Note**

- Transistor mounted on an FR4 printed-circuit board.

## RATING CHARACTERISTIC ( CHDTC363TKPT )

### CHARACTERISTICS

$T_{amb} = 25^{\circ}\text{C}$  unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
BVCBO	Collector-base breakdown voltage	$I_c=50\mu\text{A}$	30	—	—	V
BVCEO	Collector-emitter breakdown voltage	$I_c=1.0\text{mA}$	15	—	—	V
BVEBO	Emitter-base breakdown voltage	$I_E=50\mu\text{A}$	5.0	—	—	V
I <sub>CBO</sub>	Collector cutoff current	$V_{CB}=20\text{V}$	—	—	0.5	$\mu\text{A}$
I <sub>EBO</sub>	Emitter cutoff current	$V_{EB}=4\text{V}$	—	—	0.5	$\mu\text{A}$
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	$I_c/I_b=50\text{mA}/2.5\text{mA}$	—	0.04	0.08	V
h <sub>FE</sub>	DC current gain	$I_c=50\text{mA}; V_{CE}=5.0\text{V}$	100	250	600	
R <sub>1</sub>	Input resistor		4.76	6.8	8.84	$\text{k}\Omega$
f <sub>T</sub>	Transition frequency	$I_E=-50\text{mA}, V_{CE}=10.0\text{V}$ $f=100\text{MHz}$	—	200	—	MHz

### Note

1. Pulse test:  $t_p \leq 300\mu\text{s}$ ;  $\delta \leq 0.02$ .

## RATING CHARACTERISTIC CURVES ( CHDTC363TKPT )

### Typical Electrical Characteristics

Fig.1 DC current gain vs. collector current

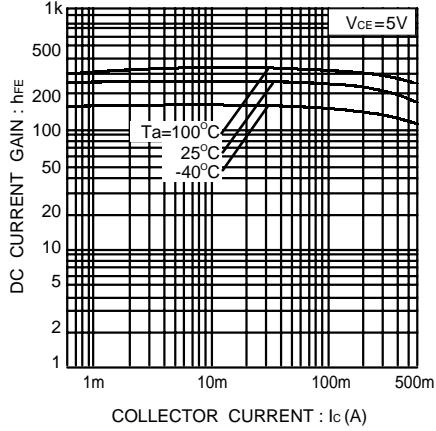


Fig.2 Collector-emitter voltage vs. collector current

