To all our customers

Regarding the change of names mentioned in the document, such as Mitsubishi Electric and Mitsubishi XX, to Renesas Technology Corp.

The semiconductor operations of Hitachi and Mitsubishi Electric were transferred to Renesas Technology Corporation on April 1st 2003. These operations include microcomputer, logic, analog and discrete devices, and memory chips other than DRAMs (flash memory, SRAMs etc.) Accordingly, although Mitsubishi Electric, Mitsubishi Electric Corporation, Mitsubishi Semiconductors, and other Mitsubishi brand names are mentioned in the document, these names have in fact all been changed to Renesas Technology Corp. Thank you for your understanding. Except for our corporate trademark, logo and corporate statement, no changes whatsoever have been made to the contents of the document, and these changes do not constitute any alteration to the contents of the document itself.

Note : Mitsubishi Electric will continue the business operations of high frequency & optical devices and power devices.

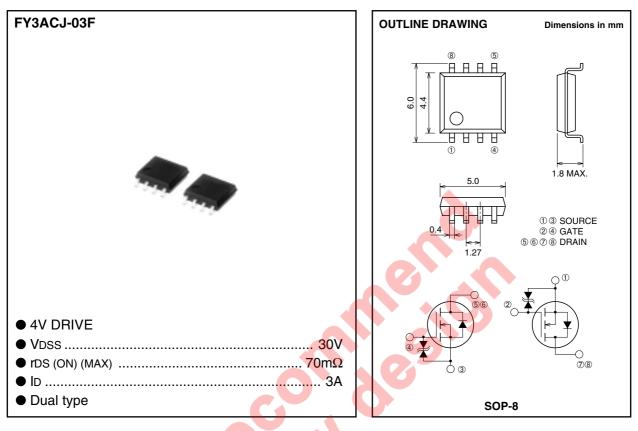
Renesas Technology Corp. Customer Support Dept. April 1, 2003



MITSUBISHI Nch POWER MOSFET

FY3ACJ-03F

HIGH-SPEED SWITCHING USE



APPLICATION

Motor control, Lamp control, Solenoid control DC-DC converter, etc.

MAXIMUM RATINGS (Tc = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit	
VDSS	Drain-source voltage	VGS = 0V	30	V	
Vgss	Gate-source voltage	VDS = 0V	±20	V	
ID	Drain current		3	Α	
ldм	Drain current (Pulsed)		21	A	
IDA	Avalanche drain current (Pulsed)	L = 10µH	3	A	
Is	Source current		1.4	Α	
ISM	Source current (Pulsed)		5.6	Α	
PD	Maximum power dissipation		1.5	W	
Tch	Channel temperature		-55~+150	°C	
Tstg	Storage temperature		-55~+150	°C	
_	Weight	Typical value	0.07	g	



Sep. 2001

MITSUBISHI Nch POWER MOSFET

FY3ACJ-03F

HIGH-SPEED SWITCHING USE

ELECTRICAL CHARACTERISTICS (Tch = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Тур.	Max.	Unit
V (BR)DSS	Drain-source breakdown voltage	ID = 1mA, VGS = 0V	30		—	V
V (BR)GSS	Gate-source breakdown voltage	$IG = \pm 100 \mu A$, $VDS = 0V$	±20	_	—	V
Igss	Gate-source leakage current	$V_{GS} = \pm 20V, V_{DS} = 0V$	—		±10	μA
IDSS	Drain-source leakage current	VDS = 30V, VGS = 0V	—	_	0.1	mA
VGS (th)	Gate-source threshold voltage	ID = 1mA, $VDS = 10V$	1.0	1.5	2.0	V
rDS (ON)	Drain-source on-state resistance	ID = 3A, VGS = 10V	—	50	70	mΩ
rds (ON)	Drain-source on-state resistance	ID = 1.5A, VGS = 4V	—	80	120	mΩ
VDS (ON)	Drain-source on-state voltage	ID = 3A, VGS = 10V	—	150	210	mV
Ciss	Input capacitance	VDS = 10V, VGS = 0V, f = 1MHz	—	260	—	pF
Coss	Output capacitance		—		—	pF
Crss	Reverse transfer capacitance		-		—	pF
td (on)	Turn-on delay time	VDD = 15V, ID = 1.5A, VGS = 10V, RGEN = RGS = 50Ω	_	4.0	_	ns
tr	Rise time		—	6.5	—	ns
td (off)	Turn-off delay time			21.0	—	ns
tf	Fall time			8.5	—	ns
Vsd	Source-drain voltage	Is = 1.4A, Vgs = 0V	—	0.75	1.10	V
Rth (ch-a)	Thermal resistance	Channel to ambient		_	83.3	°C/W

