

1.0MHz Current-Mode Step-Up DC/DC Converter

■ FEATURES

- Fixed Frequency 1.0MHz Current-Mode PWM Operation.
- Adjustable Output Voltage up to 24V.
- 2.5V to 5.5V Input Range.
- Maximum 0.1µA Shutdown Current.
- Programmable Soft-Start.
- Tiny Inductor and Capacitors are allowed.
- Space-Saving TSOT-23-6 and SOT-23-6 Package.

APPLICATIONS

- · OLED Driver for MP3 Player
- · White LED Backlight

DESCRIPTION

The current-mode pulse-width modulation, AlC1899, step up converter is designed for MP3 player. The built-in high voltage N-channel MOSFET allows AlC1899 for step-up applications with up to 24V output voltage, and other low-side switching DC/DC converter.

The high switching frequency allows the use of small external components. The Soft-Start function is programmable with an external capacitor, which sets the input current ramp rate.

The AIC1899 is available in a space-saving TSOT-23-6 and SOT-23-6 package.

■ TYPICAL APPLICATION CIRCUIT

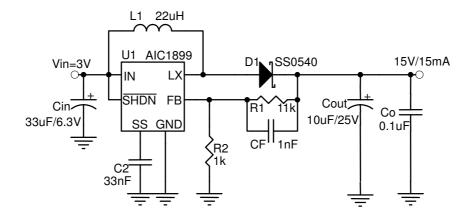
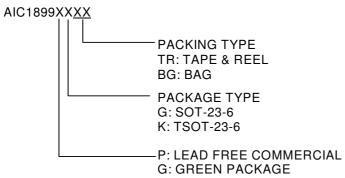


Fig. 1 Typical Step up Application Circuit



ORDERING INFORMATION



Example: AIC1899PGTR

→ in Lead Free SOT-23-6 Package & Tape

& Reel Packing Type

AIC1899PKTR

→ in Lead Free TSOT-23-6 Package & Tape

& Reel Packing Type

● TSOT-23-6 Marking

Part No.	Marking	Part No.	Marking	
AIC1899PK	899PK	AIC1899GK	899GK	

SOT-23-6 Marking

Part No.	Marking	Part No.	Marking
AIC1899PG	1899P	AIC1899GG	1899G

■ ABSOLUTE MAXIMUM RATINGS

IN, SHDN, FB, SS to GND	-0.3V to +6V
LX to GND	-0.3V to +27V
LX Pin RMS Current	
Operating Temperature Range	
Junction Temperature	125℃
Storage Temperature Range	-65℃ to 150℃
Lead Temperature (soldering, 10s)	260℃
Thermal Resistance Junction to Case	130°C /W
Thermal Resistance Junction to Ambient	220°C /W
(Assume no ambient airflow, no heatsink)	

Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.



■ ELECTRICAL CHARACTERISTICS

(V_{IN}=V SHDN =3V, FB=GND, SS=Open, T_A=25°C, unless otherwise specified) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	MAX	UNITS	
Input Supply Range	V _{IN}		2.5		5.5	V	
Output Voltage Adjust Range	V _{OUT}				24	V	
V _{IN} Undervoltage Lockout	UVLO	V _{IN} rising, 50mV hysteresis		2.2		V	
O. in a sent O	I _{IN}	V _{FB} = 1.3V, not switching		0.1	0.2	0.2 5 mA	
Quiescent Current		V _{FB} = 1.0V, switching		1	5		
Outside Outside	lout	V _{in} = 3V, Vout = 15V		15	^		
Output Current		V _{in} = 3.3V, Vout = 15V		17		mA	
Chutdaya Cuaaly Cumaat		$V \overline{SHDN} = 0, T_A = +25^{\circ}C$		0.01	0.5	μΑ	
Shutdown Supply Current		VSHDN = 0		0.01	10	μΑ	
ERROR AMPLIFIER							
Feedback Regulation Set Point	V_{FB}		1.205	1.23	1.255	٧	
FB Input Bias Current	I _{FB}	V _{FB} = 1.24V		21	80	nA	
Line Regulation		2.6V < V _{IN} < 5.5V		0.05	0.20	%/V	
OSCILLATOR							
Frequency	fosc		800	1000	1700	KHz	
Maximum Duty Cycle	DC		80	82		%	
POWER SWITCH							
On-Resistance	R _{DS(ON)}	Vin = 5V		1.2	1.6	Ω	
Laskana Ouwant		V _{LX} = 24V, T _A = +25°C		0.1	1		
Leakage Current		$V_{LX} = 24V, T_A = +25^{\circ}C$ $V_{LX} = 24V$			10	μΑ	
SOFT-START							
Reset Switch Resistance		Guaranteed By Design			100	Ω	
Charge Current		V _{SS} = 1.2V	1.5	4	7.0	μΑ	
CONTROL INPUT							
Input Low Voltage	V _{IL}	$V \overline{SHDN}$, $V_{IN} = 2.5V$ to 5.5V			0.3	V	
Input High Voltage	V _{IH}	$V \overline{SHDN}$, $V_{IN} = 2.5V$ to $5.5V$	1.0			V	
SHDN Input Current	ISHDN	VSHDN = 1.8V		25	50		
		VSHDN = 0		0.01	0.1	μΑ	
					_		

Note 1: Specifications are production tested at T_A=25°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with Statistical Quality Controls (SQC).



TYPICAL PERFORMANCE CHARACTERISTICS

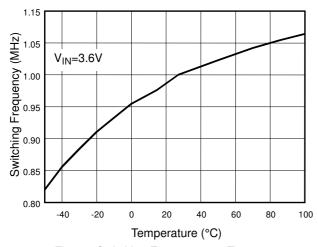


Fig. 2 Switching Frequency vs. Temperature

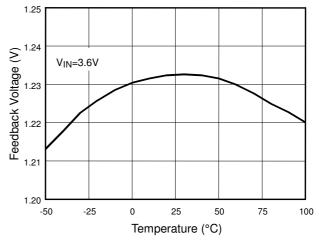
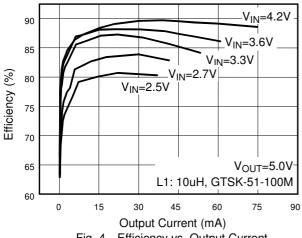


Fig. 3 Feedback Pin Voltage



Efficiency vs. Output Current

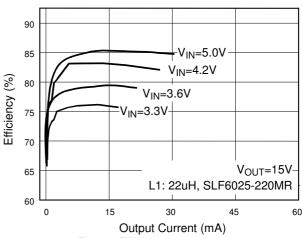


Fig. 5 Efficiency vs. output current

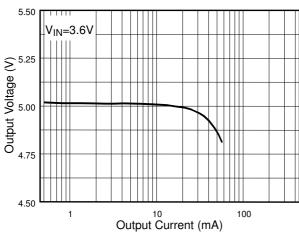


Fig. 6 Load Regulation (L1=10 μ H)

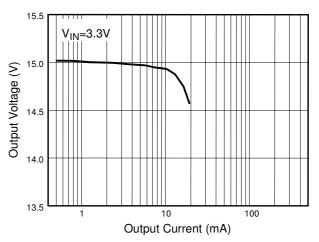
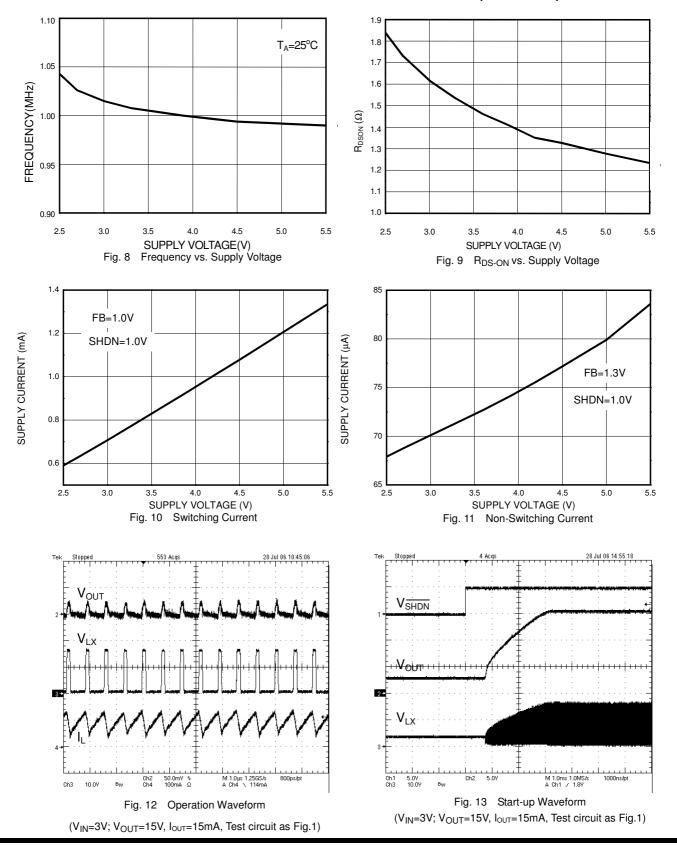


Fig. 7 Load Regulation (L1=22 μ H)

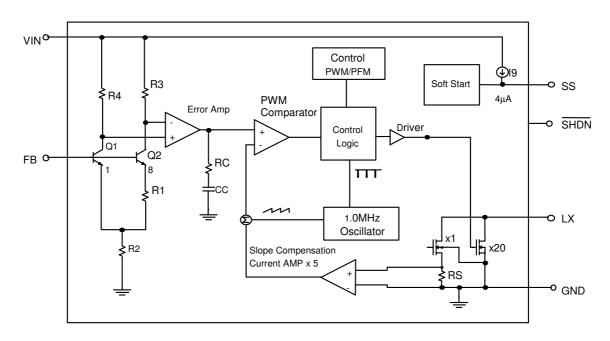


TYPICAL PERFORMANCE CHARACTERISTICS (Continued)





BLOCK DIAGRAM



■ PIN DESCRIPTIONS

PIN 1: LX - Power Switching Connection.

Connect LX to inductor and output rectifier. Keep the distance between the components as close to LX as possible.

PIN 2: GND - Ground.

PIN 3: FB - Feedback Input. Connect a resistive voltage-divider from the output to FB to set the output voltage.

PIN 4: SHDN - Shutdown Input. Drive SHDN low to turn off the converter. To automatically start the converter, connect SHDN to IN. Drive

SHDN with a slew rate of 0.1V/μs or greater. Do not leave SHDN unconnected. SHDN draws up to 50μA.

PIN 5: SS - Soft-Start Input. Connect a soft-start capacitor from SS to GND in order to soft-start the converter. Leave SS open to disable the soft-start function.

PIN 6: IN - Internal Bias Voltage Input.

Connect IN to the input voltage source. Bypass IN to GND with a capacitor sitting as close to IN as possible.



APPLICATION INFORMATION

The AIC1899 operates well with a variety of external components. The components in Figure 1 are suitable for most applications. See the following sections to optimize external components for a particular application.

Inductor Selection

A $22\mu H$ inductor is recommended for most AIC1899 applications. Although small size and high efficiency are major concerns, the inductor should have low core losses at 1.0MHz and low DCR (copper wire resistance).

Inductor selection depends on input voltage, output voltage, maximum current, size, and availability of inductor values. Other factors can include efficiency and ripple voltage. Inductors are specified by their inductance (L), peak current ($I_{L(PK)}$), and resistance (DCR). The following step-up circuit equations are useful in choosing the inductor values based on the application. They allow the trading of peak current and inductor value while considering component availability and cost.

The equation used here assumes a constant K, which is the ratio of the inductor peak-to-peak AC current to average DC inductor current. A good compromise between the size of the inductor versus loss and output ripple is to choose a K of 0.3 to 0.5. The peak inductor current is then given by:

$$i_{L(pk)} = \frac{I_{o(\max)} \cdot V_o}{\eta \cdot V_{i(\min)}} \cdot \left(1 + \frac{K}{2}\right)$$

where:

I_{O(max)}: Maximum output current, (A)

V_{i(min)}: Minimum input voltage, (V)

 η : Conversion efficiency, 0.8

$$K = \frac{\Delta i_L}{I_L}$$
 : Ratio of the inductor peak-to-peak

AC current to average DC inductor current

The inductance value is then given by:

$$L = \frac{V_{i(\min)}^{2} \cdot \boldsymbol{\eta} \cdot D}{K \cdot f \cdot V_{o} \cdot I_{o(\max)}}$$

where:

$$D = Duty \ cycle = \frac{V_{i(\text{min})} - (V_f + V_o)}{I_{i(\text{max})} \cdot R_{ds(on)} - (V_f + V_o)}$$

 $V_{\scriptscriptstyle f}$: Catch diode forward drop

f: Switching frequency

Capacitor Selection

The AIC1899 operates with both tantalum and ceramic output capacitors. When using tantalum capacitors, the zero caused by the ESR of the tantalum is used to ensure stability. When using ceramic capacitors, the zero due to the ESR will be at too high a frequency to be useful in stabilizing the control loop. When using ceramic capacitors, add a feedforward capacitor to increase the phase margin, improving the control-loop stability.

Diode Selection

Schottky diodes, with their low forward voltage drop and fast reverse recovery, are the ideal choices for AlC1899 applications. The forward voltage drop of an Schottky diode represents the conduction losses in the diode, while the diode capacitance (CT or CD) represents the switching losses. For diode selection, both forward voltage drop and diode capacitance need to be considered. Schottky diodes with higher current ratings usually have lower forward voltage drop



and larger diode capacitance, which can cause significant switching losses at the 1.0MHz switching frequency of AIC1899.

Setting the Output Voltage

The AIC1899 operates with an adjustable output from Vin to 24V. Connect a resistive voltage divider from the output to FB (see Fig.1).

Calculate $R_{\rm l}$ and $R_{\rm 2}$ using the equation:

$$\frac{\mathsf{R}_1}{\mathsf{R}_2} = \left(\frac{\mathsf{V}_0}{\mathsf{V}_{\mathsf{FB}}} - 1\right)$$

where $V_{\it FB}$, the step-up regulator feedback set point, is 1.23V. Connect the resistive-divider as close to the IC as possible.

APPLICATION EXAMPLES

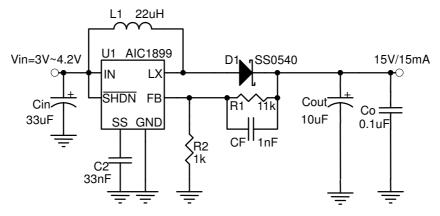
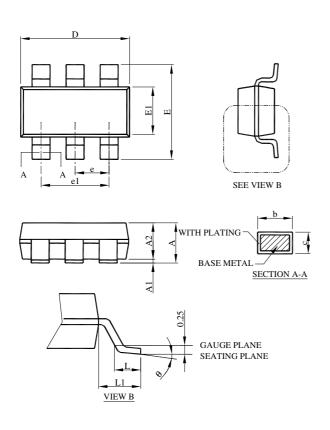


Fig. 14 1-Cell Li-lon boost converter for OLED Application



■ PHYSICAL DIMENSIONS (unit: mm)

TSOT-23-6



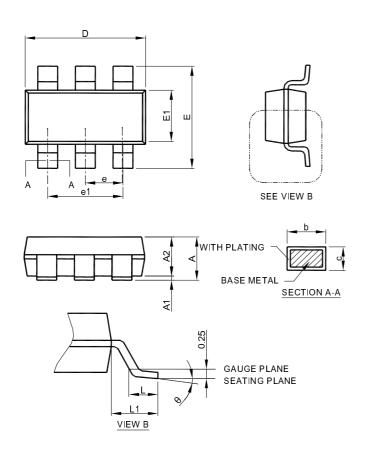
S Y	TSOT-23-6			
- М В О L	MILLIM	MILLIMETERS		
O L	MIN.	MAX.		
Α	-	1.00		
A1	0	0.10		
A2	0.70	0.90		
b	0.30	0.50		
С	0.08	0.22		
D	2.80	3.00		
Е	2.60	3.00		
E1	1.50	1.70		
е	0.95 BSC			
e1	1.90 BSC			
L	0.30	0.60		
L1	0.60 REF			
θ	0°	8°		

Note: 1. Refer to JEDEC MO-193AA.

- 2. Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
- 3. Dimension "E1" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



SOT-23-6



S Y	SOT-23-6 MILLIMETERS		
М			
B O L	MIN.	MAX.	
Α	0.95	1.45	
A1	0.05	0.15	
A2	0.90	1.30	
b	0.30	0.50	
С	0.08	0.22	
D	2.80	3.00	
Е	2.60	3.00	
E1	1.50	1.70	
е	0.95 BSC		
e1	1.90 BSC		
L	0.30	0.60	
L1	0.42 REF		
θ	0°	8°	

Note: 1. Refer to JEDEC MO-178AB.

- Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 10 mil per side.
- 3. Dimension "E1" does not include inter-lead flash or protrusions.
- 4. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

Note:

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