

Silicon NPN Power Transistors

2SD2300

DESCRIPTION

- With TO-3PML package
- High breakdown voltage
- Built-in damper diode

APPLICATIONS

- For color TV horizontal output deflection applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

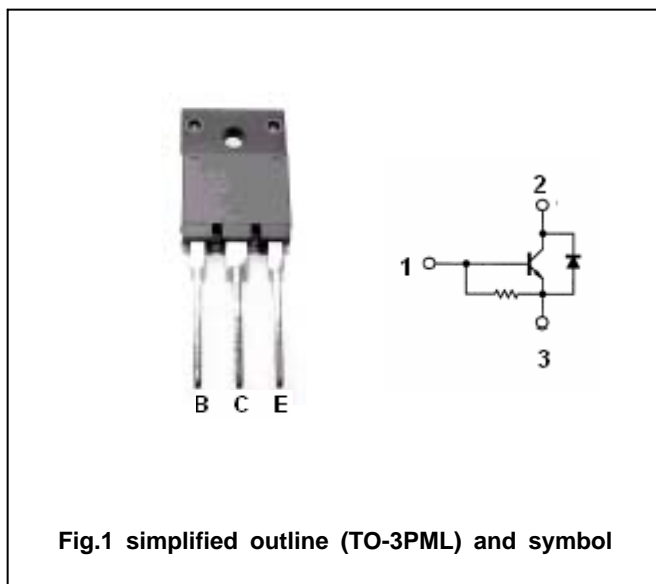


Fig.1 simplified outline (TO-3PML) and symbol

Absolute maximum ratings($T_a=25$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	1500	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current		5	A
I_{CM}	Collector current-peak		6	A
$I_{C(surge)}$	Collector surge current		16	A
P_C	Collector power dissipation	$T_C=25$	50	W
T_j	Junction temperature		150	
T_{stg}	Storage temperature		-55~150	

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CHARACTERISTICS

T_j=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =350mA, I _C =0	6			V
V _{CE(sat)}	Collector-emitter saturation voltage	I _C =4.5A; I _B =1.2A			5.0	V
V _{BE(sat)}	Base-emitter saturation voltage	I _C =4.5A; I _B =1.2A			1.5	V
I _{CES}	Collector cut-off current	V _{CE} =1500V; R _{BE} =0			500	μA
h _{FE}	DC current gain	I _C =1A; V _{CE} =5V			20	
t _f	Fall time	I _C =4A; I _{B1} =0.8A; I _{B2} -1.5A			1.0	μs
V _F	Diode forward voltage	I _F =6A			3.0	V

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PACKAGE OUTLINE

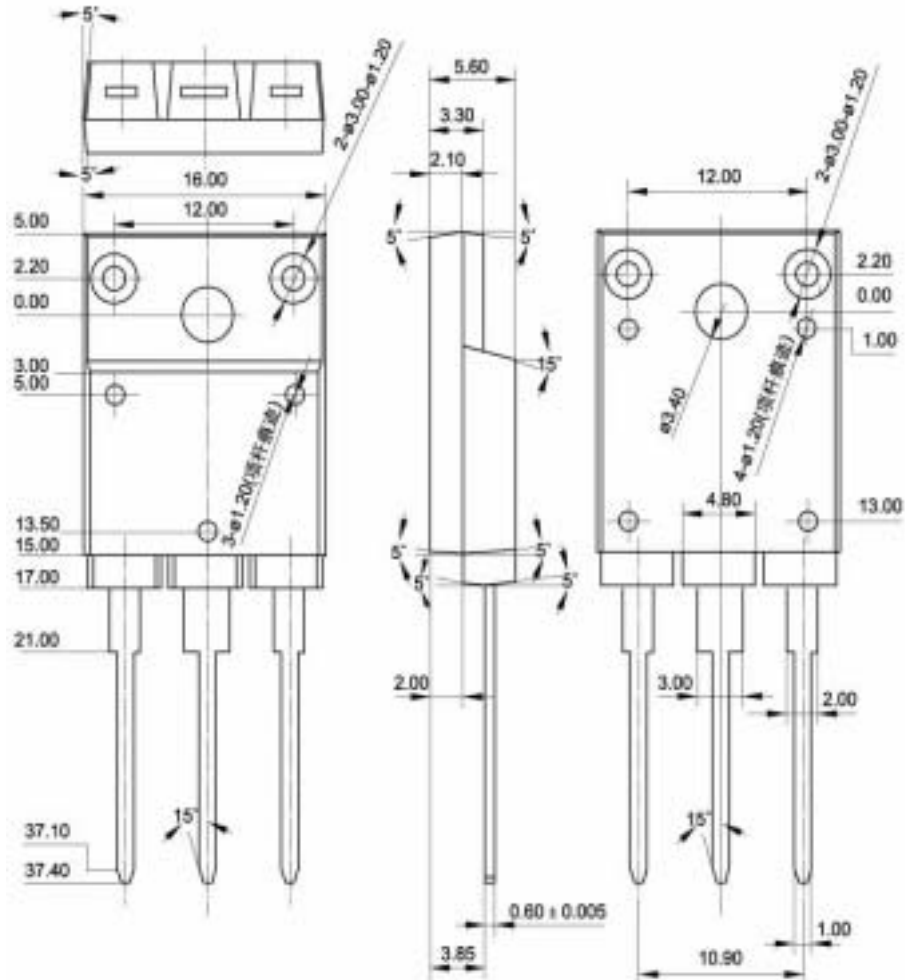


Fig.2 outline dimensions (unindicated tolerance: ±0.10 mm)