



- The Pletronics' LP49 Series is a low profile thru-hole crystal
- Bulk packaging

- 3 MHz to 70 MHz
- HC-49/US
- AT Cut Crystal
- LP21 0.082 (2.10mm) high LP24 0.100 (2.50mm) high LP49 0.140 (3.56mm) high

# Pletronics Inc. certifies this device is in accordance with the RoHS 5/6 (2002/95/EC) and WEEE (2002/96/EC) directives.

Pletronics Inc. guarantees the device does not contain the following: Cadmium, Hexavalent Chromium, Lead (<1000 ppm), Mercury, PBB's, PBDE's</li>
Weight of the Device: 0.62 grams
Moisture Sensitivity Level: 1 As defined in J-STD-020C
Second Level Interconnect code: e1, e2 or e3

## **Electrical Specification:**

Item	Min	Max	Unit	Condition
Frequency Range	3	70	MHz	AT cut
Calibration Frequency Tolerance	-	-	ppm	at +25°C ± 3°C see table on page 3 for
Frequency Stability over OTR	-	-	ppm	available options
Equivalent Series Resistance	-	200	Ohms	3 MHz to 4 MHZ LP49
(ESR)	-	150	Ohms	4 MHZ to 5 MHZ LP49/LP24
	-	120	Ohms	5 MHZ to 6 MHZ LP49/LP24
	-	100	Ohms	6 MHZ to 7 MHZ LP49/LP24 Fundamental
	-	80	Ohms	7 MHZ to 9 MHZ LP49/LP24
	-	70	Ohms	9 MHZ to 10 MHZ LP49/LP24/LP21
	-	50	Ohms	13 MHZ to 15 MHZ LP49/LP24/LP21
	-	40	Ohms	15 MHZ to 27 MHZ LP49/LP24/LP21
	-	35	Ohms	27 MHZ to 30 MHZ LP49/LP24/LP21
	-	100	Ohms	27 MHZ to 32 MHZ LP49/LP24/LP21 3rd Overtone
	-	80	Ohms	32 MHZ to 50 MHZ LP49/LP24/LP21
	-	60	Ohms	50 MHZ to 70 MHZ LP49/LP24/LP21
Drive Level	-	1	mW	use 10 µW for testing
Shunt Capacitance (C0)	-	7	pF	Pad to Pad capacitance
Aging per year	-5	+5	ppm	at +25°C <u>+</u> 3°C
Specified Temperature Range	-40	+85	°C	see table on page 3 for available options
Storage Temperature Range	-55	+125	°C	



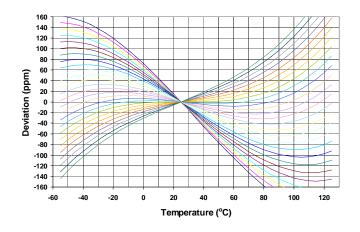
**AT Cut Crystal Frequency** 

versus Temperature

**Typical Performance:** 

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January 2009



Part Marking:

#### 2x*FFFFPymdz* or L2x*FFFFzywwz*

#### Legend:

2	= Model code for LP49
х	<ul> <li>Capacitance load code from below</li> </ul>
FFFFF	= Frequency coded
P or L	= Pletronics
ymd or yww	= Date of Manufacture (year, month and day) or year, week week
All other ma	rking is internal factory codes

Some frequency marking examples: 3.579545M = 03579, 14.31818M = 14181, 24.0M = 24000

Specifications such as frequency tolerance and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Cod	de	Α	В	С	D	Е	F	G	Н	J	к	L	М	Ν	Ρ	Q	R	S	т	U	v	w	X	Y
pF	=	10	12	13	8	15	18	20	22	24	26	28	30	32	34	36	27	series	33	50	19	16	17	14

### Codes for Date Code YMD

Code	6	7	8	9	0	1	2
Year	2006	2007	2008	2009	2010	2011	2012

Code	Α	в	С	D	E	F	G	Н	J	К	L	М
Month	JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC

Code	1	2	3	4	5	6	7	8	9	Α	В	С
Day	1	2	3	4	5	6	7	8	9	10	11	12
Code	D	E	F	G	н	J	К	L	М	Ν	Р	R
Day	13	14	15	16	17	18	19	20	21	22	23	24
Code	Т	U	V	W	Х	Y	Z					
Day	25	26	27	28	29	30	31					



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#### Part Number:

LP49	-18	-14.31818M	-50	Н	1	G	G	-XX	See chart below for available options
									Internal code or blank
									Highest Specified Operating Temperature $A = 40^{\circ}C$ $G = 70^{\circ}C$ $B = 45^{\circ}C$ $H = 75^{\circ}C$ $C = 50^{\circ}C$ $J = 80^{\circ}C$ $D = 55^{\circ}C$ $K = 85^{\circ}C$ $E = 60^{\circ}C$ $F = 65^{\circ}C$
									Lowest Specified Operating Temperature $A = +10^{\circ}C$ $F = -15^{\circ}C$ $L = -40^{\circ}C$ $B = +5^{\circ}C$ $G = -20^{\circ}C$ $C = 0^{\circ}C$ $H = -25^{\circ}C$ $D = -5^{\circ}C$ $J = -30^{\circ}C$ $E = -10^{\circ}C$ $K = -35^{\circ}C$
									Mode: 1 = Fundamental 3 = 3rd Overtone
									Frequency Stability See chart below
									Calibration Frequency Tolerance (Typ. Values shown) $15 = \pm 15 \text{ ppm} \text{ at } 25^{\circ}\text{C} \pm 3^{\circ}\text{C}$ $20 = \pm 20 \text{ ppm} \text{ at } 25^{\circ}\text{C} \pm 3^{\circ}\text{C}$ $30 = \pm 30 \text{ ppm} \text{ at } 25^{\circ}\text{C} \pm 3^{\circ}\text{C}$ (Standard)
									Frequency in MHZ
									Cload in pF Parallel Resonance from 09 to 44 pF or SR = Series Resonance
									Series Model

		Avail	able Freque	ency Stability	y versus Ter	mperature i	n ppm
Operating	]	D	E	F	G	Н	J
Temperature Range	CODE	<u>+</u> 10	<u>+</u> 15	<u>+</u> 20	± 30	± 50	<u>+</u> 100
0 to +45°C	СВ	•	•	•	•	•	•
0 to +50°C	CC	٠	•	•	•	•	٠
0 to +60°C	CE	٠	•	•	•	•	•
0 to +70°C	CG	•	٠	٠	•	STD	٠
-10 to +50°C	EC	٠	•	•	٠	•	•
-10 to +60°C	EE	٠	•	•	•	•	•
-10 to +75°C	EH	٠	•	•	•	•	•
-20 to +70°C	GG	٠	•	•	•	•	٠
-20 to +75°C	GH	٠	•	•	٠	•	•
-30 to +75°C	JH	٠	•	•	٠	•	•
-30 to +80°C	JJ	٠	•	•	٠	•	•
-30 to +85°C	JK	•	•	•	•	•	•
-35 to +80°C	KJ		•	•	•	•	•
-40 to +85°C	LK		•	•	•	•	•



# Legacy Part Number (not for new designs):

LP49	В	E	-18	-11.0592M	-XX	
						Internal code or blank
						Frequency in MHZ
						Cload in pF Parallel Resonance in pF or SR = Series Resonance
						<b>Operating Temperature Range</b> Blank = 0 to + 70°C (STD) <b>E</b> = -40 to +85°C
						Calibration Tolerance / Frequency Stability Blank = 30/50 (STD) B = 30/30 C = 15/30 D = 10/20 (not all frequencies)
						Series Model

## Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

# Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

P/N:	LP49-SR-	
Custo	mer P/N:	
Qty:	123 	0632-WYLF

RoHS Compliant
2nd LvL Interconnect
Category=e3
Max Safe Temp=260C for 10s 2X Max

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant 2nd LvL Interconnect Category=e1 Max Safe Temp=260C for 10s 2X Max

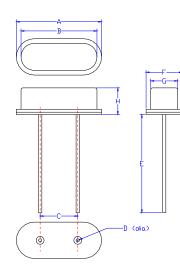
### **RoHS** Compliant

2nd LvL Interconnect Category=e2 Max Safe Temp=260C for 10s 2X Max



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## **Mechanical:**



	Inches	mm
А	0.425 max	10.80 max
В	0.404	10.26
С	0.192	4.88
D	0.017 dia	0.43 dia
E	0.500 min	12.7 min
F	0.176 max	4.47 max
G	0.145	3.68
H LP21	0.082 max	2.10 max
H LP24	0.100 max	2.50 max
H LP49	0.140 max	3.56 max

Contacts (3 types of lead plating used):

Matte Tin (Sn)

Tin over Copper (SnCu)

SAC (SnAgCu)

Not to Scale

<sup>1</sup> Typical dimensions

# Layout and application information

- Trace lengths to the crystal should be kept as short as possible.
- The crystal connections are sensitive to noise.
- The package should be grounded for optimum performance.



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#### **IMPORTANT NOTICE**

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