

HVC363A

Variable Capacitance Diode for TV tuner

REJ03G0517-0200

(Previous: ADE-208-427A)

Rev.2.00

Feb 16, 2005

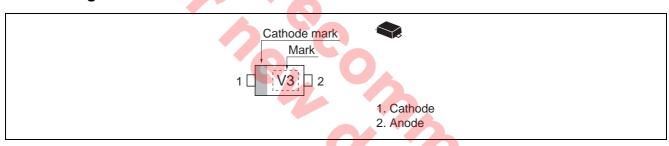
Features

- High capacitance ratio (n = 15.0 Typ)
- Low series resistance ($r_s = 0.75 \Omega \text{ max}$) and good C-V linearity.
- Ultra small Flat Lead Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Renesas Code	Previous Code
HVC363A	V3	PWSF0002ZA-A	UFP

Pin Arrangement



50,00

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	V _{RM} * ¹	35	V
Reverse voltage	V_R	32	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	−55 to +125	°C

Note: 1. $R_L = 10 \text{ k}\Omega$

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

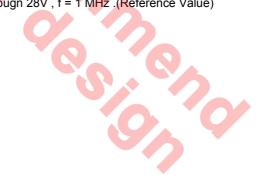
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse voltage	V_R	32	_	_	V	I _R = 1 μA
Reverse current	I _{R1}	_	_	10	nA	V _R = 30 V
	I _{R2}	_	_	100		V _R = 30 V, Ta = 60°C
Capacitance	C ₁	34.65	_	42.35	pF	V _R = 1 V, f = 1 MHz
	C ₂₈	2.361	_	2.754		V _R = 28 V, f = 1 MHz
Capacitance ratio	n	13.50	15.00	_	_	C ₁ /C ₂₈
Series resistance	r _S		_	0.75	Ω	C = 14 pF, f = 470 MHz
Matching error	ΔC/C *1	5 -		2.0	%	V _R = 1 to 28 V, f = 1 MHz
Linealty factor *2	_	_	-1.20	_	_	ΔlogC / ΔlogV

Note: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of ∆C/C continuous in a reel , expect extention to another group.

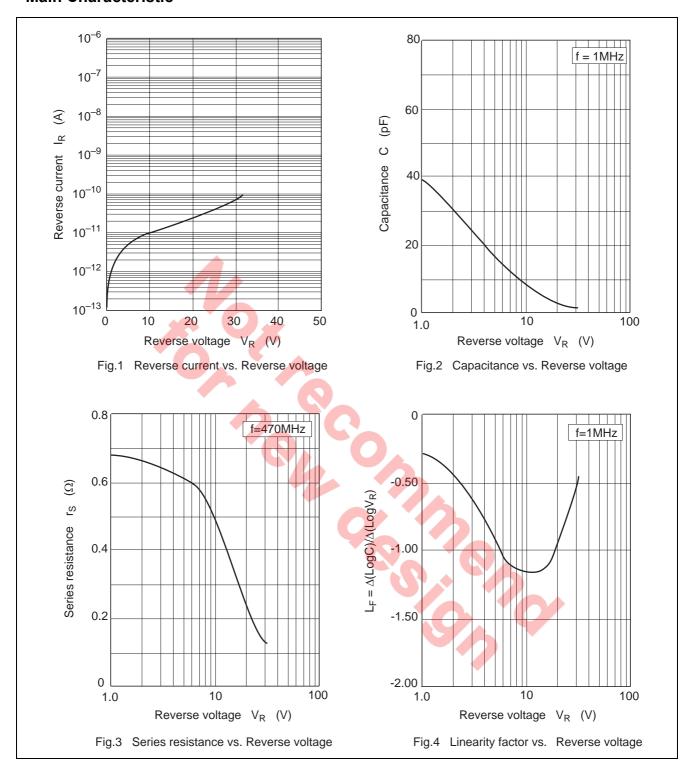
Calculate Matching Error,

$$\Delta C/C = \frac{(Cmax - Cmin)}{Cmin} \times 100 (\%)$$

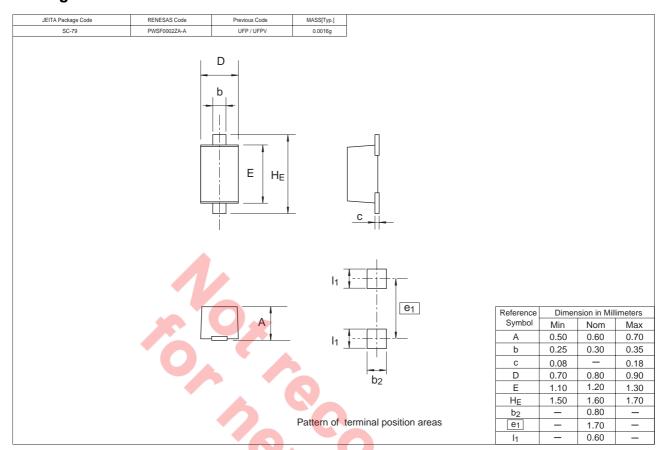
2. Calculate LF ($\triangle \log C / \triangle \log V$) at VR = 1 through 28V, f = 1 MHz.(Reference Value)



Main Characteristic



Package Dimensions



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