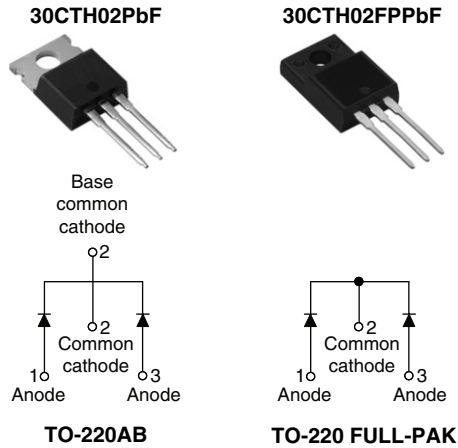


Hyperfast Rectifier, 2 x 15 A FRED Pt™



FEATURES

- Hyperfast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Fully isolated package ($V_{INS} = 2500 V_{RMS}$)
- Lead (Pb)-free ("PbF" suffix)
- TO-220 designed and qualified for AEC Q101 level
- TO-220FP designed and qualified for industrial level



RoHS*
COMPLIANT

DESCRIPTION/APPLICATIONS

200 V series are the state of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

PRODUCT SUMMARY	
t_{rr} (maximum)	30 ns
$I_{F(AV)}$	2 x 15 A
V_R	200 V

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V_{RRM}		200	V
Average rectified forward current	$I_{F(AV)}$	per diode $T_C = 159\text{ °C}$	15	A
		(FULL-PAK) per diode $T_C = 125\text{ °C}$	30	
		per device		
Non-repetitive peak surge current	I_{FSM}	$T_J = 25\text{ °C}$	200	
Operating junction and storage temperatures	T_J, T_{Stg}		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS ($T_J = 25\text{ °C}$ unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_R	$I_R = 100\ \mu A$	200	-	-	V
Forward voltage	V_F	$I_F = 15\text{ A}$	-	0.92	1.05	
		$I_F = 15\text{ A}, T_J = 125\text{ °C}$	-	0.78	0.85	
Reverse leakage current	I_R	$V_R = V_R$ rated	-	-	10	μA
		$T_J = 125\text{ °C}, V_R = V_R$ rated	-	5	300	
Junction capacitance	C_T	$V_R = 200\text{ V}$	-	57	-	pF
Series inductance	L_S	Measured lead to lead 5 mm from package body	-	8	-	nH

* Pb containing terminations are not RoHS compliant, exemptions may apply

30CTH02PbF/30CTH02FPPbF



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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Reverse recovery time	t _{rr}	I _F = 1 A, dI _F /dt = 50 A/μs, V _R = 30 V	-	-	35	ns	
		I _F = 1 A, dI _F /dt = 100 A/μs, V _R = 30 V	-	-	30		
		T _J = 25 °C	I _F = 15 A dI _F /dt = 200 A/μs V _R = 160 V	-	26		-
		T _J = 125 °C		-	40		-
Peak recovery current	I _{RRM}	T _J = 25 °C	-	2.8	-	A	
		T _J = 125 °C	-	6.0	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C	-	37	-	nC	
		T _J = 125 °C	-	120	-		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, _____ per diode junction to case (FULL-PAK) per diode	R _{thJC}	Mounting surface, flat, smooth and greased	-	-	1.1	°C/W
			-	-	3.5	
Marking device		Case style TO-220AB	30CTH02			
		Case style TO-220 FULL-PAK	30CTH02FP			

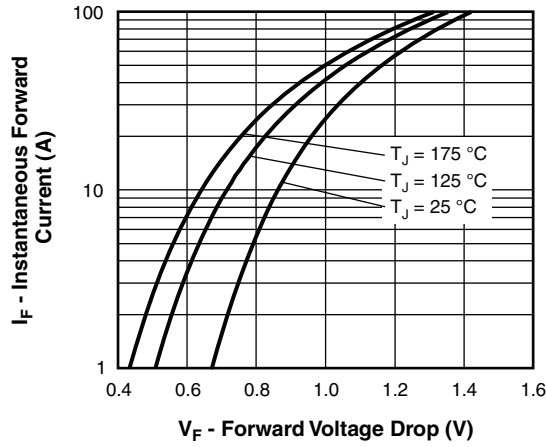


Fig. 1 - Typical Forward Voltage Drop Characteristics

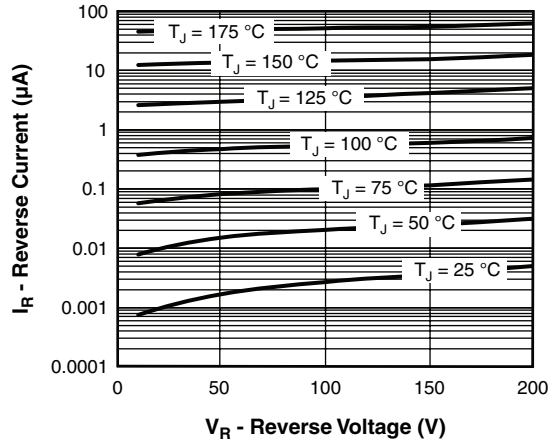


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

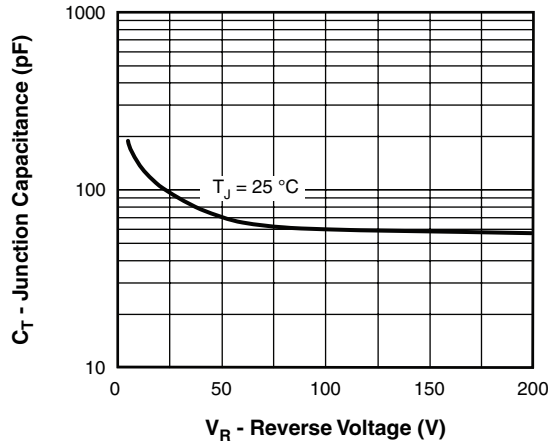


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

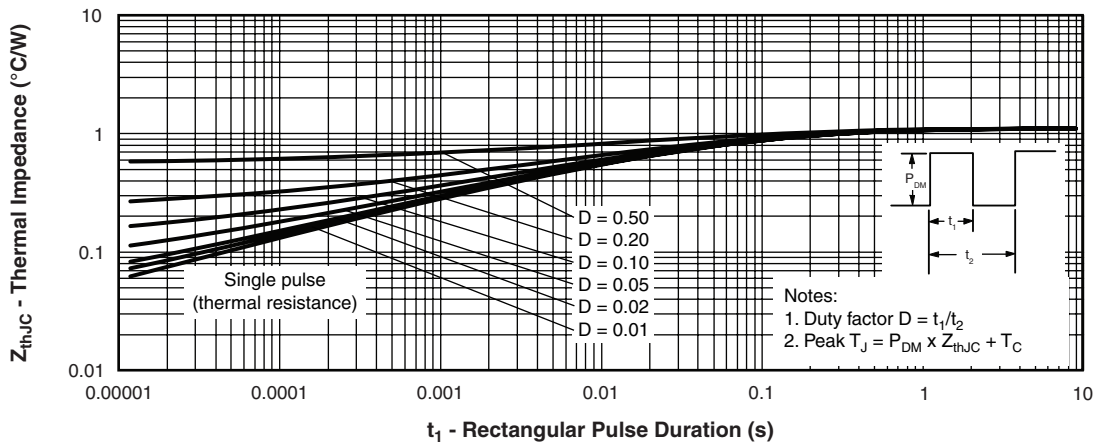


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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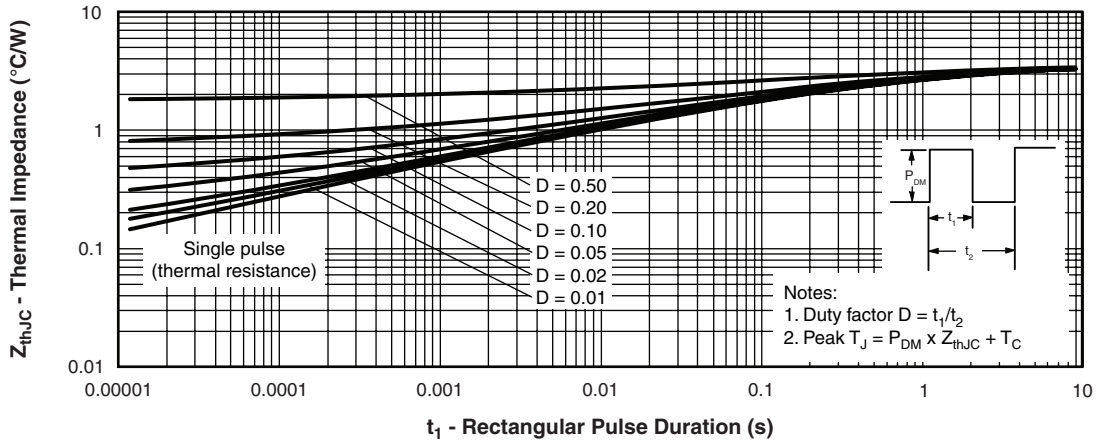


Fig. 5 - Maximum Thermal Impedance Z_{thJC} Characteristics (FULL-PAK)

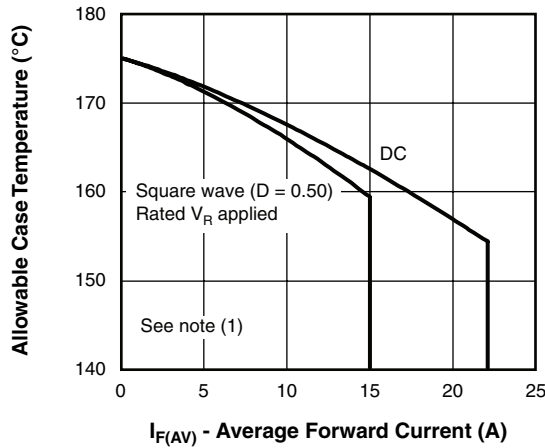


Fig. 6 - Maximum Allowable Case Temperature vs. Average Forward Current

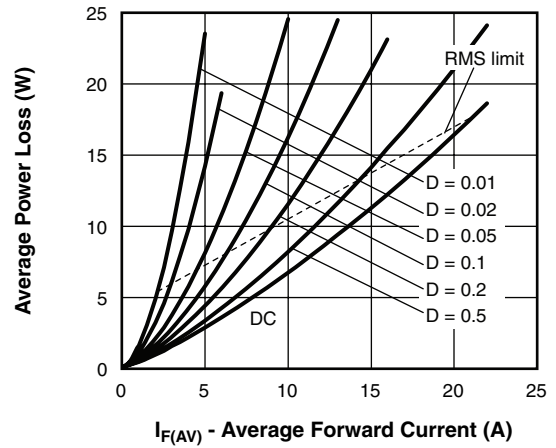


Fig. 8 - Forward Power Loss Characteristics

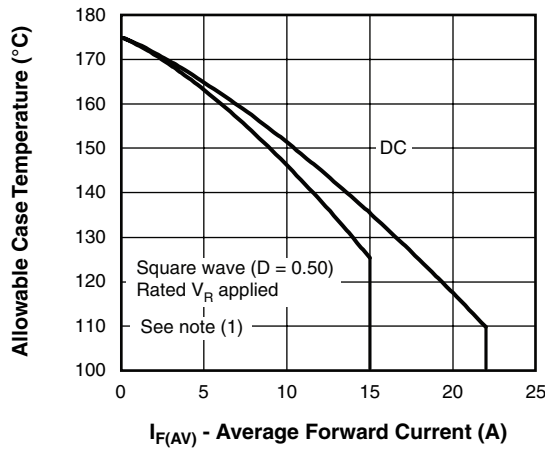


Fig. 7 - Maximum Allowable Case Temperature vs. Average Forward Current (FULL-PAK)

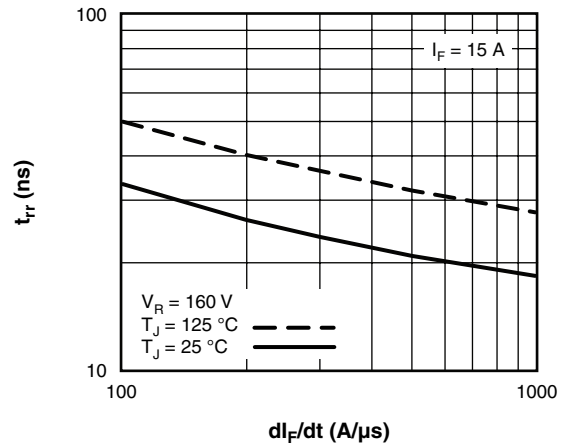


Fig. 9 - Typical Reverse Recovery Time vs. di_F/dt

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 8);
 P_{dREV} = Inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = Rated V_R

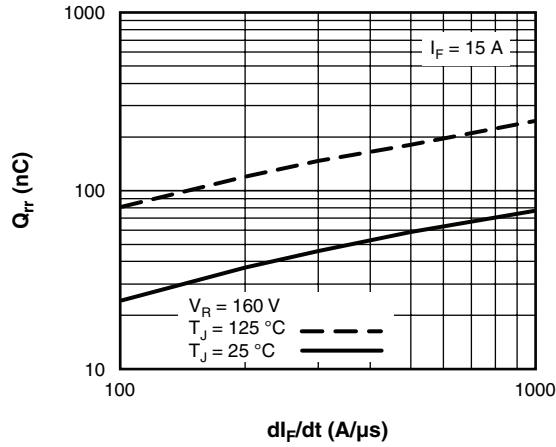


Fig. 10 - Typical Stored Charge vs. di/dt

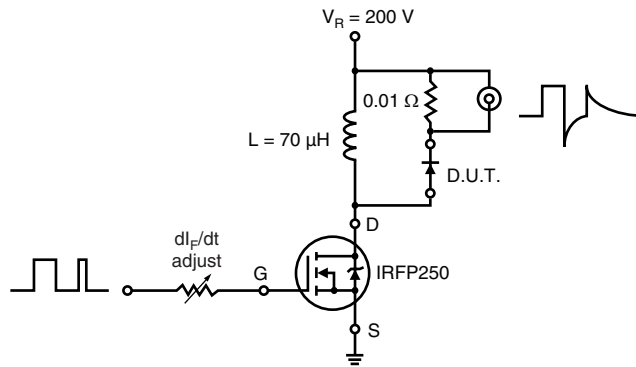
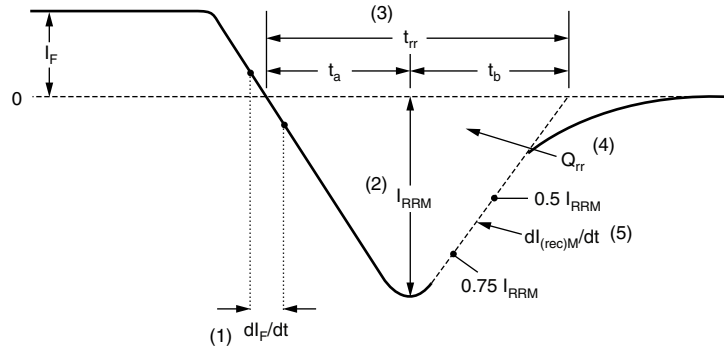


Fig. 11 - Reverse Recovery Parameter Test Circuit



(1) di/dt - rate of change of current through zero crossing

(2) I_{RRM} - peak reverse recovery current

(3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dl_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 12 - Reverse Recovery Waveform and Definitions

30CTH02PbF/30CTH02FPPbF



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ORDERING INFORMATION TABLE

Device code	30	C	T	H	02	FP	PbF
	①	②	③	④	⑤	⑥	⑦

- 1** - Current rating (30 = 30 A)
- 2** - C = Common cathode
- 3** - T = TO-220
- 4** - H = Hyperfast recovery
- 5** - Voltage rating (02 = 200 V)
- 6** -
 - None = TO-220AB
 - FP = TO-220 FULL-PAK
- 7** -
 - None = Standard production
 - PbF = Lead (Pb)-free

Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95040
Part marking information	http://www.vishay.com/doc?95042



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