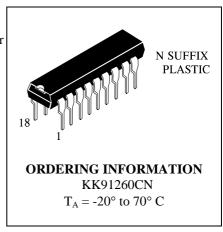


KK91260C

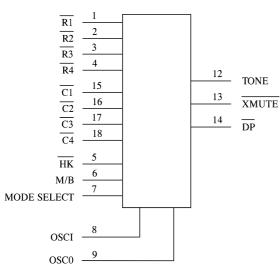
10 Memory Tone/Pulse Dialer **High-Performance Silicon-Gate CMOS**

The KK91260C is tone/pulse switchable dialer with ten 16-digit number memories and 32-digit redial memory. Pulse to tone mode switching is performed via a slide switch.

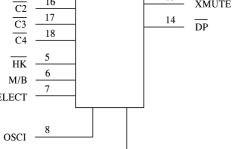
- 32-digit redial memory (31 digits in tone mode)
- Ten indirect memories, 16 digits in pulse mode, 15 digits in tone
- Tone/Pulse mode switching via slide switch (4.1 second pause inserted automatically)
- Wide operating voltage: $1.8 \text{ V} \sim 5.5 \text{ V}$
- Uses 480 KHz ceramic resonator
- Low memory retention current
- Selectable Make/Break ratio
- Dial Pulse Rate: 10 pps



PIN ASSIGNMENT



LOGIC DIAGRAM

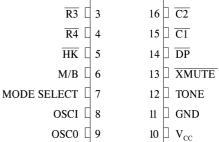


$$PIN 11 = GND$$

$$PIN 10 = V_{CC}$$

1	2	3	S	R1
4	5	6	A/L	$\overline{R2}$
7	8	9	P	R3
*	0	#	R	R4
	<u></u>			
C1	C2	C3	C4	

R1 □ 1 • 18 \[\overline{C4} R2 ☐ 2 17 🛚 C3



Keyboard Assignments*

S : Store

A/L : Auto/Location

: Pause R : Redial



Pin Description

Pin No.	Designation	Description		
1	R1	Keyinputs.		
2	R2	When a row and a column are connected, a key operation is activated.		
3	R3	Scanning signals are present on both the row and column pins during a valid key-in condition.		
4	R4	key-in condition.		
5	НК	Hook switch input. When $HK = V_{CC}$, an ON-Hook state exists. When $HK = GND$, an Off-Hook state exists.		
6	M/B	Dial pulse Make/Break ratio select input. If M/B = V _{CC} , the Make/Break ratio is 1/2. If M/B = GND, the Make/Break ratio is 2/3.		
7	MODE SELECT	$\label{eq:pulse} \begin{split} & \text{Pulse/DTMF mode select input.} \\ & \text{If MODE SELECT} = V_{CC} \text{ , Pulse mode is in effect.} \\ & \text{If MODE SELECT} = GND, DTMF mode is in effect.} \end{split}$		
8 9	OSCI OSC0	Oscillator I/O. A 480 KHz ceramic resonator and two 100 pF serial loading <u>cap</u> acitors form a complete oscillator circuit. The circuit is activated when HK is low. Oscillator start-up time is typically 10 ms. $C = 100 \text{ pF}$		
10	V_{CC}	Positive power supply.		
11	GND	Negative power supply.		
12	TONE	DTMF signal output. Pull-down load resistance is $10,000~\Omega$. The minimum tone and IDP durations are built-in for both normal dialing and redialing.		
13	XMUTE	Transmit mute output. This is an N-channel open drain output. The output transistor is switched on while a sequence of digits is being dialed (for both Pulse and Tone modes). Otherwise, it is switched off.		
14	DP	Dial pulse output. This pin is an N-channel open drain output. When DP output is low (switched on), it serves as a break signal in Pulse dialing. For other operations, DP output is normally high impedance (switched off).		
15	C1	Keyinputs.		
16	C2	When a row and a column are connected, a key operation is activated. Scanning signals are present on both the row and column pins during a valid key-in condition.		
17	C3			
18	C4			



Operation Procedures

Symbol Definitions:

a. D_t : tone digit, 1, 2, 3, 4, 5, 6, 7, 8, 9, 0, *, # D_P : Pulse digit, 1, 2, 3, 4, 5, 6, 7, 8, 9, 0

b. LOCi: i = 1, 2, 3, 4, 5, 6, 7, 8, 9, 0

c. ZiZiZi: Conversation mode

d. 0 - 0 ↑ : OFF-HOOK

e. $0 - 0 \downarrow$: ON-HOOK

f. _____ : Input Level from Low to High

g. 🔻 : Input Level from High to Low

Recommended dialing, redialing, mixed dialing and storing operations:

1. Normal dialing in pulse mode $0 - 0 \uparrow$, $D_P \dots D_P$, ZiZiZi $0 - 0 \downarrow$

2. Normal dialing in tone mode $0 - 0 \uparrow$, $D_t \dots D_t$, ZiZiZi $0 - 0 \downarrow$

3. Mixed dialing in pulse-to-tone mode 0 - 0 ↑, D_P...D_P, MODE SELECT

→ , D₁...D_t, ZiZiZi 0 - 0 ↓

4. Redialing

$$0 - 0 \uparrow$$
, $D_P \dots D_P$ $0 - 0 \downarrow$, $0 - 0 \uparrow$, R, ZiZiZi, $0 - 0 \downarrow$
 $0 - 0 \uparrow$, $D_t \dots D_t$ $0 - 0 \downarrow$, $0 - 0 \uparrow$, R, ZiZiZi, $0 - 0 \downarrow$

- 5. (ii) On/Off-hook store: $0 0 \downarrow$, S , $D_P \dots D_P$ or $D_t \dots D_t$, A/L , LOCi
- 6. Dialing from Repertory Memory 0 0 ↑, A/L , LOCi, ZiZiZi 0 0 ↓

Functional Description

1. N-Channel Open Drain Output - DP, XMUTE

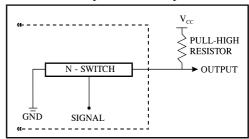


Figure 1.

2. DTMF Generator

The digitally synthesized sinewave of the IN91260 series is well designed, with a 6 level, 12 segment, $1/2~V_{CC}$ reference voltage. The THD (Total Harmonic Distortion) of the DTMF output is typically 1%, when $V_{CC} = 2.5~V$ to 5.5~V and frequency is in the 500 HZ to 3400 HZ

MAXIMUM RATINGS*

Symbol	Parameter	Value	Unit
VCC	DC Supply Voltage (Referenced to GND)	-0.3 to +6.0	V
VIN	DC Input Voltage (Referenced to GND)	-0.3 to VCC +0.3	V
VOUT	DC Output Voltage (Referenced to GND)	-0.3 to VCC +0.3	V
VOUT	DC Output Voltage (DP, XMUTE)	-0.3 to 1.2	V
ITONE	DC Output Current(Tone)	50	mA
P_{D}	Power Dissipation in Still Air, Plastic DIP**	500	mW
Tstg	Storage Temperature	-40 to +125	°C



RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage (Referenced to GND)	1.8	5.5	V
$V_{\rm IN}$	DC Input Voltage (Referenced to GND)	0	V_{CC}	V
V_{OUT}	DC Output Voltage (Referenced to GND)	0	V_{CC}	V
T_A	Operating Temperature	-20	+70	°C

This device contains protection circuitry to guard against damage due to high static voltages or electric fields. However, precautions must be taken to avoid applications of any voltage higher than maximum rated voltages to this high-impedance circuit. For proper operation, V_{IN} and V_{OUT} should be constrained to the range $GND \le (V_{IN})$ or $V_{OUT} \le V_{CC}$.

Unused inputs must always be tied to an appropriate logic voltage level (e.g., either GND or V_{CC}). Unused outputs must be left open.

$\label{eq:control_control_control} \textbf{DC ELECTRICAL CHARACTERISTICS} \text{ (Voltages Referenced to GND, } V_{CC} = 1.8 \text{ V to } 5.5 \text{ V,}$

 $T_A = -20 \text{ to } +70^{\circ}\text{C}$

= -20 to +70					anteed nits	
Symbol	Parameter	Test	Conditions	Min	Max	Unit
V_{IH}	High-Level Input Voltage			$0.8V_{\rm CC}$	V _{CC}	V
V _{IL}	Low-Level Input Voltage			0	$0.2V_{CC}$	V
V _{MR}	Minimum Memory Retention Voltage	$V_{CC} = 1.0 \text{ V}$		1.5		V
	Single Column Tone	$R_{LOAD} = 10 \text{ K}$	$\Omega V_{CC} = 2.5 V$	540	640	
V _{OC} *	Output Amplitude	$R_{LOAD} = 10 \text{ K}$	$\Omega V_{\rm CC} = 5.5 \text{ V}$	100	1300	mVp-p
1 7 4	Single Row Tone	$R_{LOAD} = 10 \text{ K}$	Ω V _{CC} = 2.5 V	410	490	mVp-p
V _{OR} *	Output Amplitude	$R_{LOAD} = 10 \text{ K}\Omega \text{ V}_{CC} = 5.5 \text{ V}$		850	990	т, ь ь
I _C *	Column Input Current	V _{IN} =3.5 V All outputs unloaded		0.6	2.0	mA
I _R *	Row Input Current	V _{IN} = 0 V All outputs unloaded		5	50	μΑ
I_{OL1}	Minimum Sink Output	$V_{CC} = 3.5 \text{ V}, \ V_{OL} = 0.4 \text{ V}$ $V_{CC} = 2.0 \text{ V}, \ V_{OL} = 0.4 \text{ V}$		0.7		mA
I_{OL2}	Current DP, XMUTE			0.2		mA
I_{MR}	Maximum Memory Retention Current	V _{CC} = 1.0 V, HK = V _{CC} All outputs unloaded			0.6	μΑ
T		V _{CC} =3.5 V	Oscillator running;		0.7	
I_{CCP}	Maximum Supply	V _{CC} =5.5 V	all outputs		2.1	mA
I_{CCT}	Operation Current	V _{CC} =3.5 V	unloaded, $V_{IL} = 0$,		1.3	
1CCT		V _{CC} =5.5 V	$V_{IH} = V_{CC}$		3.9	mA
I_{SD}	Maximum Standby Current	V _{CC} = 2.5 V, HK = GND All outputs unloaded			7.0	μΑ
T_{WIST}	Preemphasis	$V_{CC} = 3.5 \text{ V}$		1	3	dB
V_{V}	Valley of Single Tone	$V_{CC} = 3.5 \text{ V}$		0.3	6.0	V_{DD}
DIS%	Distortion	*Note 1, V _{CC} = 3.5 V			6.0	%

^{* @ 25 °}C



Note 1:

DIS% =
$$\frac{100.(V_1^2 + V_2^2 + ... + V_n^2)}{(V_{OC}^2 + V_{OR}^2)^{1/2}}$$

a. $V_1 \dots V_n$ are the intermodulation or the harmonic frequencies in the 500 Hz to 3400 Hz band.

b. V_{OC} and V_{OR} are the individual frequency components of the DTMF signal.

AC ELECTRICAL CHARACTERISTICS(All Voltages referenced to GND. $V_{CC} = 1.8 \text{ V}$ to 5.5 V, $T_A = -20 \text{ to } +70^{\circ}\text{C}$, $F_{OSK} = 480 \text{ KHz}$)

70 C, 1 OSK			Guarant	eed Limits		
Symbol	Parameter	Test Conditions	Min	Max	Unit	
PULSE						
T_{M}	Make Time (Figure 2)	M/B = 1/2	30	35	ms	
1 M	(M/B = 2/3	38	42		
T_{B}	Break Time (Figure 2)	M/B = 1/2	64	69	ms	
1 B	Dicak Time (11gure 2)	M/B = 2/3	57	63	1113	
T_{IDP}		M/B = 1/2	790	815	ms	
1 IDb	Inter Digit Pause Time (Figure 2)	M/B = 2/3	790	815		
T_{PDP}	Pre-Digit Pause (Figure 2)	M/B = 1/2	820	860	ms	
1 PDP	rie-Digit rause (Figure 2)	M/B = 2/3	820	860		
T_{MDP}	XMUTE, Delay Time (Figure 2)	M/B = 1/2	0	3	ms	
1 MDP	111120 12, 2011, 111110 (11,9110 2)	M/B = 2/3	0	3		
TONE						
T_{MFD}	Minimum Tone Duration (Figure 3)		100	120	ms	
T_{TIDP}	Minimum Tone Inter-Digit Pause (XMUTE) (Figure 3)		100	120	ms	
T_{TPDP}	Tone Output Pre-Digit Pause (Figure 3)		115	145	ms	
T_{MDT}	XMUTE Delay Time (Figure 3)		100	120	ms	
T_{AP}	Auto Pause Time (Figure 4)		3.9	4.3	s	
T_{START}	Oscillator Start-up Time (Figure 2)		4	13	ms	
T_{MS}	XMUTE Start-up Time (Figure 2)		25	35	ms	
KEY-IN						
T_{KD}	Keypad Debounce Time (Figure 2)		19	25	ms	
F_{KS}	Key Scan Frequency	C1 ~ C4, R1~ R4	365	390	Hz	



R/C	Spec.	Actual	Error(%)	Unit	Conditions
R1	697	695.65	-0.19	Hz	
R2	770	769.23	-0.1	Hz	
R3	852	851.06	-0.11	Hz	
R4	941	941.18	+0.02	Hz	$F_{OSC} = 480 \text{ KHz}$
C1	1,209	1,212.12	+0.26	Hz	
C2	1,336	1,333.33	-0.2	Hz	
C3	1,477	1,481.48	+0.3	Hz	

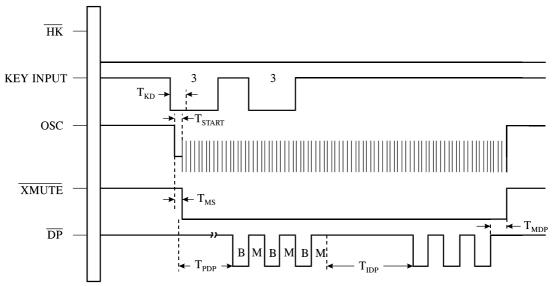


Figure 2. Pulse Mode

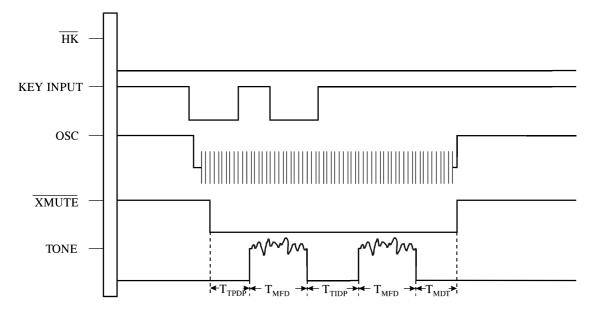


Figure 3. Tone Mode



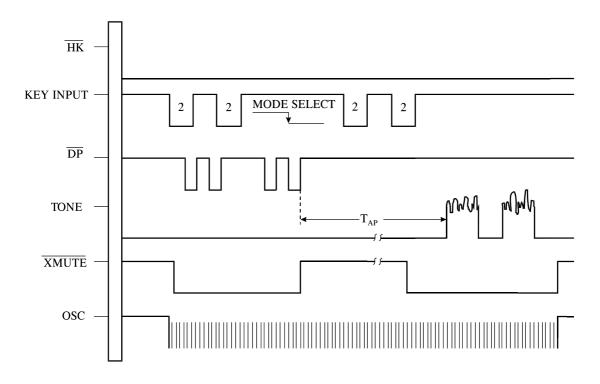
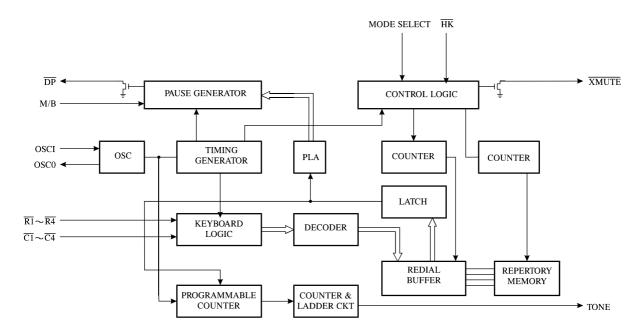


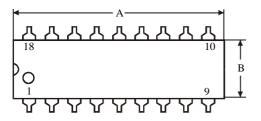
Figure 4. Normal Pulse to Tone mixed dialing via slide switch

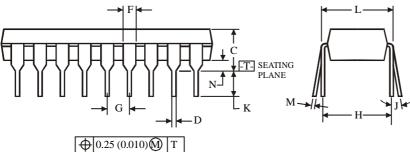
EXPANDED LOGIC DIAGRAM





N SUFFIX PLASTIC DIP (MS - 001AC)





NOTES:

Dimensions "A", "B" do not include mold flash or protrusions.
 Maximum mold flash or protrusions 0.25 mm (0.010) per side.

18

1					
	Dimension, mm				
Symbol	MIN	MAX			
A	22.35	23.37			
В	6.1	7.11			
C		5.33			
D	0.36	0.56			
F	1.14	1.78			
G	2.54				
H	7.	62			
J	0°	10°			
K	2.92	3.81			
L	7.62	8.26			
M	0.2	0.36			
N	0.38				