

Silicon NPN Power Transistors

2SC3756

DESCRIPTION

- With TO-3PML package
- High speed
- High breakdown voltage
- High reliability

APPLICATIONS

- For TV horizontal deflection output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter

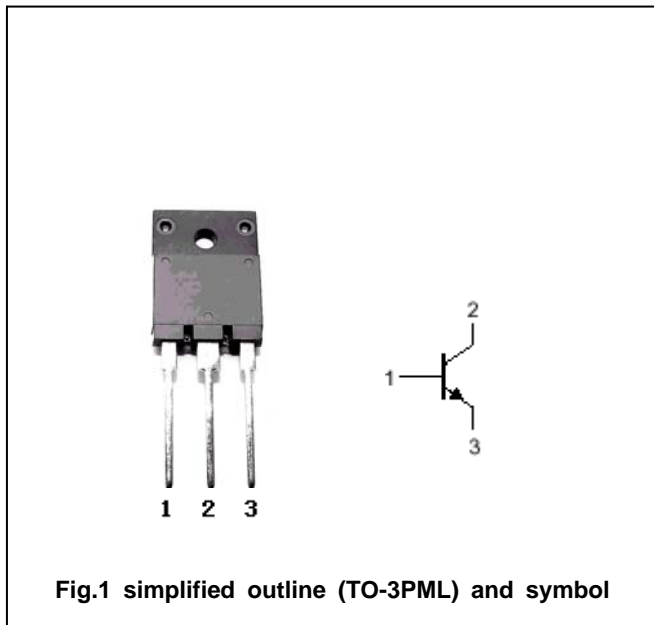


Fig.1 simplified outline (TO-3PML) and symbol

Absolute maximum ratings (Ta=25°C)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
V_{CBO}	Collector-base voltage	Open emitter	1500	V
V_{CEO}	Collector-emitter voltage	Open base	800	V
V_{EBO}	Emitter-base voltage	Open collector	6	V
I_C	Collector current		6	A
P_C	Collector dissipation	Ta=25°C	3.0	W
		Tc=25°C	60	
T_j	Junction temperature		150	°C
T_{stg}	Storage temperature		-55~150	°C

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CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =0.1A ; I _B =0	800			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =4A ; I _B =1A			5.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =4A ; I _B =1A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =800V ; I _E =0			10	μ A
I _{CES}	Collector cut-off current	V _{CE} =1500V ; R _{BE} =0			1.0	mA
I _{EBO}	Emitter cut-off current	V _{EB} =4V ; I _C =0			10	μ A
h _{FE-1}	DC current gain	I _C =1A ; V _{CE} =5V	8		36	
h _{FE-2}	DC current gain	I _C =4A ; V _{CE} =5V	4			

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PACKAGE OUTLINE

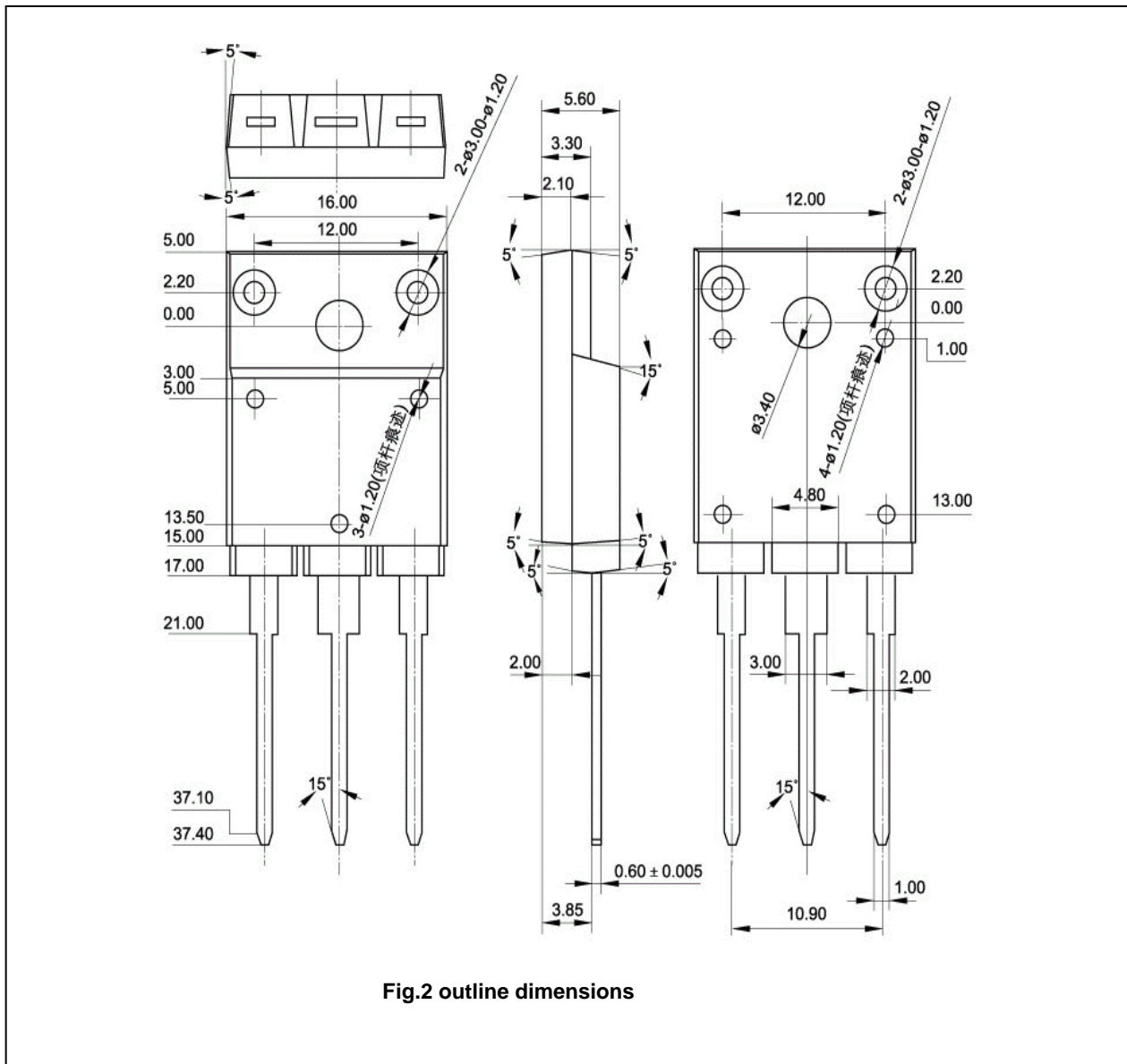


Fig.2 outline dimensions