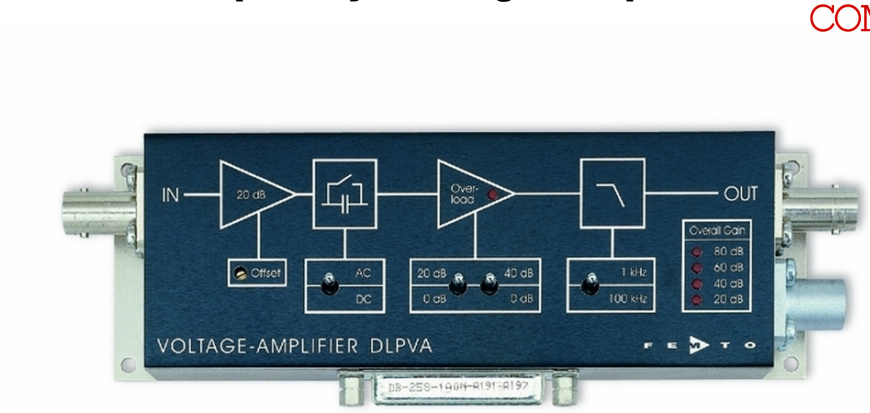
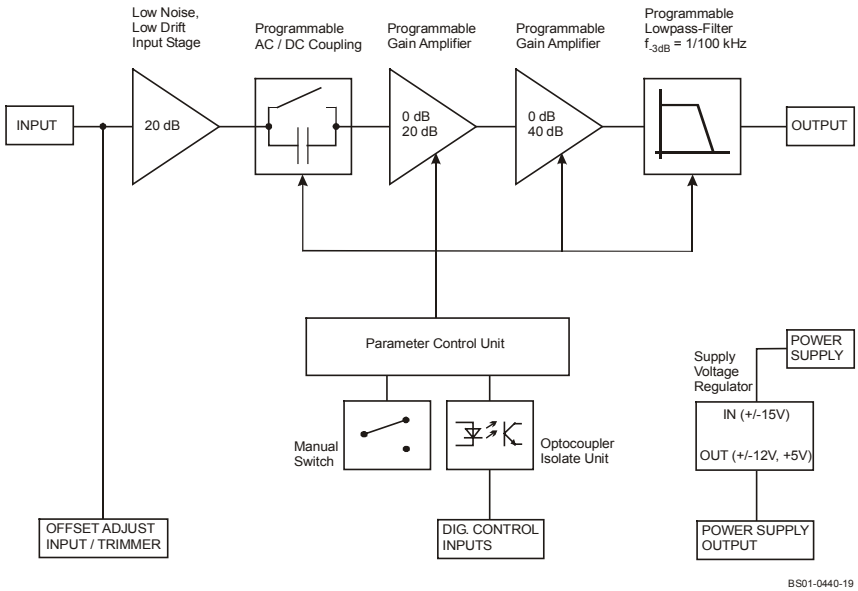
Variable Gain
Low Frequency Voltage Amplifier

LASER COMPONENTS



Features	• **Variable Gain 20 to 80 dB, Switchable in 20 dB Steps** • **FET Input Stage, 1 TΩ Impedance** • **Protection against ± 3 kV Transients** • **Single Ended and True Differential Input Models** • **Bandwidth DC - 100 kHz, Switchable to 1 kHz** • **1.3 µV/°C DC-Drift** • **120 dB CMRR** • **5.5 nV/√Hz Input Noise** • **Switchable AC/DC-Coupling** • **Local and Remote Control**
Applications	• **Universal Laboratory Amplifier** • **Automated Measurements** • **Industrial Sensors** • **Detector Preamplifier** • **Integrated Measurement Systems**



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Specifications

Test Conditions Vs = ±15 V, Ta = 25°C

Gain

Gain Values	20, 40, 60, 80 dB indicated by four LEDs	
Gain Accuracy	± 0.1 %	(between settings)
	± 1 %	(overall)
Gain Flatness	± 0.1 dB	

Frequency Response

Lower Cut-Off Frequency	DC, switchable to 1.5 Hz
Upper Cut-Off Frequency	100 kHz, switchable to 1 kHz
Upper Cut-Off Frequency Rolloff	12 dB/Oct.

Time Response

Rise / Fall Time (10% - 90%)	3.5 µs (@ BW = 100 kHz) 350 µs (@ BW = 1 kHz)

Input

Input Impedance	1 TΩ		
Input Voltage Drift	1.3 µV/K		
Equivalent Input Voltage Noise	Gain Setting	DLPVA-100-F–S	DLPVA-100-F–D
	60, 80 dB	5.5 nV/√Hz	6.9 nV/√Hz
	40 dB	8 nV/√Hz	10 nV/√Hz
	20 dB	60 nV/√Hz	60 nV/√Hz
Equivalent Input Current Noise	1.6 fA/√Hz		
1/f-Noise Corner	80 Hz		
Input Bias Current	1 pA		
Input Bias Current Drift	Factor 2.3 / 10 °C		
Input Offset Voltage	± 5 mV, adjustable by offset trimmer and external control voltage		

Single Ended Input, Model "DLPVA-100-F-S" only:

Input Voltage Range for linear Amplification: ± 0.6 V

True Differential Input, Model "DLPVA-100-F-D" only:

Common Mode Voltage Range	± 5 V	
CMRR	120 dB	(@ 100 Hz)
	100 dB	(@ 10 kHz)
	80 dB	(@ 60 kHz)

Output

Output Impedance	50 Ω (terminate with > 10 kΩ load for best performance)
Output Voltage Range	
For Linear Amplification	± 10 V (@ > 10 kΩ load)
Output Current (max.)	± 20 mA
Output Overload Recovery Time	0.5 ms (after 20x overload)

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Overload LED	The amplifier features a LED to signalize an overload condition. The Overload LED will turn on if the signal level within the signal path exceeds the linear operating range. In order to ensure the correct operation of the amplifier without signal distortions reduce the gain setting until the Overload LED turns off. The Overload LED may also turn on under the following operating conditions: - The amplifier is operated with open input or with a high source impedance. For proper operation please use a source impedance of less than 100 MΩ or switch to a lower gain setting. - When using a DLPVA-F-D with differential input stage the Overload LED may turn on if the common mode input voltage exceeds ± 5 V or if the source is totally floating with respect to the amplifier ground. For proper operation make sure that the common mode voltage stays within ± 5 V with respect to the amplifier ground and make a valid connection between the source ground and the amplifier ground to ensure that the inputs cannot drift outside the tolerable common mode range.	
Remote Offset Control	Offset Control Voltage Range	± 10 V, corresponds to ± 5 mV input offset
	Offset Control Input Impedance	200 kΩ
Remote Digital Control	Control Input Voltage Range	Low: - 0.8 ...+ 0.8 V High: + 1.8 ... + 12 V, TTL / CMOS compatible
	Control Input Current	0 mA @ 0 V, 1.5 mA @ + 5 V, 4.5 mA @ + 12 V
	Overload Output	Non active: + 5 V, max. 1 mA, active: 0.8 V, max. -10 mA
Power Supply	Supply Voltage	± 15 V (± 14.5 V to ± 16 V)
	Supply Current	± 75 mA typ. (depends on operating conditions, recommended power supply capability minimum 150 mA)
Case	Weight	0.32 kg (0.7 lbs)
	Material	AlMg4.5Mn, nickel-plated
Temperature Range	Storage Temperature	- 40 °C to + 100 °C
	Operating Temperature	0 °C to + 60 °C
Absolute Maximum Ratings	Power Supply Voltage	± 21 V
	Control Input Voltage	+ 16 V / - 5 V
	Signal Input Voltage	± 15 Vp
	Transient Input Voltage	± 3 kV (discharge from 5 nF source)

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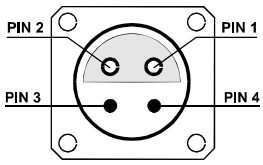
Connectors

Input

Single Ended Input, Model "DLPVA-100-F-S":
BNC

True Differential Input, Model "DLPVA-100-F-D":
LEMO series 1S, 4-pin fixed socket
Pin 1: non inverting input
Pin 2: inverting input
Pin 3: GND
Pin 4: N.C.

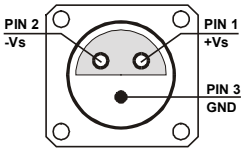


Output

BNC

Power Supply

LEMO series 1S, 3-pin fixed socket
Pin 1: + 15V
Pin 2: - 15V
Pin 3: GND



Control Port

Sub-D 25-pin, female, qual. class 2
Pin 1: +12 V (stabilized power supply output, max. 100 mA)
Pin 2: -12 V (stabilized power supply output, max. 100 mA)
Pin 3: AGND (analog ground)
Pin 4: +5 V (stabilized power supply output, max. 50 mA)
Pin 5: digital output: overload
Pin 6: NC
Pin 7: NC
Pin 8: offset control voltage input
Pin 9: DGND (ground f. digital control Pin 10 - 25)
Pin 10: NC
Pin 11: digital control input: gain, LSB
Pin 12: digital control input: gain, MSB
Pin 13: digital control input: AC/DC
Pin 14: digital control input: 100 kHz / 1 kHz
Pin 15 - 25: NC

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Remote Control Operation

General

Remote control input bits are opto-isolated and connected by logical OR to local switch setting. For remote control a switch setting, set the corresponding local switch to "0 dB", "AC" and "1 kHz" and select the wanted setting via a bit-code at the corresponding digital inputs. Mixed operation, e.g. local gain setting and remote controlled bandwidth setting, is also possible.

Gain Setting

Gain	Pin 11	Pin 12
20 dB	low	low
40 dB	high	low
60 dB	low	high
80 dB	high	high

AC/DC Setting

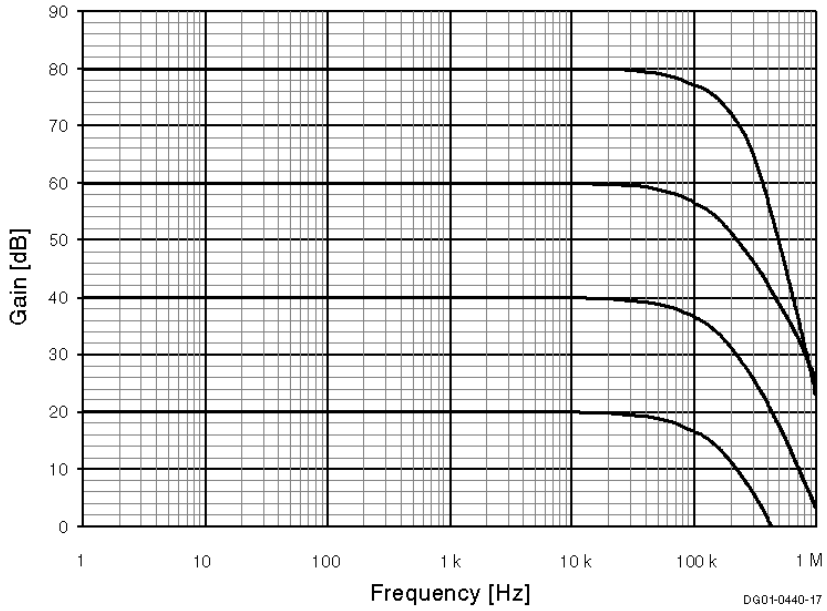
Coupling	Pin 13
AC	low
DC	high

Bandwidth Setting

Bandwidth	Pin 14
1 kHz	low
100 kHz	high

Typical Performance Characteristics

Frequency Response (Logarithmic)



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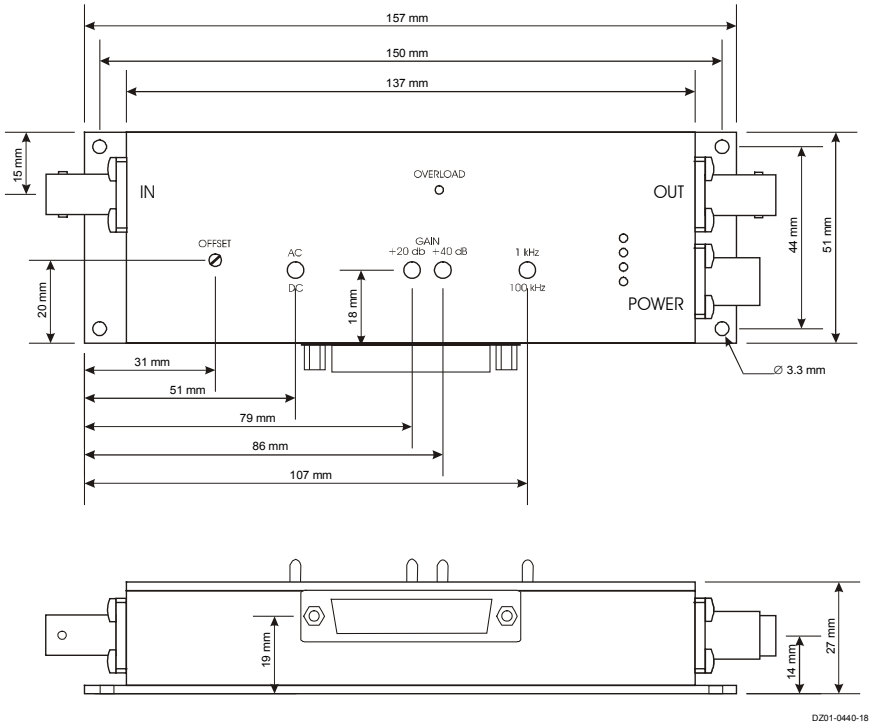
Dimensions



Ordering Information

Available Models

Model No.: DLPVA-100-F-S
- FET, singe-ended input (BNC-connector input)

Model No.: DLPVA-100-F-D
- FET, true differential input (LEMO-connector input)