

## Silicon NPN Power Transistors

2SC1828

## DESCRIPTION

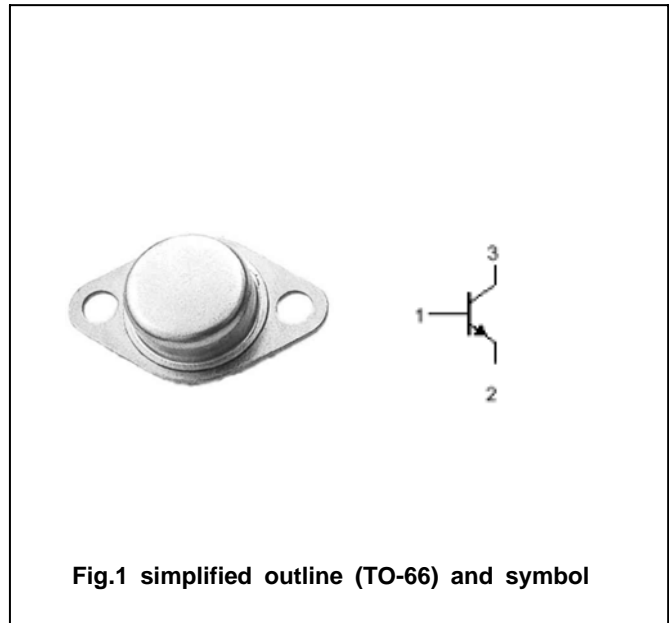
- With TO-66 package
- Continuous collector current- $I_C=1A$
- Power dissipation  $-P_C=40W @T_C=25^\circ C$

## APPLICATIONS

- For power amplifier applications

## PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

Absolute maximum ratings( $T_a=25^\circ C$ )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
$V_{CBO}$	Collector-base voltage	Open emitter	800	V
$V_{CEO}$	Collector-emitter voltage	Open base	400	V
$V_{EBO}$	Emitter-base voltage	Open collector	6	V
$I_C$	Collector current		1	A
$P_T$	Total power dissipation	$T_C=25^\circ C$	40	W
$T_j$	Junction temperature		150	$^\circ C$
$T_{stg}$	Storage temperature		-55~150	$^\circ C$

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## CHARACTERISTICS

T<sub>j</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>CEO(SUS)</sub>	Collector-emitter sustaining voltage	I <sub>C</sub> =100mA ; I <sub>B</sub> =0	400			V
V <sub>(BR)CBO</sub>	Collector-base breakdown voltage	I <sub>C</sub> =1mA ; I <sub>E</sub> =0	800			V
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =1mA ; I <sub>C</sub> =0	6			V
V <sub>CEsat</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =1A; I <sub>B</sub> =0.2A			1.0	V
V <sub>BEsat</sub>	Base-emitter saturation voltage	I <sub>C</sub> =1A; I <sub>B</sub> =0.2A			1.5	V
I <sub>CBO</sub>	Collector cut-off current	V <sub>CB</sub> =800V; I <sub>E</sub> =0			0.1	mA
I <sub>EBO</sub>	Emitter cut-off current	V <sub>EB</sub> =6V; I <sub>C</sub> =0			0.1	mA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> =0.2A ; V <sub>CE</sub> =10V	30		200	
f <sub>T</sub>	Transition frequency	I <sub>C</sub> =0.1A ; V <sub>CE</sub> =10V		10		MHz

PACKAGE OUTLINE

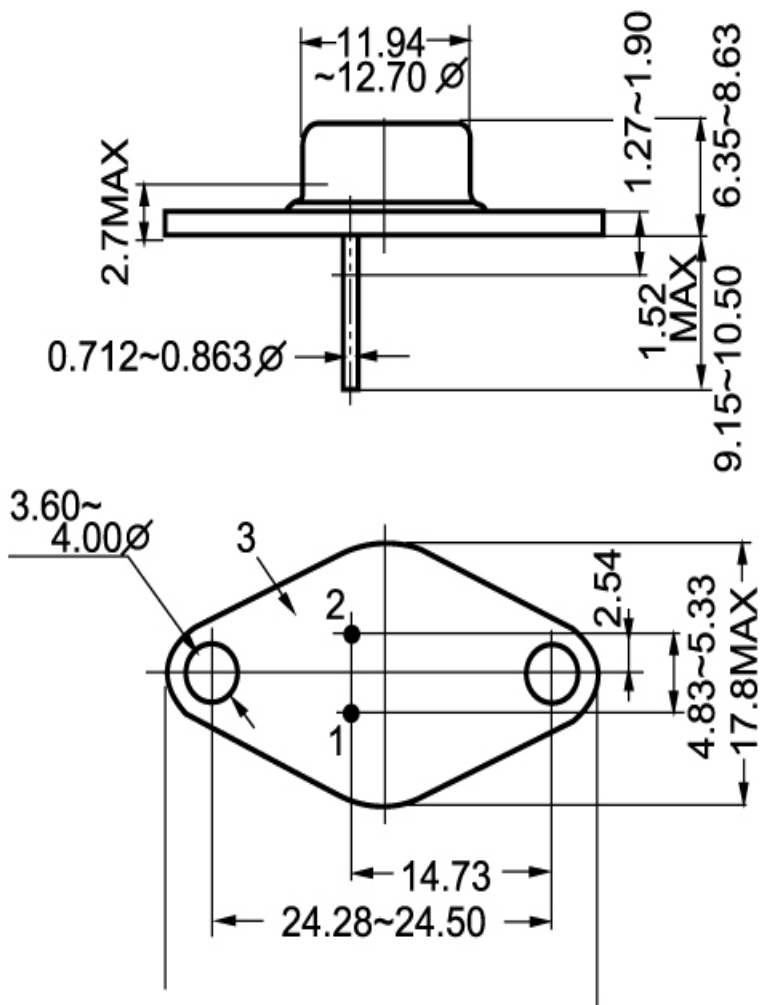


Fig.2 Outline dimensions