

- Preliminary Datasheet -

## 1W Single-Bias and Prematched GaAs Power PHEMTs using SMT package

### FEATURES

- Prematched for 5~10 GHz
- 1W Typical Output Power at 5~10 GHz
- 6.5dB Typical Linear Power Gain at 10 GHz
- High Linearity: IP3 = 40 dBm Typical at 5~10 GHz
- High Power Added Efficiency: Nominal PAE of 35% at 5~10 GHz
- Breakdown Voltage:  $BV_{DGO} \geq 15V$
- $W_g = 2.4 \text{ mm}$
- 100 % DC Tested
- Suitable for High Reliability Application
- Lost Cost SMT Ceramic Package

### PHOTO ENLARGEMENT



### DESCRIPTION

The TC3953A is a single-bias and prematched GaAs PHEMT. It is designed for use in low cost, high volume, and 5~10 GHz 1W amplifiers. It provides a typical gain of 6.5 dB and P1dB of 30dBm at 10 GHz. The single positive drain bias is 9V and the typical drain-source current is 300mA. The device is packaged in copper based ceramic 10 pins SMT packages. The copper based carrier of the package allows direct soldering of the device to the PCB.

### ELECTRICAL SPECIFICATIONS ( $T_A=25^\circ\text{C}$ )

Symbol	CONDITIONS	MIN	TYP	MAX	UNIT
$P_{1dB}$	Output Power at 1dB Gain Compression Point, $f = 10 \text{ GHz}$ $V_{DS} = 9V$	29	30		dBm
$G_L$	Linear Power Gain, $f = 10 \text{ GHz}$ $V_{DS} = 9V$	5.5	6.5		dB
IP3	Intercept Point of the 3 <sup>rd</sup> -order Intermodulation, $f = 10 \text{ GHz}$ $V_{DS} = 9V$ , $*P_{SCL} = 17 \text{ dBm}$		40		dBm
PAE	Power Added Efficiency at 1dB Compression Power, $f = 10 \text{ GHz}$		35		%
$I_{DS}$	Drain-Source Current at $V_{DS} = 9V$		300		mA
$BV_{DGO}$	Drain-Gate Breakdown Voltage at $I_{DGO} = 1.2\text{mA}$	15	18		Volts

**Note:**  $*P_{SCL}$ : Output Power of Single Carrier Level.

**ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25 °C)**

Symbol	Parameter	Rating
V <sub>DS</sub>	Drain-Source Voltage	12 V
P <sub>in</sub>	RF Input Power, CW	28 dBm
P <sub>T</sub>	Continuous Dissipation	3.8 W
T <sub>CH</sub>	Channel Temperature	175 °C
T <sub>STG</sub>	Storage Temperature	- 65 °C to +175 °C

**RECOMMENDED OPERATING CONDITION**

Symbol	Parameter	Rating
V <sub>DS</sub>	Drain to Source Voltage	9V

**HANDLING PRECAUTIONS :**

The user must operate in a clean, dry environment. Electrostatic Discharge (ESD) precautions should be observed at all stages of storage, handling, assembly, and testing. The static discharge must be less than 300V.

**EVALUATION BOARD**

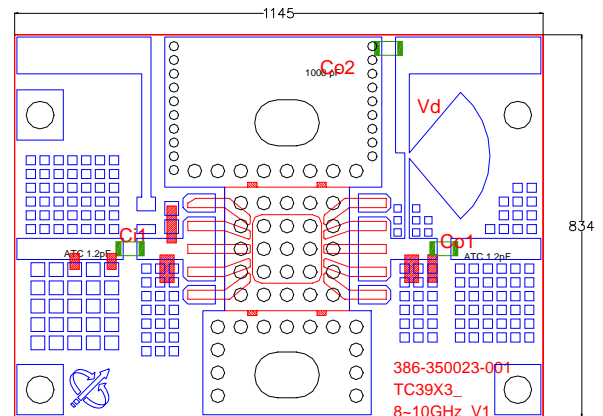
**PCB Material: RO4003**

**ER = 3.38**

**Thickness = 20 mil**

**Unit : mil**

- DXF file of the PCB can be downloaded from our Web site at [www.transcominc.com.tw](http://www.transcominc.com.tw)



Q'ty	Description	Reference Designator	Manufacturer	Inventory ID
1	Chip CAP(0603)1000PF±10%		Murata	GRM39X7R102K50V
2	Chip Cap (0805) 1.2PF±0.1PF		ATC	ATC 600F 1R2BT (1.2pF±0.1pF 250WVDC)