



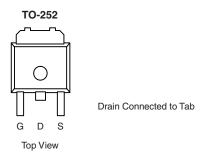
P-Channel 30-V (D-S), MOSFET

PRODUC	PRODUCT SUMMARY			
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a		
- 30	0.010 at V _{GS} = - 10 V	- 15		
- 30	0.018 at V _{GS} = - 4.5 V	- 12		

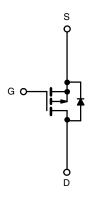
FEATURES

• TrenchFET® Power MOSFETs





Ordering Information: SUD45P03-10-E3 (Lead (Pb)-free)



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS $T_A = 2$	5 °C, unless other	wise noted			
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 30	V	
Gate-Source Voltage		V _{GS}	± 20	V	
Ocation and David Ocation	T _A = 25 °C	1-	- 15		
Continuous Drain Current ^b	T _A = 100 °C	l _D	- 8	^	
Pulsed Drain Current		I _{DM}	- 100	A	
Continuous Source Current (Diode Conduction)		I _S	- 15		
Mariana Branco Birata di Jah	T _C = 25 °C	P _D	70	w	
Maximum Power Dissipation ^b	T _A = 25 °C		4 ^b		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^b	R _{thJA}		30	°C/W	
Maximum Junction-to-Case	R _{thJC}		1.8] 0/11	

Notes

- a. Calculated Rating for $T_A = 25$ °C, for comparison purposes only. This cannot be used as continuous rating (see Absolute Maximum Ratings and Typical Characteristics).
- b. Surface Mounted on FR4 board, $t \le 10 \text{ s.}$

SUD45P03-10

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.0		- 3.0	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current		V _{DS} = - 30 V, V _{GS} = 0 V			- 1	4
	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 125 °C			- 50	μΑ
On Otata Dunin Communità	1	V _{DS} = - 5 V, V _{GS} = - 10 V	- 50			Α
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 4.5 V	- 20			
		V _{GS} = - 10 V, I _D = - 15 A			0.010	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 15 A, T _J = 125 °C			0.015	
		V _{GS} = - 4.5 V, I _D = - 15 A			0.018	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 15 V, I _D = - 15 A	20			S
Dynamic ^b						
Input Capacitance	C _{iss}			6000		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = - 25 V, f = 1 MHz		1100		
Reverse Transfer Capacitance	C _{rss}			700		
Total Gate Charge ^c	Q_g			90	150	
Gate-Source Charge ^c	Q_{gs}	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 45 A		20		nC
Gate-Drain Charge ^c	Q_{gd}			16		
Turn-On Delay Time ^c	t _{d(on)}			15	25	
Rise Time ^c	t _r	V_{DD} = - 15 V, R_L = 0.33 Ω I_D \cong - 45 A, V_{GEN} = - 10 V, R_G = 2.4 Ω		375	550	ns
Turn-Off Delay Time ^c	t _{d(off)}			100	200	
Fall Time ^c	t _f			140	250	
Source-Drain Diode Ratings and Cha	racteristic T	_C = 25 °C				
Pulsed Current	I _{SM}				100	Α
Diode Forward Voltage ^a	V_{SD}	I _F = - 45 A, V _{GS} = 0 V		1.0	1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 45 A, dI/dt = 100 A/μs		55	100	ns

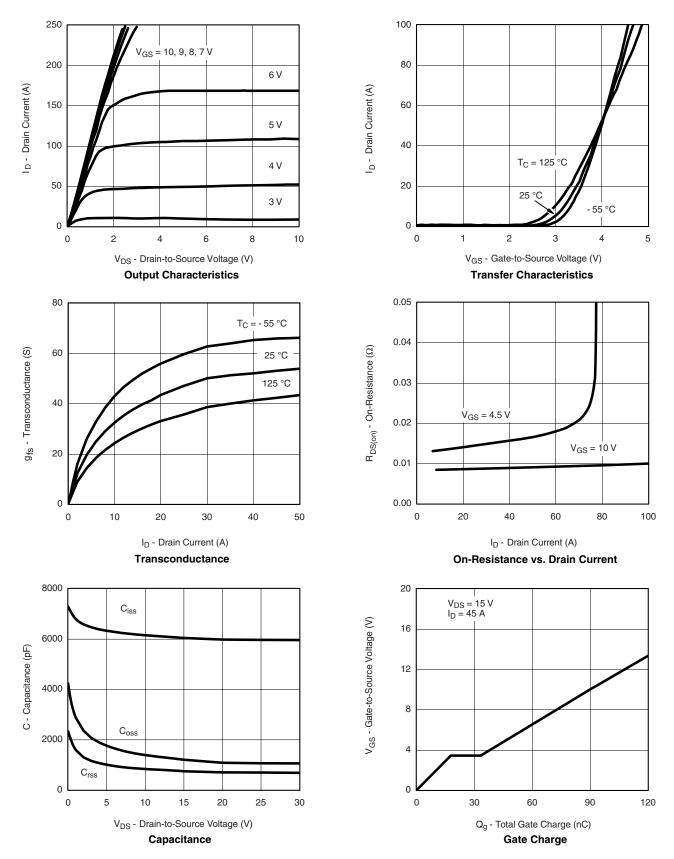
Notes:

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



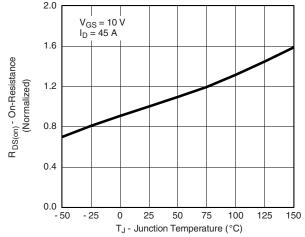
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



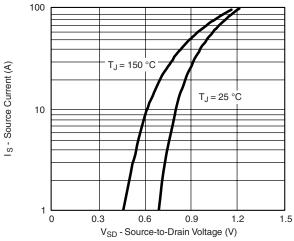
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

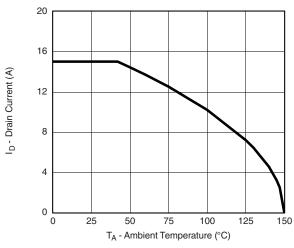


On-Resistance vs. Junction Temperature

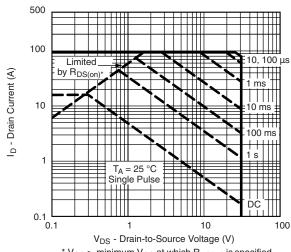


Source-Drain Diode Forward Voltage

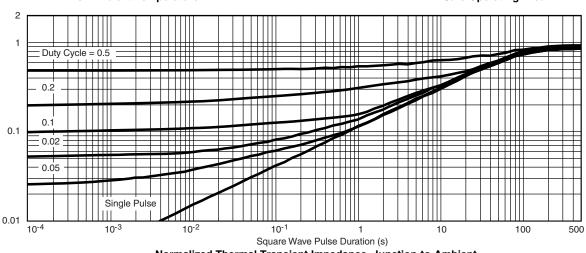
THERMAL RATINGS



Maximum Drain Current vs. Ambient Temperature



* V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified **Safe Operating Area**



Normalized Thermal Transient Impedance, Junction-to-Ambient

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Normalized Effective Transient Thermal Impedance



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