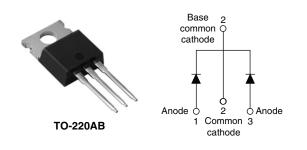
Vishay High Power Products

Ultrafast Rectifier, 2 x 10 A FRED Pt[™]



PRODUCT SUMMARY				
t _{rr}	25 ns			
I _{F(AV)}	2 x 10 A			
V _R	200 V			

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL TEST CONDITIONS		MAX.	UNITS
Peak repetitive reverse voltage		V _{RRM}		200	V
Average rectified forward current	per leg			10	
tot	tal device		Rated V_R , T_C = 145 °C	20	
Non-repetitive peak surge current per leg		I _{FSM}		100	A
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 145 °C	20	
Operating junction and storage temperatures		T_J, T_Stg		- 65 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS MIN.		TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
		I _F = 8 A, T _J = 125 °C	-	-	0.85	v	
Forward voltage	V _F	I _F = 16 A	-	-	1.15		
		I _F = 16 A, T _J = 125 °C	-	-	1.05		
Reverse leakage current I _R	1	$V_R = V_R$ rated	-	-	15		
	'R	$T_J = 150 \ ^{\circ}C, \ V_R = V_R \text{ rated}$	-	-	250	μΑ	
Junction capacitance	CT	V _R = 200 V - 55		-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body - 8.0 -		-	nH		

* Pb containing terminations are not RoHS compliant, exemptions may apply

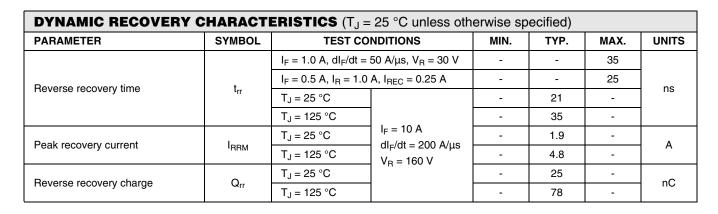




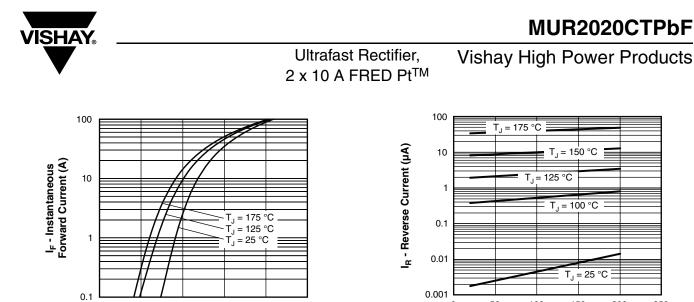
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THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, per leg	R _{th.IC}		-	-	2.5	
junction to case total device			-	-	1.25	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Weight			-	2.0	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf ⋅ cm (lbf ⋅ in)
Marking device		Case style TO-220AB		MUR2	020CT	



2.0

0.8

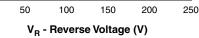
V_{FM} - Forward Voltage Drop (V) Fig. 1 - Maximum Forward Voltage Drop Characteristics

0.4

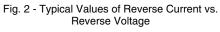
0

1.2

1.6



0



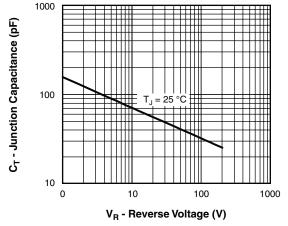


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

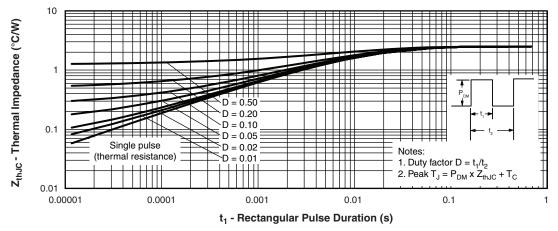
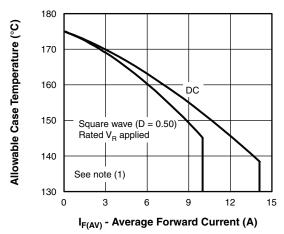


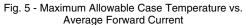
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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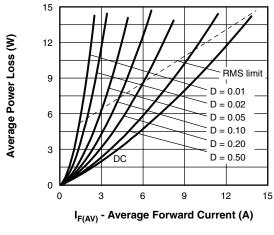
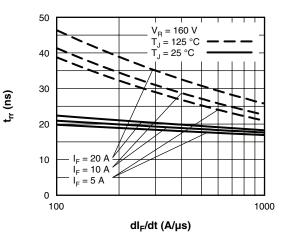


Fig. 6 - Forward Power Loss Characteristics

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{Rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

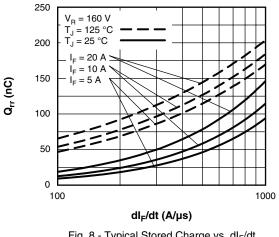


Fig. 8 - Typical Stored Charge vs. dl_F/dt



Ultrafast Rectifier, $2 \times 10 \text{ A FRED Pt}^{\text{TM}}$

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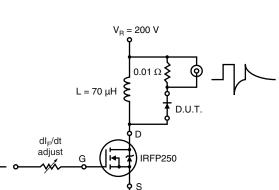
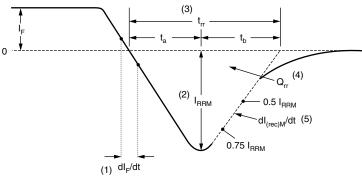


Fig. 9 - Reverse Recovery Parameter Test Circuit



(1) dl_F/dt - rate of change of current through zero crossing

Π

(4) ${\rm Q}_{\rm rr}$ - area under curve defined by ${\rm t}_{\rm rr}$ and ${\rm I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.
- (5) dI_{(rec)M}/dt peak rate of change of current during $t_{\rm b}$ portion of $t_{\rm rr}$
- Fig. 10 Reverse Recovery Waveform and Definitions

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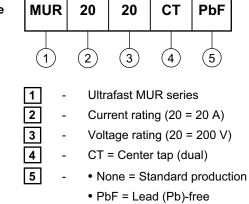
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ORDERING INFORMATION TABLE

Device code



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95222		
Part marking information	http://www.vishay.com/doc?95225		
SPICE model	http://www.vishay.com/doc?95272		



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