

Silicon NPN Power Transistors

2N5664 2N5665

DESCRIPTION

- With TO-66 package
- High breakdown voltage

APPLICATIONS

- High speed switching and linear amplifier
- High-voltage operational amplifiers
- Switching regulators ,converters
- Deflection stages and high fidelity amplifiers

PINNING (See Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

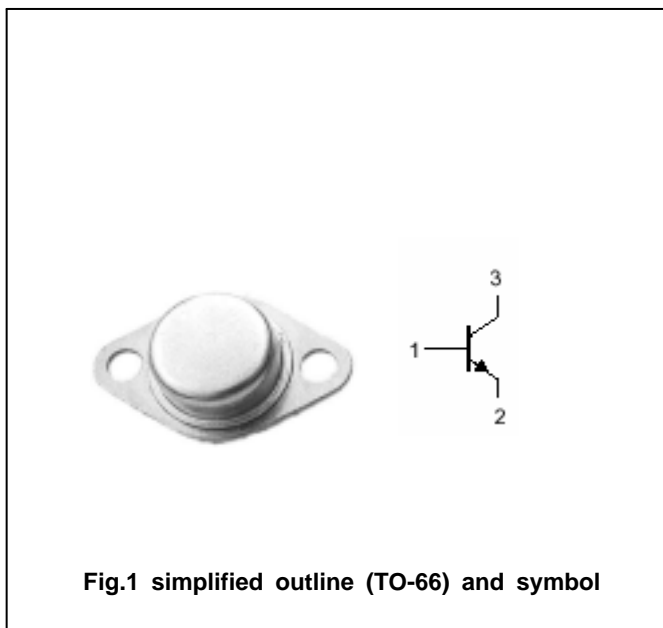


Fig.1 simplified outline (TO-66) and symbol

Absolute maximum ratings(Ta=25 )

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V <sub>CBO</sub>	Collector-base voltage	2N5664	250	V
		2N5665	400	
V <sub>CEO</sub>	Collector-emitter voltage	2N5664	200	V
		2N5665	300	
V <sub>EBO</sub>	Emitter-base voltage	Open collector	6	V
I <sub>C</sub>	Collector current		5.0	A
I <sub>B</sub>	Base current		1.0	A
P <sub>T</sub>	Total power dissipation	T <sub>C</sub> =25	52.5	W
T <sub>j</sub>	Junction temperature		200	
T <sub>stg</sub>	Storage temperature		-65~200	

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal resistance junction to case	5.0	/W

## Silicon NPN Power Transistors

## 2N5664 2N5665

## CHARACTERISTICS

T<sub>j</sub>=25 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-emitter breakdown voltage	2N5664	I <sub>C</sub> =10mA ; I <sub>B</sub> =0			V
		2N5665				
V <sub>(BR)EBO</sub>	Emitter-base breakdown voltage	I <sub>E</sub> =10 μ A ; I <sub>C</sub> =0	6			V
V <sub>CEsat-1</sub>	Collector-emitter saturation voltage	2N5664	I <sub>C</sub> =3A ; I <sub>B</sub> =0.3A		0.4	V
		2N5665				
V <sub>CEsat-2</sub>	Collector-emitter saturation voltage	I <sub>C</sub> =5A ; I <sub>B</sub> =1A			1.0	V
V <sub>BEsat-1</sub>	Base-emitter saturation voltage	2N5664	I <sub>C</sub> =3A ; I <sub>B</sub> =0.3A		1.2	V
		2N5665				
V <sub>BEsat-2</sub>	Base-emitter saturation voltage	I <sub>C</sub> =5A ; I <sub>B</sub> =1A			1.5	V
I <sub>CES</sub>	Collector cut-off current	2N5664	V <sub>CE</sub> =200V ; V <sub>BE(off)</sub> =1.5V		0.2	mA
		2N5665				
I <sub>CBO</sub>	Collector cut-off current	2N5664	V <sub>CB</sub> =250V ; I <sub>E</sub> =0		1.0	mA
		2N5665				
h <sub>FE-1</sub>	DC current gain	2N5664	I <sub>C</sub> =0.5A ; V <sub>CE</sub> =2V			
		2N5665				
h <sub>FE-2</sub>	DC current gain	2N5664	I <sub>C</sub> =1A ; V <sub>CE</sub> =5V			
		2N5665				
h <sub>FE-3</sub>	DC current gain	2N5664	I <sub>C</sub> =3A ; V <sub>CE</sub> =5V			
		2N5665				
h <sub>FE-4</sub>	DC current gain	I <sub>C</sub> =5A ; V <sub>CE</sub> =5V	5			
C <sub>OB</sub>	Output capacitance	I <sub>E</sub> =0 ; V <sub>CB</sub> =10V ; f=1MHz			120	pF
t <sub>on</sub>	Turn-on time	V <sub>CC</sub> =30V ; I <sub>C</sub> =1A ; I <sub>B1</sub> =-I <sub>B2</sub> =30mA			0.25	μ s
t <sub>off</sub>	Turn-off time	2N5664	V <sub>CC</sub> =30V ; I <sub>C</sub> =1A ; I <sub>B1</sub> =-I <sub>B2</sub> =50mA			μ s
		2N5665				
					2.0	

PACKAGE OUTLINE

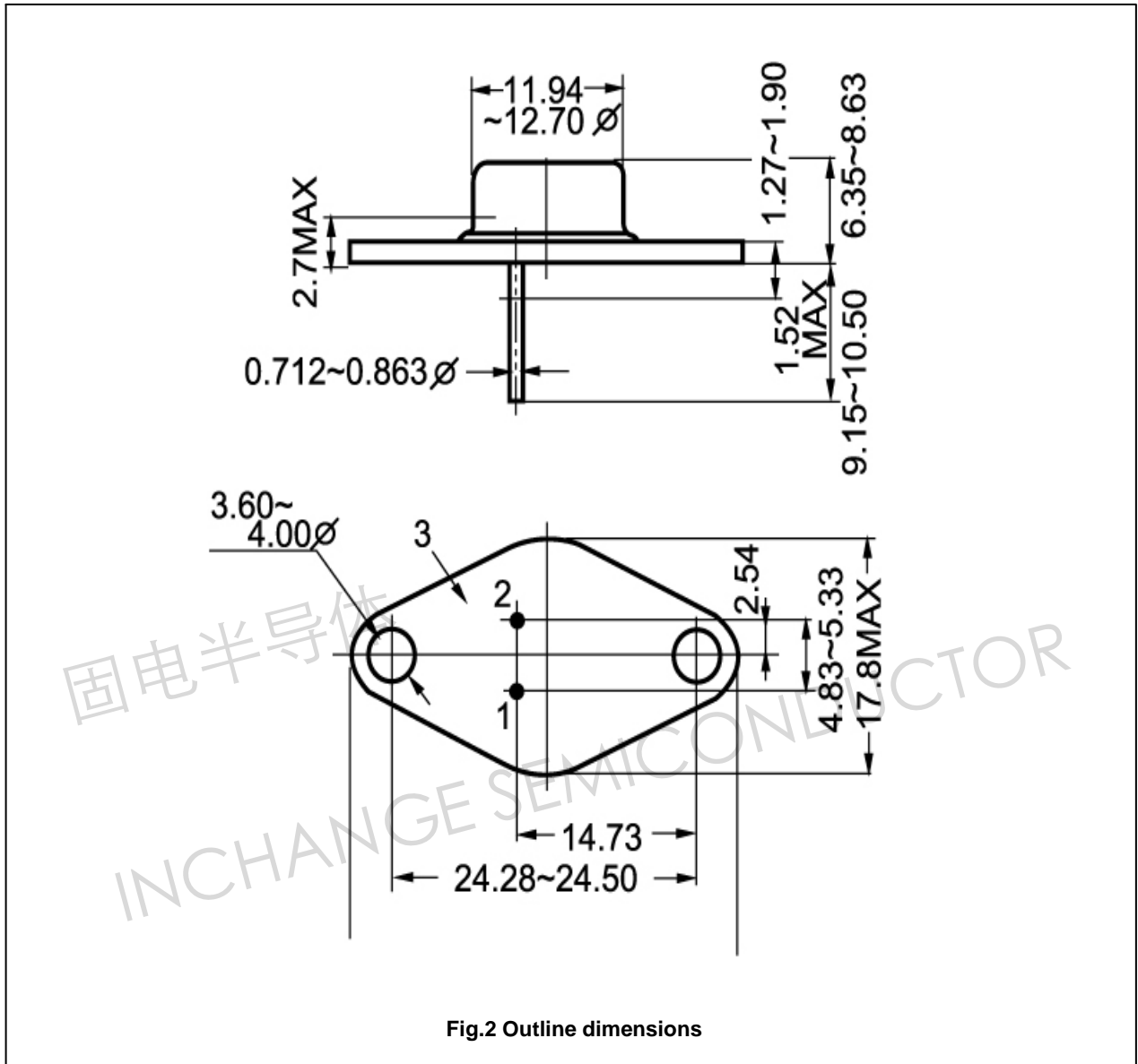


Fig.2 Outline dimensions