

Silicon PNP Power Transistors

2SA1065

DESCRIPTION

- With TO-3 package
- Low collector saturation voltage
- High transition frequency
- Complement to type 2SC2489

APPLICATIONS

- For audio frequency amplifier and high power amplifier applications

PINNING(see Fig.2)

PIN	DESCRIPTION
1	Base
2	Emitter
3	Collector

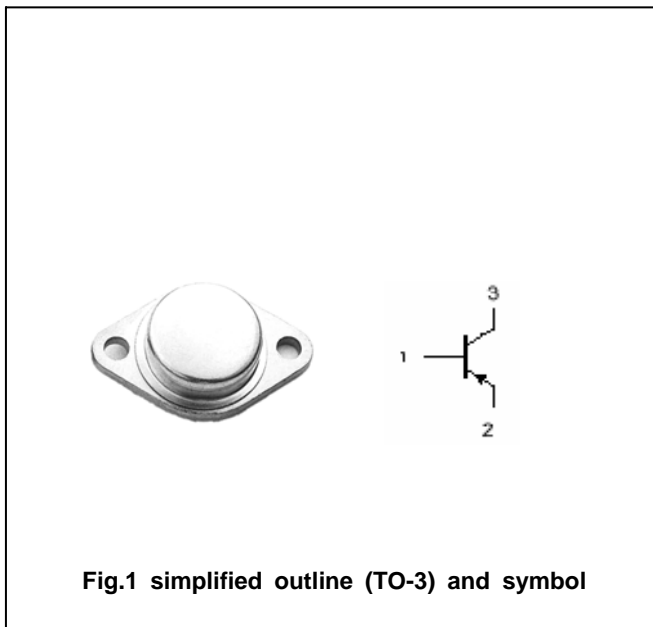


Fig.1 simplified outline (TO-3) and symbol

Absolute maximum ratings(Ta=°C)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V _{CBO}	Collector-base voltage	Open emitter	-150	V
V _{CEO}	Collector-emitter voltage	Open base	-150	V
V _{EBO}	Emitter-base voltage	Open collector	-5	V
I _C	Collector current		-10	A
I _{CM}	Collector current-peak		-15	A
P _C	Collector power dissipation	T _C =25°C	120	W
T _j	Junction temperature		150	°C
T _{stg}	Storage temperature		-65~150	°C

Silicon PNP Power Transistors

2SA1065

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-0.1A ; I _B =0	-150			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-10A ; I _B =-1A			-2.0	V
V _{BE}	Base-emitter on voltage	I _C =-10A ; V _{CE} =-5V			-2.5	V
I _{CBO}	Collector cut-off current	V _{CB} =-70V ; I _E =0			-1	mA
I _{EBO}	Emitter cut-off current	V _{EB} =-5V ; I _C =0			-2	mA
h _{FE-1}	DC current gain	I _C =-2A ; V _{CE} =-5V	40		280	
h _{FE-2}	DC current gain	I _C =-10A ; V _{CE} =-5V	20			
f _T	Transition frequency	I _C =-0.5A ; V _{CE} =-5V		50		MHz

◆ h_{FE-1} Classifications

R	Q	P	O
40-80	60-120	90-180	140-280

PACKAGE OUTLINE

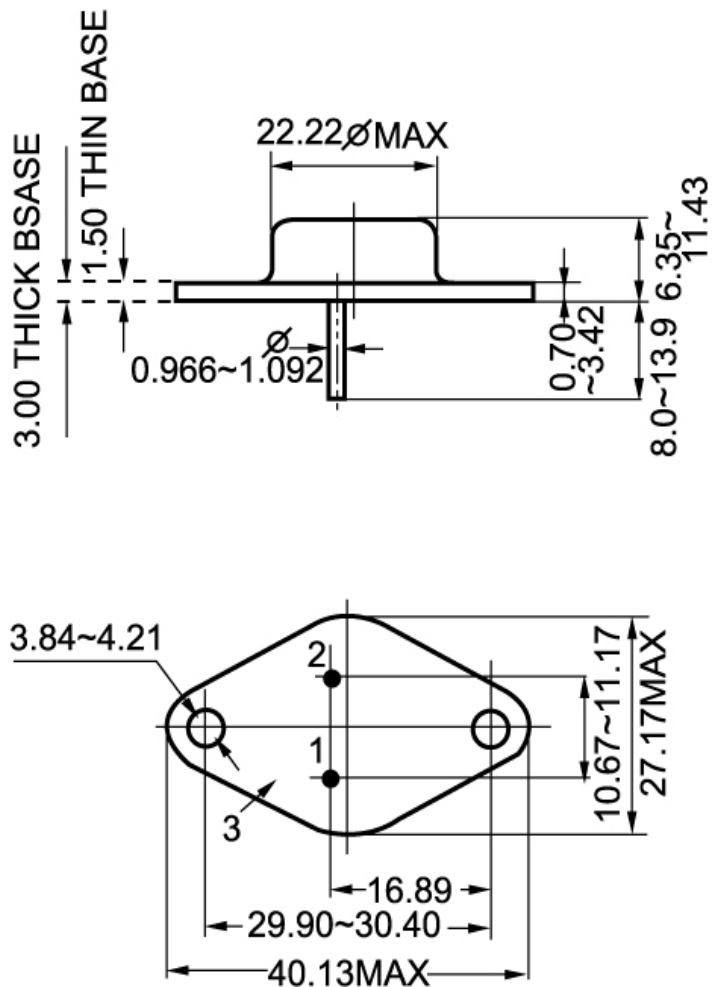


Fig.2 outline dimensions (unindicated tolerance: ± 0.1 mm)