

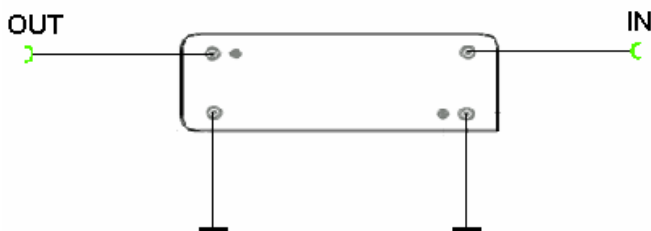
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	139.96	140	140.04
Insertion Loss	dB	-	19	21
1 dB Bandwidth	MHz	0.45	0.47	-
3 dB Bandwidth	MHz	0.5	0.674	-
40 dB Bandwidth	MHz	-	1.41	1.5
50 dB Bandwidth	MHz	-	1.47	1.6
Phase Linearity	deg	-	6	-
Passband Variation	dB	-	0.3	1.0
Ultimate Rejection (over -1.1 ~ +1.1MHz)	dB	55	60	-
Absolute delay	usec	-	3.72	-
Substrate Material		Quartz		
Ambient Temperature	°C	25		
Package Size	DIP2712 (27.0x12.8x4.7mm ³)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance shown

Matching Configuration



Source/Load Impedance=50 ohm

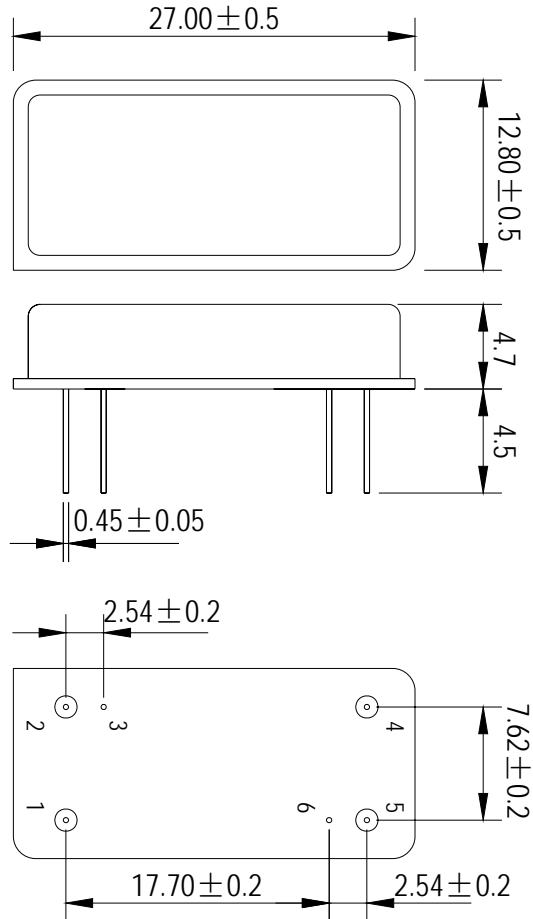
Notes - Component values may change depending on board layout.



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

Part Number	SP LBS14061	
Rev. Date	2004-11-29	
Rev.	1.0	Page 1/3

Package Dimension

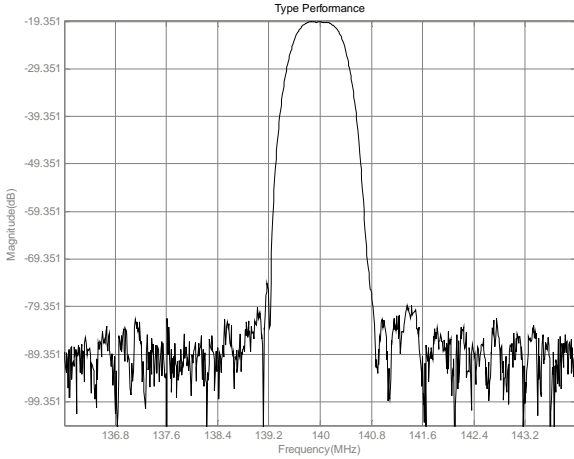


SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

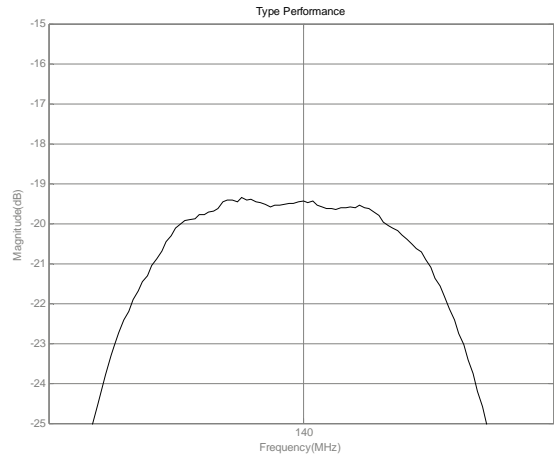
Part Number	SP LBS14061	
Rev. Date	2004-11-29	
Rev.	1.0	Page 2/3

Typical Performance

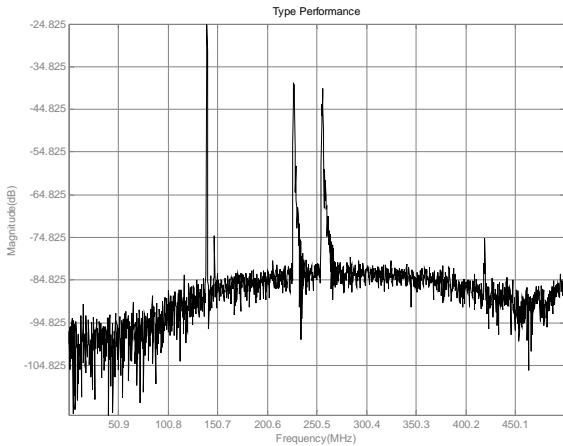
Frequency Respond



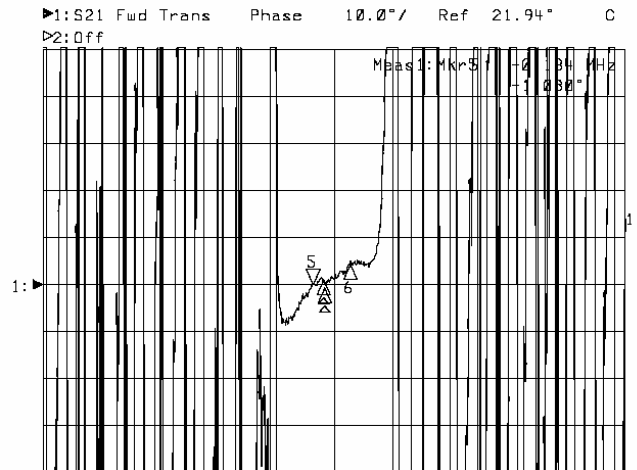
Passband Respond



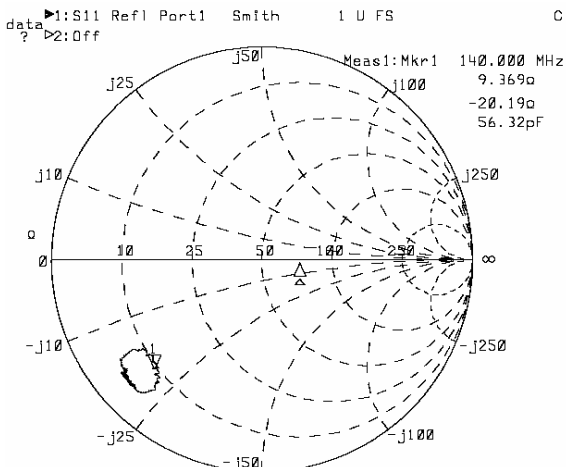
Wideband Respond



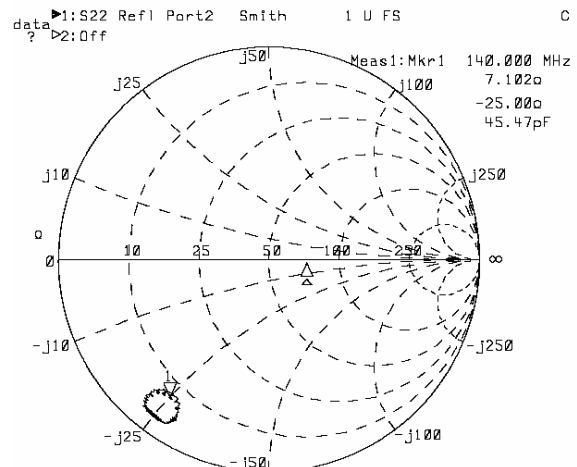
1.0 dB Phase Linearity



Smith Chart S11



Smith Chart S22



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanning Huayuan Road No. 14
Chongqing, China, 400060

Part Number

SP LBS14061

Rev. Date

2004-11-29

Rev.

1.0

Page 3/3