

HRV103A

Silicon Schottky Barrier Diode for Rectifying

REJ03G0398-0300 Rev.3.00 Mar 25, 2008

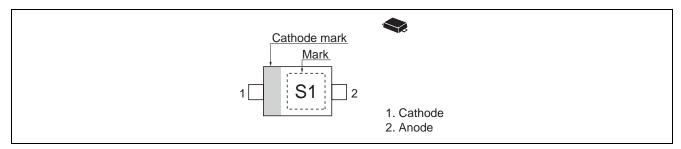
Features

- Low forward voltage drop and suitable for high efficiency rectifying.
- Thin Ultra small Resin Package (TURP) is suitable for high density surface mounting and high speed assembly.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code
HRV103A	S1	TURP	PUSF0002ZC-A

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V _{RRM}	30	V
Reverse voltage	V _R	30	V
Average rectified current	I ₀ * ²	1	А
Non-Repetitive peak forward surge current	I _{FSM} * ¹	5	А
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. 10 ms sine wave 1 pulse

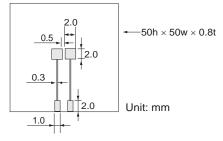
2. Ta = 44°C, With Ceramics board (board size: 50 mm \times 50 mm, Land size 2 mm \times 2 mm) Short form wave (θ 180°C), V_R = 10 V.

Electrical Characteristics

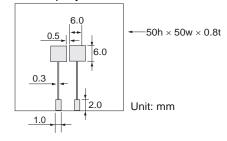
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _{F1}	_	_	0.27	V	I _F = 100 mA
	V _{F2}	_	_	0.36		I _F = 700 mA
	V _{F3}	_	_	0.42		I _F = 1 A
Reverse current	I _{R1}	_		100	μΑ	V _R = 5 V
	I _{R2}	_	_	1000		V _R = 30 V
Capacitance	С	_	_	40	pF	V _R = 10 V, f = 1 MHz
Thermal resistance	Rth(j-a)	_	100	_	°C/W	Ceramics board *1
		_	200	_		Glass epoxy board *2

Notes: 1. Ceramics board



2. Glass epoxy board



3. TURP is the structure which radiates heat to a substrate, please perform mounting to a substrate by reflow.

Main Characteristics

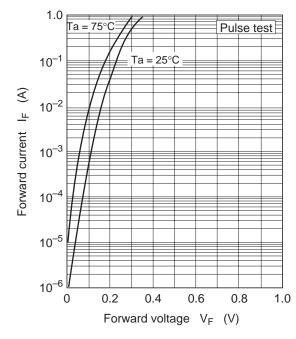


Fig.1 Forward current vs. Forward voltage

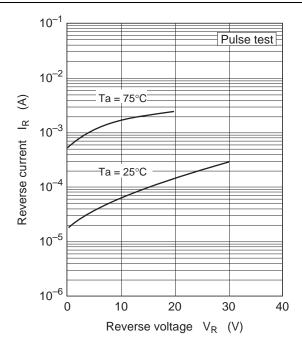
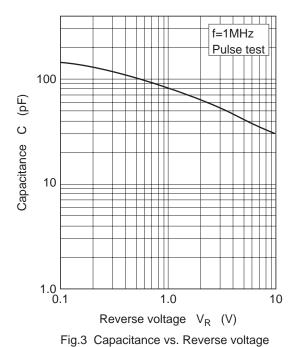
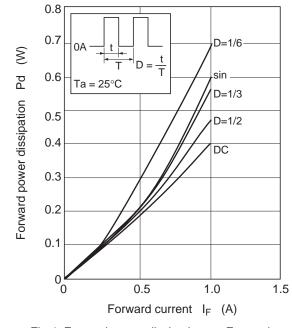


Fig.2 Reverse current vs. Reverse voltage





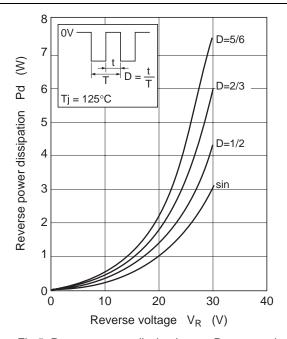
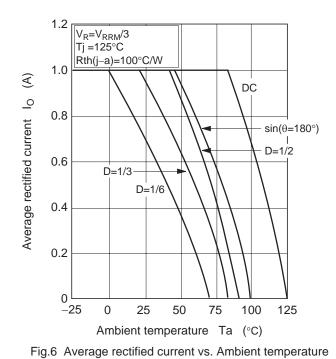


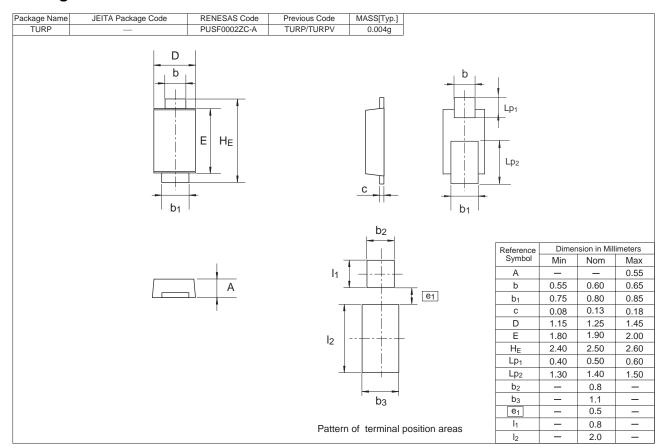
Fig.4 Forward power dissipation vs. Forward current

Fig.5 Reverse power dissipation vs. Reverse voltage



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Package Dimensions



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