

H5N2501LD, H5N2501LS, H5N2501LM

Silicon N Channel MOS FET High Speed Power Switching

REJ03G1250-0200 Rev.2.00 Jul.21,2005

Features

- Low on-resistance
- Low leakage current
- High speed switching

Outline

RENESAS Package code: PRSS0004AE-A (Package name LDPAK(L))

RENESAS Package code: PRSS0004AE-B (Package name LDPAK(S)-(1))

RENESAS Package code: PRSS0004AE-B (Package name LDPAK(S)-(1))

RENESAS Package code: PRSS0004AE-C (Package name LDPAK(S)-(2))

H5N2501LS

H5N2501LD

1. Gate 2. Drain 3. Source 4. Drain

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to Source voltage	V _{DSS}	250	V
Gate to Source voltage	V _{GSS}	±30	V
Drain current	I _D	18	A
Drain peak current	I _{D (pulse)} Note1	72	A
Body-Drain diode reverse Drain current	I _{DR}	18	A
Avalanche current	I _{AP} Note3	18	Α
Channel dissipation	Pch Note2	75	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

3. STch = 25° C, Tch $\leq 150^{\circ}$ C

Electrical Characteristics

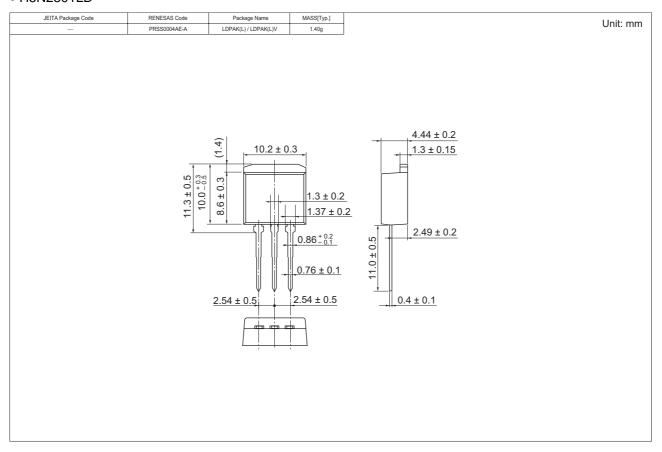
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to Source breakdown voltage	$V_{(BR)DSS}$	250	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero Gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 250 \text{ V}, V_{GS} = 0$
Gate to Source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to Source cutoff voltage	$V_{GS(off)}$	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Forward transfer admittance	yfs	8	14	_	S	$I_D = 9 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Static Drain to Source on state resistance	R _{DS(on)}	_	0.14	0.18	Ω	$I_D = 9 \text{ A}, V_{GS} = 10 \text{ V}^{Note4}$
Input capacitance	Ciss	_	1350	_	рF	V _{DS} = 25 V
Output capacitance	Coss	_	170	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	50	_	pF	f = 1 MHz
Turn-on delay time	td(on)	_	30	_	ns	I _D = 9 A
Rise time	tr	_	65	_	ns	V _{GS} = 10 V
Turn-off delay time	td(off)	_	95	_	ns	$R_L = 13.9 \Omega$
Fall time	tf	_	18	_	ns	$Rg = 10 \Omega$
Total Gate charge	Qg	_	45	_	nC	V _{DD} = 200 V
Gate to Source charge	Qgs	_	8	_	nC	V _{GS} = 10 V
Gate to Drain charge	Qgd	_	22	_	nC	I _D = 18 A
Body-Drain diode forward voltage	V_{DF}	_	0.9	1.4	V	$I_F = 18 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-Drain diode reverse recovery time	trr	_	160	_	ns	I _F = 18 A, V _{GS} = 0
Body-Drain diode reverse recovery charge	Qrr	_	1.0		μС	di _F /dt = 100 A/μs

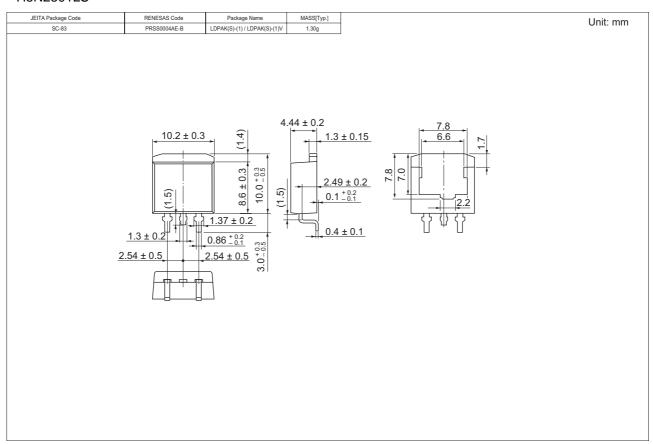
Notes: 4. Pulse test

Package Dimensions

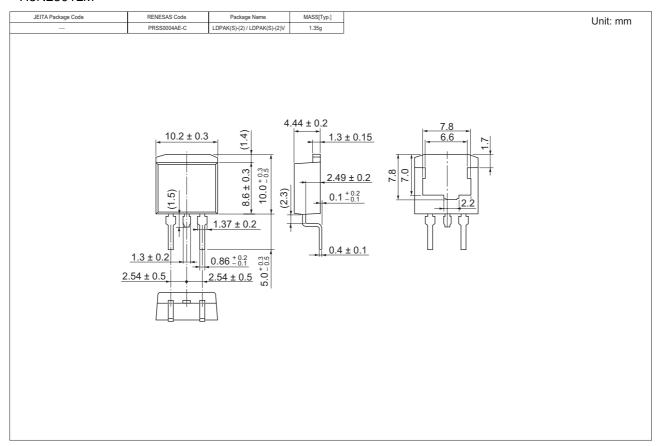
• H5N2501LD



• H5N2501LS



• H5N2501LM



Ordering Information

Part Name	Quantity	Shipping Container
H5N2501LSTL-E	1000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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