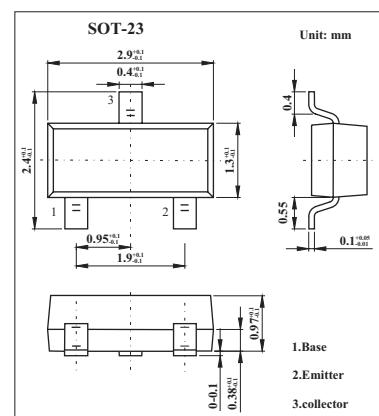


## PNP Silicon Epitaxial Transistor

### 2SA1330

#### ■ Features

- High DC current gain.
- High voltage.



#### ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	-200	V
Collector-emitter voltage	V <sub>C EO</sub>	-200	V
Emitter-base voltage	V <sub>EBO</sub>	-5	V
Collector current	I <sub>C</sub>	-100	mA
Total power dissipation	P <sub>T</sub>	200	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to +150	°C

**2SA1330**■ Electrical Characteristics  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
Collector cutoff current	$I_{CBO}$	$V_{CB} = -200\text{V}, I_E = 0$			-100	nA
Emitter cutoff current	$I_{EBO}$	$V_{EB} = -5\text{V}, I_C = 0$			-100	nA
DC current gain *	$h_{FE}$	$V_{CE} = -10\text{V}, I_C = -10\text{mA}$	90	200	450	
		$V_{CE} = -10\text{V}, I_C = -50\text{mA}$	50	195		
Base-emitter voltage *	$V_{BE}$	$V_{CE} = -10\text{V}, I_C = -10\text{mA}$	-0.6	-0.65	-0.7	V
Collector-emitter saturation voltage *	$V_{CE(sat)}$	$I_C = -50\text{mA}, I_B = -5\text{mA}$		-0.21	-0.3	V
Base saturation voltage *	$V_{BE(sat)}$	$I_C = -50\text{mA}, I_B = -5\text{mA}$		-0.8	-1.2	V
Gain bandwidth product	$f_T$	$V_{CE} = -10\text{V}, I_E = 10\text{mA}$		120		MHz
Output capacitance	$C_{ob}$	$V_{CB} = -30\text{V}, I_E = 0, f = 1.0\text{MHz}$		3.6		pF
Turn-on time	$t_{on}$	$I_C = -10\text{mA}, I_{B1} = -I_{B2} = -1\text{mA}, V_{CC} = -10\text{V}$		0.16		$\mu\text{s}$
Storage time	$t_{stg}$			1.3		$\mu\text{s}$
Fall time	$t_f$	$V_{BE(off)} = 2.5\text{V}$		0.18		$\mu\text{s}$

\* Pulse test:  $t_p \leq 350 \mu\text{s}; d \leq 0.02$ .

## ■ hFE Classification

Marking	O5	O6	O7
hFE	90~180	135~270	200~450