

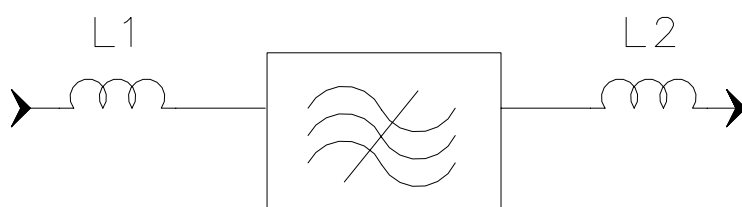
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	74.7	74.75	74.8
Insertion Loss	dB	-	22.9	25
3 dB Bandwidth	MHz	4.5	4.6	-
35 dB Bandwidth	MHz	-	6.6	6.9
Absolute Delay	usec	-	2.77	-
Passband Variation	dB	-	0.5	1
Ultimate Rejection	dB	50	53	-
Substrate Material		112 LT		
Ambient Temperature	°C	25		
Package Size		DIP2212 (22.7x12.7x5.2mm ³)		

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show.


Matching Configuration



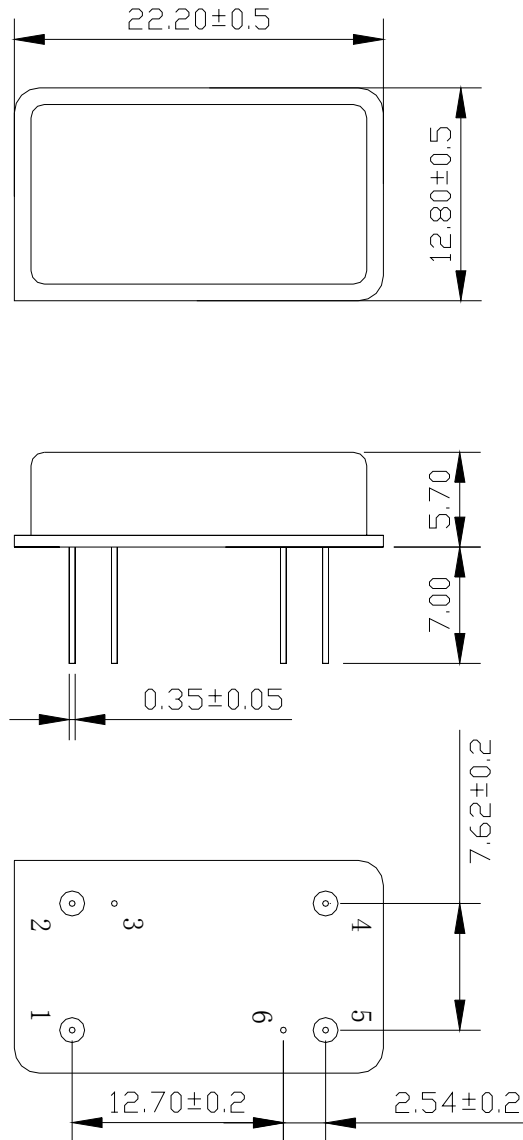
L1=L2=100nH

Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	LBT07402	
		Rev. Date	2004-11-10	
		Rev.	1.0	Page

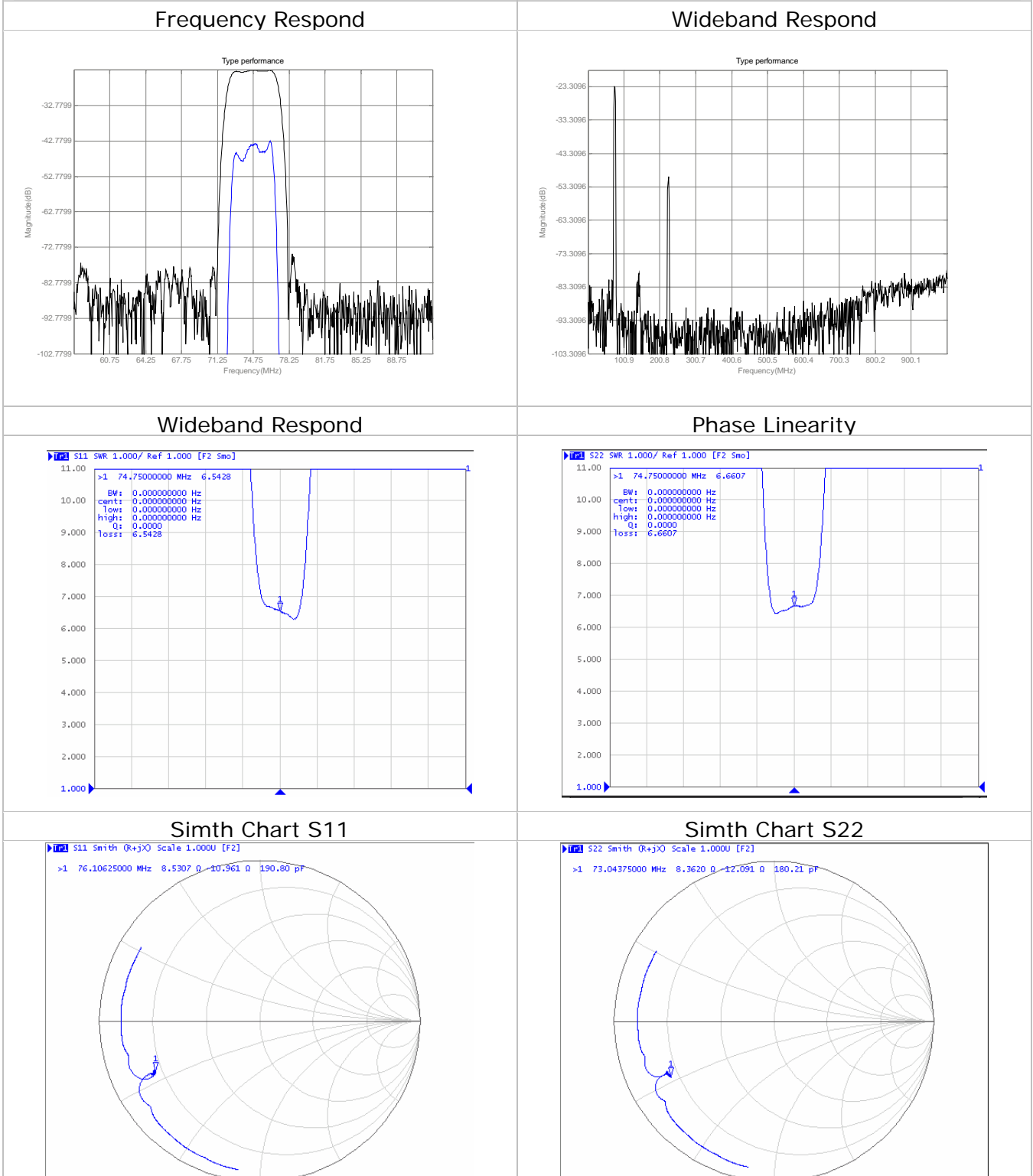
Package Dimension



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

Part Number	LBT07402	
Rev. Date	2004-11-10	
Rev.	1.0	Page 2/3

Typical Performance



SIPAT Co., Ltd.
(CETC No. 26 Research Institute)
Nanping Huayuan Road No. 14
Chongqing, China, 400060

Part Number	LBT07402	
Rev. Date	2004-11-10	
Rev.	1.0	Page 3/3