ECLIPTEK[®] CORPORATION

EB71F72 Series

- Oven Controlled Crystal Oscillator (OCXO)
- SC-Cut Crystal
- HCMOS output
- 5.0V supply voltage
- 5 pin DIP package
- External control voltage
- Stability to ±20ppb

ELECTRICAL SPECIFICATIONS

ator (OCXO)	
	H 15.0 L 25.5 W 25.5 W 25.5

EB71F7

Frequency Ra	-	10.000MHz, 12.288MHz, 12.800MHz, 16.000MHz, 19.440MHz, or 20.000MHz						
	perature Range (OTR)			0°C to 50°C, 0°C to 70°C, or -20°C to 70°C				
Storage Temperature Range			-55°C to	-55°C to 125°C				
Supply Voltage (V _{DD})			5.0V _{DC} ±5°	5.0V _{DC} ±5%				
Frequency To	lerance / Stability							
vs. Initial Tole	erance	at Nominal V_{DD} and V_c , at 25°C		±500ppb	±500ppb or ±300ppb Maximum			
vs. Temperature Stability		at Nominal V_{DD} and V_{C}		±20ppb, <u>-</u>	± 20 ppb, ± 30 ppb, ± 50 ppb, ± 80 ppb, ± 100 ppb			
				±200ppb,	or ±280ppb Maximum			
vs.Vdd	d V _{DD} ±5%		±20ppb M	±20ppb Maximum				
vs. Load		Vload ±5%		±20ppb M	±20ppb Maximum			
vs. Aging (1 D	Jay)	after 72 Hours of Operation		±2.0ppb1	±2.0ppb Maximum			
vs. Aging (1 Y	'ear)	after 72 Hours of Operation		±100ppb	±100ppb Maximum			
vs. Aging (10	Years)	after 72 Hours of Ope	eration	±500ppb	±500ppb Maximum			
Crystal Cut				SC-Cut	SC-Cut			
Warm Up Tim	e	to ±50ppb of Final Frequency at 1 Hour at 25°C		3 Minute	3 Minute Maximum			
Power Consumption		at Steady State, at 25°C		1.2 Watts	1.2 Watts Maximum			
		During Warm Up, at 25°C		3.6 Watts	3.6 Watts Maximum			
Output Volta	Itput Voltage Logic High (V он) I _{он} = -8mA			V _{DD} -0.5V _{DD}	V _{DD} -0.5V _{DC} Minimum			
Output Volta	ge Logic Low (V _{ol})	$I_{0L} = +8mA$		0.5V _{DC} Ma	0.5V _{DC} Maximum			
Rise Time / Fa	all Time	Measured at 20% to 80% of Waveform		6nSec Ma	6nSec Maximum			
Duty Cycle	le Measured at 50% of Waveform		50 ±5(%)					
Load Drive Capability			15pF HCMOS Load Maximum					
Frequency Deviation Referenced to F_0 at $V_c = 2.5V_{DC}$; $V_{DD}=5.0V_{DC}$ over OTR			±0.5ppm Minimum					
Control Voltage Range			$0.0V_{DC}$ to V_{DD}					
Control Voltage (V _c)			2.5V _{DC} ±2.5V _{DC}					
Transfer Function Positive Transfer Characteristic								
Reference Voltage Output4.5Vpc ±0.3Vpc (Pin 4)								
Linearity								
Input Impedance 10kOhms Typical								
Typical Phase Noise (at 12.800MHz) 1Hz Of		1Hz Offset -90dBc/Hz						
10Hz Offset-100dBc/Hz100Hz Offset-130dBc/Hz				Hz				
				-130dBc/	130dBc/Hz			
1kHz Offset			-145dBc/Hz					
10kHz Offset -150dBc/Hz								
1 A N U F A C T U R E R	CATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV _ DATE		
CLIPTEK CORP.	OSCILLATOR	EB71F72	5 pin DIP	5.0V	0S2L	05/07		

PART NUMBERING GUIDE

EB71F72 <u>D</u> <u>10</u> <u>B</u> <u>V</u> <u>2</u> - <u>20.000M</u>

INITIAL TOLERANCE

D=±500ppb E=±300ppb

FREQUENCY STABILITY

2 Digit Code Per Table 1

OPERATING TEMPERATURE RANGE

1 Letter Code Per Table 1

DUTY CYCLE

2=50% ±5%

VOLTAGE CONTROL OPTION

V=Voltage Control on Pin 3 and Reference Voltage Output on Pin 4



