TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SHU04FE

#### INVERTER (Un-Buffer)

#### **Features**

• Super high speed operation :tpD = 3.5 ns (typ.)

$$@V_{CC} = 5 V$$

• Low power dissipation :  $I_{CC} = 2 \mu A (Max.)$ 

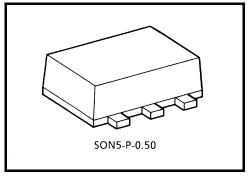
$$@$$
 Ta = 25°C

• High noise immunity :  $V_{NIH} = V_{NIH}$ 

$$= 10\% \text{ V}_{\text{CC}} \text{ (Min.)}$$

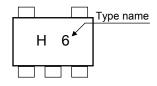
• 5.5V tolerant input.

• Wide operation voltage range :  $V_{CC}$  (opr) = 2~5.5 V

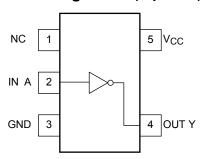


Weight: 0.003 g (typ.)

## Marking



#### Pin Assignment (top view)



#### **Absolute Maximum Ratings (Ta = 25°C)**

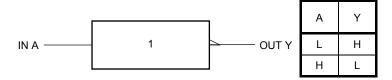
Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5~7	V
DC input voltage	V <sub>IN</sub>	-0.5~7	V
DC output voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5	V
Input diode current	l <sub>IK</sub>	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T <sub>stg</sub>	-65~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

# **Logic Diagram**

# **Truth Table**



# **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2~5.5	V
Input voltage	V <sub>IN</sub>	0~5.5	V
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40~85	°C

# **Electrical Characteristics**

### **DC Characteristics**

Characteristics Symbol Test Circuit		Test	Tost Condition			Ta = 25°C			Ta = -40~85°C		Unit
		Circuit			V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage						1.7	_	_	1.7	_	٧
		_	_		3.0~5.5	V <sub>CC</sub> × 0.8			V <sub>CC</sub> × 0.8		
Low lovel input					2.0	_	_	0.3	_	0.3	
voltage	Low-level input voltage			_		_	_	V <sub>CC</sub> × 0.2	_	V <sub>CC</sub> × 0.2	V
			$V_{IN} = V_{IL}$	Ι <sub>ΟΗ</sub> = -50 μΑ	2.0	1.8	2.0	_	1.8	_	V
High-level VOH					3.0	2.7	3.0	_	2.7	_	
	V <sub>OH</sub>				4.5	4.0	4.5	_	4.0	_	
			V <sub>IN</sub> =GND	$I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48	_	
				$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
					2.0	_	0	0.2		0.2	
Low-level output voltage	_	$V_{IN} = V_{IH}$	$I_{OL} = 50 \mu A$	3.0	_	0	0.3		0.3	V	
				4.5	_	0	0.5		0.5		
		\/\/a=	$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36		0.44		
			V <sub>IN</sub> =V <sub>CC</sub>	$I_{OL} = 8 \text{ mA}$	4.5	_	_	0.36		0.44	
Input leakage current	I <sub>IN</sub>	_	V <sub>IN</sub> = 5.5 V	or GND	0~5.5	_	_	±0.1	_	±1.0	μА
Quiescent supply current	Icc	_	V <sub>IN</sub> = V <sub>CC</sub> o	or GND	5.5	_	_	2.0	_	20.0	μА



# AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit	
			V <sub>CC</sub> (V)	C <sub>L (</sub> pF)	Min	Тур.	Max	Min	Max	Onit
Propagation delay time	tplH tpHL	3.3 ± 0.3	22+02	15	_	5.0	8.9	1.0	10.5	
			50	_	7.5	11.4	1.0	13.0	20	
			F 0 + 0 F	15	_	3.5	5.5	1.0	6.5	ns
		5.0 ± 0.5	50	_	5.0	7.0	1.0	8.0		
Input capacitance	C <sub>IN</sub>				_	5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>		(Note)		_	6	_	_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

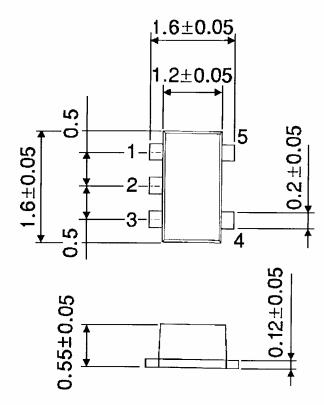
$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

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# **Package Dimensions**

SON5-P-0.50 Unit: mm



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Weight: 0.003 g (typ.)

#### **RESTRICTIONS ON PRODUCT USE**

20070701-EN GENERAL

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