

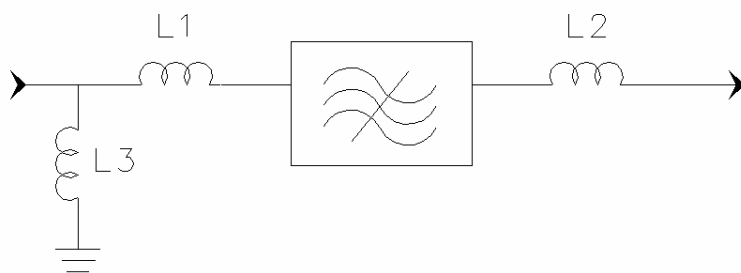
Specifications

Parameter	Unit	Minimum	Typical	Maximum
Center Frequency	MHz	69.95	70	70.05
Insertion Loss	dB	-	25	27
3 dB Bandwidth	MHz	2.05	2.1	2.15
35 dB Bandwidth	MHz	-	2.83	2.9
45 dB Bandwidth	MHz	-	2.94	3.3
50 dB Bandwidth	MHz	-	3.08	-
Passband Variation	dB	-	0.5	1.0
Absolute Delay	usec	-	3.35	-
Ultimate Rejection	dB	50	52	-
Material Temperature coefficient	KHz/°C	0.07		
Ambient Temperature	°C	25		
Package Size	DIP3512 (35.0x12.8x4.7mm3)			

Notes:

1. All specifications are based on the test circuit shown
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. This is the optimum impedance in order to achieve the performance show

Matching Configuration




L1=270nH L2=180nH

L3=100nH

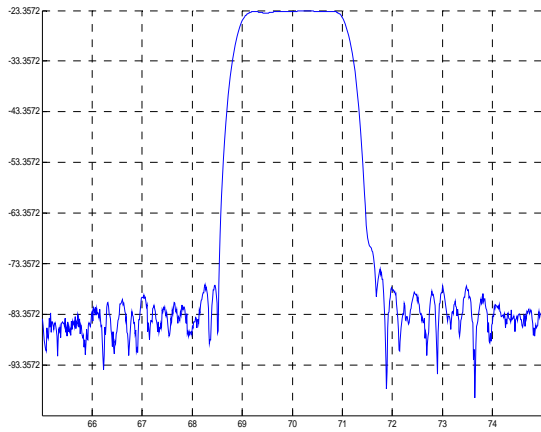
Source/Load Impedance=50 ohm

Notes - Component values may change depending on board layout.

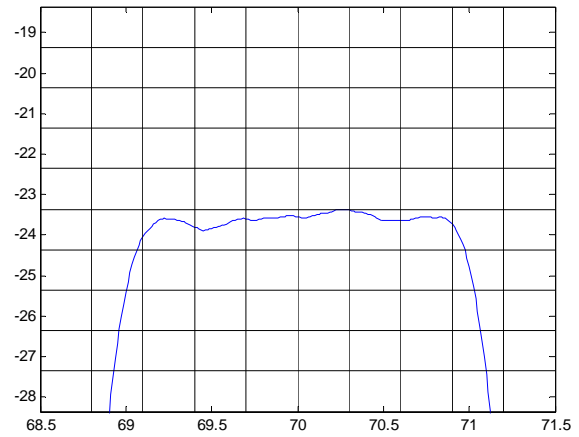
	SIPAT Co., Ltd. (CETC No. 26 Research Institute) Nanping Huayuan Road No. 14 Chongqing, China, 400060	Part Number	LBS70A10	
		Rev. Date	2005-10-17	
		Rev.	1.0	Page

Typical Performance

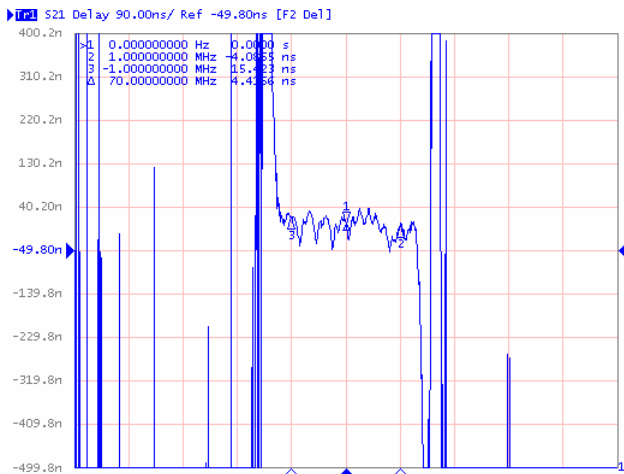
Frequency Respond



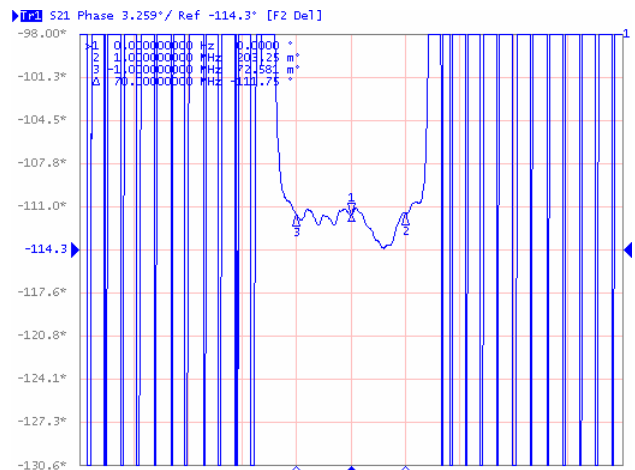
Passband Respond



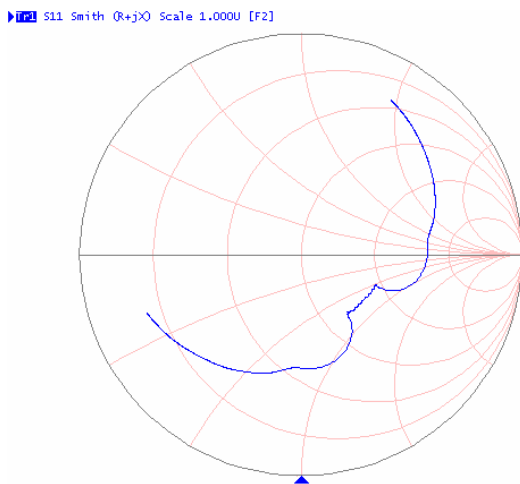
Group Delay Variation($f_0 \pm 1\text{MHz}$)



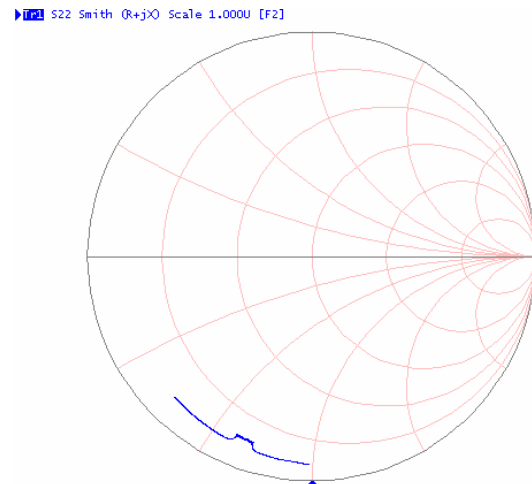
Phase Linearity($f_0 \pm 1\text{MHz}$)



Smith Chart S11



Smith Chart S22



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(CETC No. 26 Research Institute)
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Chongqing, China, 400060

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