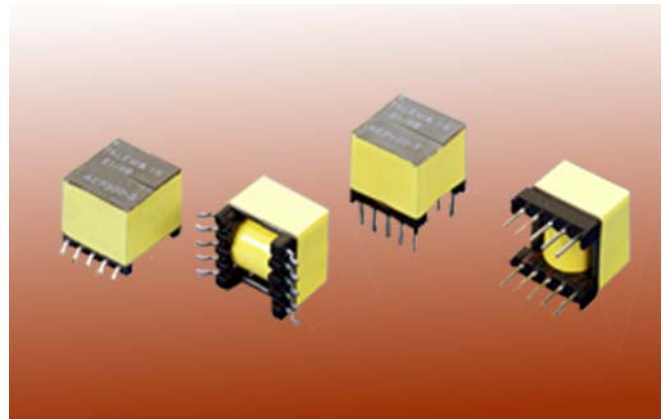




ADSL Transformers for Legerity (AMD) Chipsets

Features

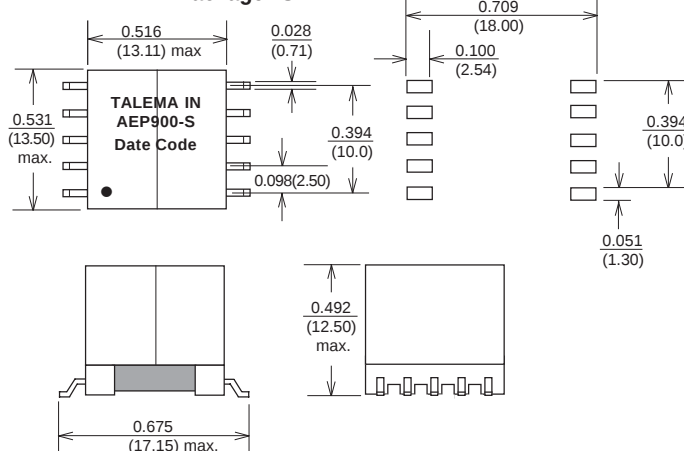
- complies with ANSI and ETSI requirements
- matched to Legerity's AMDL134 chipsets
- very low THD
- 1500V minimum isolation
- all materials approved to UL94V-0
- excellent quality at competitive price due to high volume production
- manufactured in an ISO-9001:2000, TS-16949:2002 and ISO-14001:2004 certified Talema facility
- fully RoHS compliant and meets lead free reflow level J-STD-020C



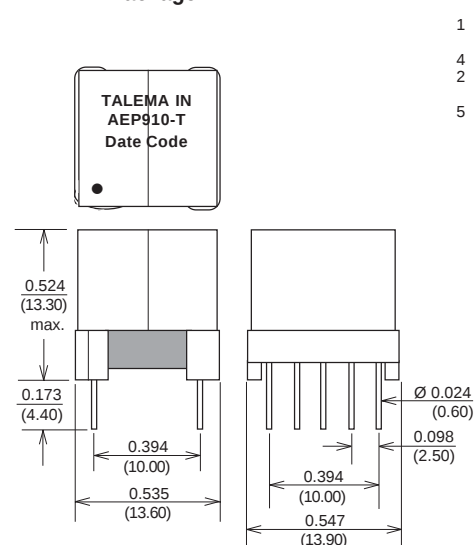
Electrical Specifications @ 25°C • Operating Temperature -40° to +85°C

Part Number	Package	IC Driver	Turns Ratio ±2% Line : IC	L _p (μH) ±10%	L _L (μH) Max	C _{ww} (pF) Max	DCR (Ohms) Max Line / IC	THD (Linearity) Typical	Longitudinal Balance Min	Insertion Loss Max	Freq. Response Max	Isolation Voltage (Vrms)
AEP900-T AEP900-S	TH SMD	AMDSL134 CO	2 : 1	410	8	60	0.90 / 0.50	-80dB 100kHz	-40dB 25kHz - 1MHz	0.5dB 100kHz	1.0dB 30kHz-1.1MHz	1500
AEP910-T AEP910-S	TH SMD	AMDSL134 CPE	1 : 1	410	8	60	0.90 / 1.00	-80dB 100kHz	-40dB 25kHz - 1MHz	0.5dB 100kHz	1.0dB 30kHz-1.1MHz	1500

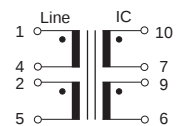
Package "S"



Package "T"



Schematic



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