

# TT2234 —

# NPN Triple Diffused Planar Silicon Transistor Color TV Horizontal Deflection Output Applications

#### **Features**

- High speed.
- High breakdown voltage (VCBO=1500V).
- High reliability (Adoption of HVP process).
- · Adoption of MBIT process.
- On-chip damper diode.

## **Specifications**

#### Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		1500	V
Collector-to-Emitter Voltage	VCEO		800	V
Emitter-to-Base Voltage	VEBO		5	V
Collector Current	IC		5	А
Collector Current (Pulse)	ICP		12	А
Collector Dissipation	PC		3.0	W
		Tc=25°C	55	W
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

#### Electrical Characteristics at Ta=25°C

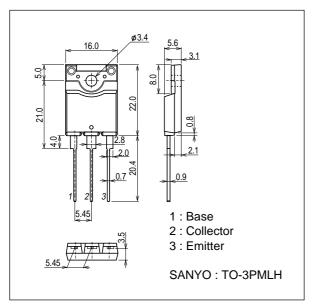
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Collector Cutoff Current	ICBO	VCB=800V, IE=0A			10	μΑ
Collector Cutoff Current	ICES	V <sub>CE</sub> =1500V, R <sub>BE</sub> =0Ω			1.0	mA
Collector Sustain Voltage	V <sub>CEO</sub> (sus)	IC=100mA, IB=0A	800			V
Emitter Cutoff Current	IEBO	VBE=4V, IC=0A	40		130	mA
DC Current Gain	hFE1	V <sub>CE</sub> =5V, I <sub>C</sub> =0.5A	10			
	hFE2	V <sub>CE</sub> =5V, I <sub>C</sub> =3A	5		8	
Collector-to-Emitter Saturation Voltage	VCE(sat)	IC=2.7A, IB=0.54A			3	V
Base-to-Emitter Saturation Voltage	V <sub>BE</sub> (sat)	IC=2.7A, IB=0.54A			1.5	V
Diode Forward Voltage	VF	IEC=4A			2	V
Fall Time	tf	IC=1.8A, IB1=0.36A, IB2=-0.72A			0.3	μs

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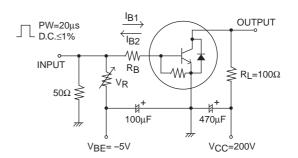
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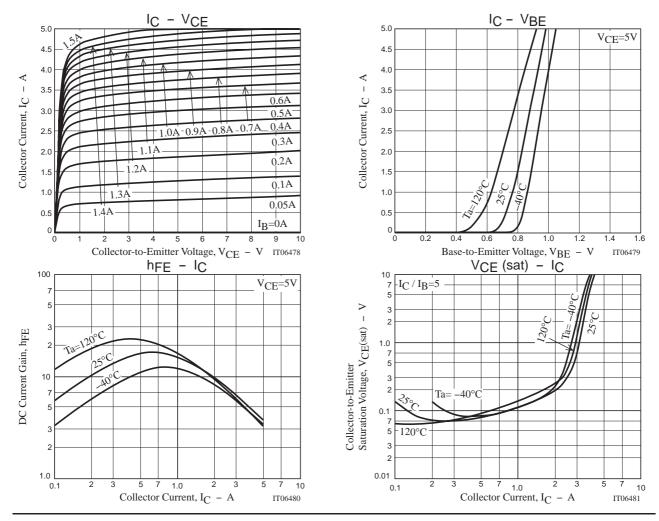
#### Package Dimensions

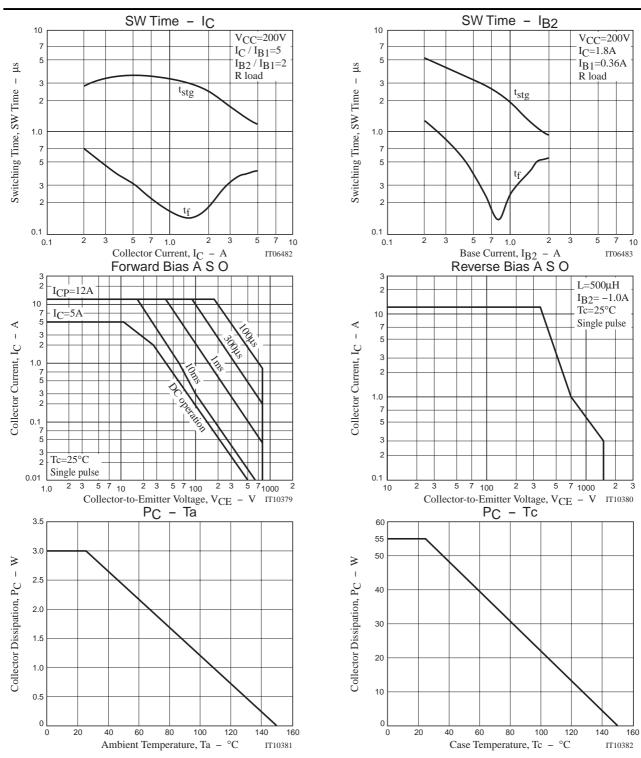
unit : mm 7504-001



### Switching Time Test Circuit







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