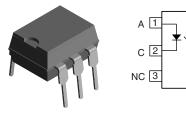
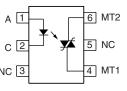


Vishay Semiconductors

Optocoupler, Phototriac Output, Low Input Current





DESCRIPTION

i179035

The IL440 consists of a GaAs infrared emitter optically coupled to a silicon planar triac chip with a non-zero crossing network. The two semiconductors are assembled in a 6 pin dual-in-line plastic package. The IL440 can handle currents up to 100 mA RMS.

AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- CSA 93751
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 pending available with option 1
- BAB/BSI IEC 60950; IEC 60065

FEATURES

- 400 V blocking voltage
- 5.0 mA maximum trigger current
- Isolation test voltage, 5300 V_{RMS}, t = 1.0 s
- Isolation materials per UL94
- · Pin compatible with optocouplers:
- IL440-4 MOC 3021
- IL440-5 MOC 3022
- IL440-6 MOC 3023
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

APPLICATIONS

- High current triac driver
- Solid state relay
- · Switch small AC loads

| ORDER INFORMATION | | | | |
|-------------------|----------------------------------------------------------------------------|--|--|--|
| PART | REMARKS | | | |
| IL440-4 | 15 mA I _{FT} , 400 V V _{DRM} , DIP-6 | | | |
| IL440-5 | 10 mA I _{FT} , 400 V V _{DRM} , DIP-6 | | | |
| IL440-6 | 5.0 mA I _{FT} , 400 V V _{DRM} , DIP-6 | | | |
| IL440-4X007 | 15 mA I _{FT} , 400 V V _{DRM} , SMD-6 (option 7) | | | |
| IL440-4X009 | 15 mA I _{FT} , 400 V V _{DRM} , SMD-6 (option 9) | | | |
| IL440-5X006 | 10 mA I _{FT} , 400 V V _{DRM} , DIP-6 400 mil (option 6) | | | |
| IL440-5X009 | 10 mA I _{FT} , 400 V V _{DRM} , SMD-6 (option 9) | | | |
| IL440-6X006 | 5.0 mA I _{FT} , 400 V V _{DRM} , DIP-6 400 mil (option 6) | | | |
| IL440-6X007 | 5.0 mA I _{FT} , 400 V V _{DRM} , SMD-6 (option 7) | | | |
| IL440-6X009 | 5.0 mA I _{FT} , 400 V V _{DRM} , SMD-6 (option 9) | | | |

Note

For additional information on the available options refer to option information.





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| PARAMETER | TEST CONDITION | PART | SYMBOL | VALUE | UNIT |
|-------------------------------------------------------------------------|------------------------------------------------------------------|----------|---------------------|--------------------|------------------|
| INPUT | | <u> </u> | | | - |
| Reverse voltage | | | V _R | 5.0 | V |
| Forward current | | | I _F | 60 | mA |
| Surge current | P.W. < 10 μs | | I _{FSM} | 3.0 | А |
| Power dissipation | | | P _{diss} | 100 | mW |
| Junction temperature | | | Т _і | 100 | °C |
| OUTPUT | · | • | | | |
| | | IL440-4 | V _{DRM} | 400 | V |
| Peak off-state voltage | | IL440-5 | V _{DRM} | 400 | V |
| | | IL440-6 | V _{DRM} | 400 | V |
| On-state RMS current | | | I _{D(RMS)} | 100 | mA |
| Peak surge current | t _p ≤ 10 ms | | I _{FSM} | 1.2 | А |
| Peak on-state current | $t_p/T=0.01\leq 100~\mu s$ | | I _{DRM} | 2.0 | А |
| Power dissipation | | | P _{diss} | 300 | mW |
| Junction temperature | | | Tj | 125 | °C |
| COUPLER | | | | | |
| Isolation voltage, 1.0 s, per standard climate 23 °C/50 % RH, DIN 50014 | | | V _{ISO} | 5300 | V _{RMS} |
| Creepage distance | | | | ≥ 7.0 | mm |
| Clearance distance | | | | ≥ 7.0 | mm |
| legistion registeres | $V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$ | | R _{IO} | ≥ 10 ¹² | Ω |
| Isolation resistance | $V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$ | | R _{IO} | ≥ 10 ¹¹ | Ω |
| Total power dissipation | | | P _{tot} | 330 | mW |
| Storage temperature range | | | T _{stg} | - 55 to + 125 | °C |
| Ambient temperature | | | T _{amb} | - 40 to + 100 | °C |
| Junction temperature | | | Ti | 100 | °C |
| Lead soldering temperature ⁽²⁾ | 2.0 mm from case, t < 10 s | | T _{sld} | 260 | °C |

Notes

(1) T_{amb} = 25 °C, unless otherwise specified. Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

⁽²⁾ Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

| ELECTRICAL CHARACTERISTICS | | | | | | |
|--------------------------------------------|---------------------------------------------------------|-----------------------|------|------|------|------|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| INPUT | | | | | | |
| Forward voltage | I _F = 50 mA | V _F | | 1.25 | | V |
| Reverse voltage | I _R = 10 μA | V _R | 5.0 | | | V |
| Junction capacitance | V _R = 0 V, f = 1.0 MHz | Cj | | 50 | | pF |
| OUTPUT ⁽²⁾ | | | | | | |
| Peak on-state voltage | I _{TM} = 100 mA, I _{FT} = 30 mA | V _{TM} | | 1.5 | 3.0 | V |
| Critical rate of rise of off-state voltage | $I_{F} = 0, V_{S} = 240 V_{RMS}$ | dV _{cr} /dt | | 50 | | V/µs |
| | $I_F = 30 \text{ mA}, \text{ V}_S = 60 \text{ V}_{RMS}$ | dV _{crq} /dt | 0.13 | 0.25 | | V/µs |
| COUPLER | | | | | | |
| Holding current | $I_F \ge 10$ mA, $V_S \ge 3.0$ V | Ι _Η | | 1.0 | | mA |

Notes

⁽¹⁾ $T_{amb} = 25$ °C, unless otherwise specified. Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

⁽²⁾ Off-state output terminal voltage (see table 1.)



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| IFT AND BLOCKING VOLTAGE SELECTION | | | | | | |
|---------------------------------------------------------------------------------------|-----|-----|--|--|--|--|
| BIN NUMBER B_V (V) MAXIMUM at I_{DRM} = 500 nA I_{FT} MAXIMUM at V_T = 6 V, R | | | | | | |
| IL440-4 | 400 | 15 | | | | |
| IL440-5 | 400 | 10 | | | | |
| IL440-6 | 400 | 5.0 | | | | |

Note

Test voltage must be applied within dV/dt rating of 0.13 V/ μ s.

| MAXIMUM SAFETY RATINGS ⁽¹⁾ | | | | | | | |
|---------------------------------------|----------------|------------------------|------|------|------|------|--|
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT | |
| INPUT ⁽²⁾ | • | | | • | • | • | |
| Forward voltage | | I _{S, INPUT} | | | 130 | mA | |
| OUTPUT | · | | | | | | |
| Power dissipation | | P _{S, OUTPUT} | | | 300 | mW | |

Notes

(1) According to DIN EN 60747-5-2(VDE 0884)/ DIN EN 60747-5-5 pending (see figure 1). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

⁽²⁾ The device is used for protective separation agains electrical shock within the maximum safety ratings. This must be ensured by protective circuits in the applications.

| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
|---------------------------------------------------------|----------------------------------------------------------|-----------------|------|-----------|---------------------------|-------------------|
| Climatic classification (according to IEC 68 part 1) | | | | 55/100/21 | | |
| Pollution degree | DIN VDE 0109 | | | 2 | | |
| Comparative tracking index | | CTI | 175 | | | |
| V _{IOTM} | | | 8000 | | | V _{peak} |
| V _{IORM} | | | 890 | | | V _{peak} |
| Insulation resistance at 25 °C | V _{IO} = 500 V | R _{IS} | | | ≥ 10 ¹² | Ω |
| Insulation resistance at T _S | V _{IO} = 500 V | R _{IS} | | | ≥ 10 ⁹ | Ω |
| Insulation resistance at 100 °C | V _{IO} = 500 V | R _{IS} | | | ≥ 10 ¹¹ | Ω |
| Partial discharge test voltage | Method a, V _{pd} = V _{IORM} x 1.875 | V _{pd} | | | 1669 | V _{peak} |
| P _{SO} | | | | | 500 | mW |
| I _{SI} | | | | | 250 | mA |
| T _{SI} | | | | | 175 | °C |
| Clearance distance | standard DIP-6 | | 7 | | | mm |
| Creepage distance | standard DIP-6 | | 7 | | | mm |
| Clearance distance | 400 mil DIP-6 | | 8 | | | mm |
| Creepage distance | 400 mil DIP-6 | | 8 | | | mm |

Note

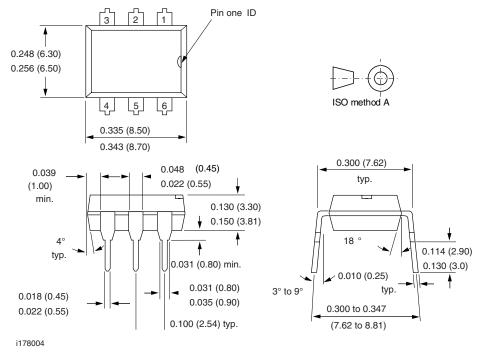
As per IEC60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of prodective circuits.

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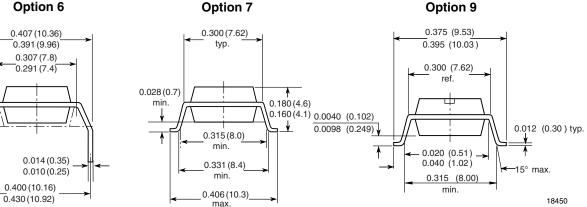
PACKAGE DIMENSIONS in inches (millimeters)



PACKAGE DIMENSIONS in inches (millimeters)









Optocoupler, Phototriac Output, Low Input Current

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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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