

## DESCRIPTION

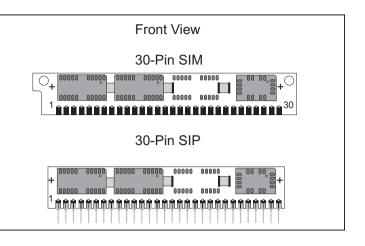
The Accutek AK59256A high density memory modules is a random access memory organized in 256K x 9 bit words. The assembly consists of two 256K x 4 and one 256K x 1 DRAMs in surface mount packages mounted to the front side of a printed circuit board. The module can be configured as a leadless 30 pad SIM or a leaded 30 pin SIP. This packaging approach provides a better than 6 to 1 density increase over standard DIP packaging.

The operation of the AK59256A is identical to two 256K x 4 DRAMs plus one 256K x 1 DRAM. For the lower eight bits data input is tied to data output and brought out separately for each 256K x 4 device, with common RAS, CAS and WE control. The OE pins are tied to Vss which dictates the use of early-write cycles to prevent contention of D and Q. Since the Write-Enable (WE) signal must always go low before CAS in a write cycle, Read-Write and Read-Modify-Write operation is not possible. For the ninth bit, the data input (D<sub>9</sub>) and data output (Q<sub>9</sub>) pins are brought out separately and controlled by a separate PCAS for that bit. Bit nine is generally used for parity.

## FEATURES

- 262,144 x 9 bit organization
- Optional 30 Pad SIM (Single In-Line Module) or 30 Pin leaded SIP (Single In-Line Package)
- JEDEC standard pinout
- Common  $\overline{CAS}$ ,  $\overline{RAS}$  and  $\overline{WE}$  control for the lower eight bits
- Separate PCAS control for D<sub>9</sub> and Q<sub>9</sub>

# AK59256AS / AK59256AG 262,144 Word by 9 bit, CMOS Dynamic Random Access Memory



- Power: 1.26 Watt Max Active (70 nSEC) 1.10 Watt Max Active (80 nSEC) .935 Wat Max Active (100 nSEC) 22 mWatt Standby (Max)
- Operating free air temperature: 0° to 70°C
- Upward compatible with AK491024, AK591024, AK594096 and AK5916384

FUNCTIONAL DIAGRAM

- · Functionally and Pin compatible with AK49256
- · Available with access times of 60 nSEC to 100 nSEC

# **PIN NOMENCLATURE**

		_		
DQ <sub>1</sub> - DQ <sub>8</sub>	Data In / Data Out	F	PIN #	
D <sub>9</sub>	Data In	$\left  \right $	1	
Q9	Data Out		3	
A <sub>0</sub> - A <sub>9</sub>	Address Inputs		4	
CAS, PCAS	Column Address Strobe	┝	5	
RAS	Row Address Strobe		7	
WE	Write Enable		8	
Vcc	5v Supply	┝	9 10	-
Vss	Ground	$\left  \right $	10	
NC	No Connect		12	
			13	

## **MODULE OPTIONS**

Leadless SIM: AK59256AS

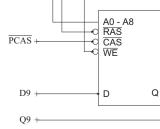
Leaded SIP: AK59256AG

# PIN ASSIGNMENT

	PIN #	SYMBOL	PIN #	SYMBOL
+	1	Vcc	16	DQ5
	2	CAS	17	A8
	3	DQ1	18	NC
1	4	A0	19	NC
+	5	A1	20	DQ6
	6	DQ2	21	WE
	7	A2	22	Vss
1	8	A3	23	DQ7
+	9	Vss	24	NC
	10	DQ3	25	DQ8
	11	A4	26	Q9
1	12	A5	27	RAS
	13	DQ4	28	PCAS
	14	A6	29	D9
	15	A7	30	Vcc

DIN #

#### A0 - A8 A0 - A8 RAS RAS CAS CAS WE WE DQ1 DO1 -DQ2 DO2 DQ3 DO3 OE 0 DQ4 DO4 A0 - A8 RAS CAS -WE DQ5 DQ1 DQ6 DQ2 DQ7 DQ3 OE DQ8 DQ4 A0 - A8 RAS



0	RDERING INFORMATION				
	ART NUMBER CODING INTERPRETATION				
	sition 1 2 3 4 5 6 7 8				
1	Product				
	AK = Accutek Memory				
2	Туре				
	4 = Dynamic RAM 5 = CMOS Dynamic RAM 6 = Static RAM				
3	Organization/Word Width				
	1 = by 1 16 = by 16 4 = by 4 32 = by 32 8 = by 8 36 = by 36 9 = by 9				
4	Size/Bits Depth				
	64 = 64K 4096 = 4 MEG 256 = 256K 8192 = 8 MEG 1024 = 1 MEG 16384 = 16 MEG				
5	Package Type				
6	G = Single In-Line Package (SIP) S = Single In-Line Module (SIM) D = Dual In-Line Package (DIP) W = .050 inch Pitch Edge Connect Z = Zig-Zag In-Line Package (ZIP) Special Designation				
•	P = Page Mode N = Nibble Mode K = Static Column Mode W = Write Per Bit Mode V = Video Ram				
7	Separator				
	<ul> <li>Commercial 0<sup>0</sup>C to +70<sup>0</sup>C</li> <li>M = Military Equivalent Screened (-55<sup>0</sup>C to +125<sup>0</sup>C)</li> <li>I = Industrial Temperature Tested (-45<sup>0</sup>C to +85<sup>0</sup>C)</li> <li>X = Burned In</li> </ul>				
8	Speed (first two significant digits) DRAMS SRAMS				
	$50 = 50 \text{ nS} \qquad 8 = 8 \text{ nS} \\60 = 60 \text{ nS} \qquad 10 = 10 \text{ nS} \\70 = 70 \text{ nS} \qquad 12 = 12 \text{ nS} \\80 = 80 \text{ nS} \qquad 15 = 15 \text{ nS}$				

The numbers and coding on this page do not include all variations available but are show as examples of the most widely used variations. Contact Accutek if other information is required.

## EXAMPLES:

### AK59256AGP-60

256K x 9, 60 nSEC, DRAM, SIP Configuration, 30 Pin, Page Mode

## AK59256ASP-70

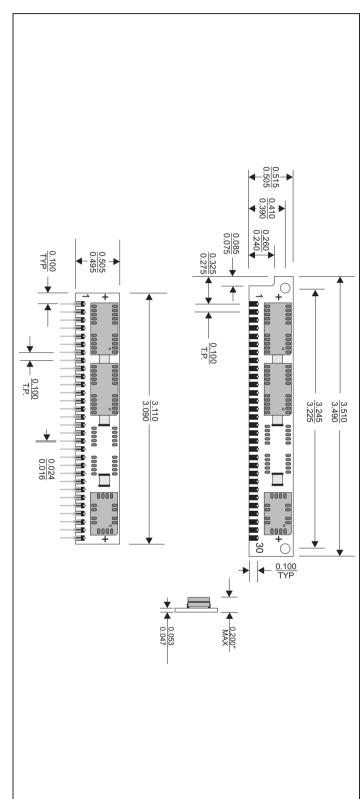
256K x 9, 70 nSEC, DRAM, SIM Configuration, 30 Pin, Page Mode



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## **MECHANICAL DIMENSIONS**

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Inches
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Accutek reserves the right to make changes in specifications at any time and without notice. Accutek does not assume any responsibility for the use of any circuitry described; no circuit patent licenses are implied. Preliminary data sheets contain minimum and maximum limits based upon design objectives, which are subject to change upon full characterization over the specific operating conditions.