

## **SANYO Semiconductors**

## DATA SHEET

LA5777 —

# Monolithic Linear IC Separately-excited Step-down Switching Regulator (5V)

#### Overview

The LA5777 is a Separately-excited step-down switching regulator (5V).

#### **Functions**

- High efficiency.
- Six external parts.
- Time-base generator (160kHz) incorporated.
- Current limiter incorporated.
- Thermal shutdown circuit incorporated.
- ON/OFF function.

#### **Specifications**

#### **Absolute Maximum Ratings** at Ta = 25°C

| Parameter                          | Symbol              | Conditions                  | Ratings     | Unit |
|------------------------------------|---------------------|-----------------------------|-------------|------|
| Maximum Input voltage              | V <sub>IN</sub> max |                             | 30          | V    |
| Maximum Output current             | I <sub>O</sub> max  |                             | 3           | Α    |
| SW pin application reverse voltage | V <sub>SW</sub>     |                             | -1          | V    |
| Allowable power dissipation        | Pd max1             | Infinitely large heat sink. | 7.5         | W    |
|                                    | Pd max2             | Independent IC.             | 1.75        | W    |
| Operating temperature              | Topr                |                             | -30 to +125 | °C   |
| Storage temperature                | Tstg                |                             | -40 to +150 | °C   |

#### **Recommended Operating Conditions** at Ta = 25°C

| Parameter           | Symbol   | Conditions | Ratings | Unit |
|---------------------|----------|------------|---------|------|
| Input voltage range | $V_{IN}$ |            | 8 to 28 | V    |

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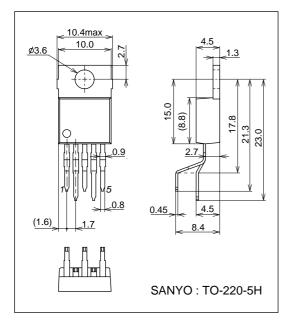
### **Electrical Characteristics** at Ta = 25°C, $V_O = 3.3$ V

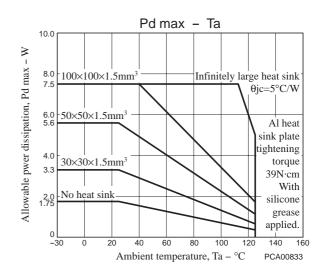
| Parameter   | Symbol               | Conditions  | Ratings |      |          | Unit  |
|---|----------------------|---|---------|------|----------|-------|
|   |                      |   | min     | typ  | max      | Unit  |
| Reference voltage   | ٧o                   | V <sub>IN</sub> = 15V, I <sub>O</sub> = 1.0A        | 4.80    | 5.00 | 5.20     | V     |
| Efficiency  | η                    | V <sub>IN</sub> = 15V, I <sub>O</sub> = 1.0A        |         | 84   |          | %     |
| Switching frequency   | f                    | V <sub>IN</sub> = 15V, I <sub>O</sub> = 1.0A        | 128     | 160  | 192      | kHz   |
| Switching frequency when short-circuit protection is active | fshort               | V <sub>IN</sub> = 15V, V <sub>OS</sub> = 0V         | 15      | 30   | 45       | kHz   |
| Line regulation   | ΔV <sub>O</sub> LINE | V <sub>IN</sub> = 8 to 20V, I <sub>O</sub> = 1.0A   |         | 40   | 100      | mV    |
| Load regulation   | ΔV <sub>O</sub> LOAD | V <sub>IN</sub> = 15V, I <sub>O</sub> = 0.5 to 1.5A |         | 10   | 30       | mV    |
| Output voltage temperature coefficient                      | ∆V <sub>O</sub> /∆Ta | Designed target value. *                            |         | ±0.5 |          | mV/°C |
| Ripple attenuation factor                                   | RREJ                 | f = 100 to 120Hz                                    |         | 45   |          | dB    |
| Output leak current   | l <sub>O</sub> leak  | V <sub>IN</sub> = 15V, SW <sub>OUT</sub> = -0.4V    |         |      | 50       | μА    |
| Current limiter operating voltage                           | IS                   | V <sub>IN</sub> = 15V                               | 3.1     |      |          | Α     |
| Operating current   | IVIN                 | V <sub>IN</sub> = 15V                               |         | 5.6  |          | mA    |
| Standby current   | ISTBY                | V <sub>IN</sub> = 15V, ENA = 5V                     |         | 50   | 100      | μΑ    |
| ENA pin LOW voltage range                                   | V <sub>ENA</sub> L   |   |         |      | 0.6      | V     |
| ENA pin HIGH voltage range                                  | V <sub>ENA</sub> H   |   | 2.4     |      | $V_{IN}$ | V     |
| Thermal shutdown operating temperature                      | TSD                  | Designed target value. *                            |         | 165  |          | °C    |
| Thermal shutdown Hysteresis width                           | ΔTSD                 | Designed target value. *                            |         | 15   |          | °C    |

<sup>\*</sup> Design target value: No measurement made.

## **Package Dimensions**

unit : mm (typ) 3079A

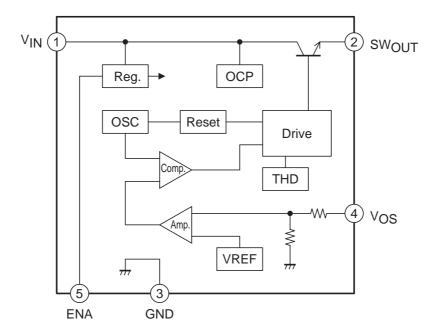




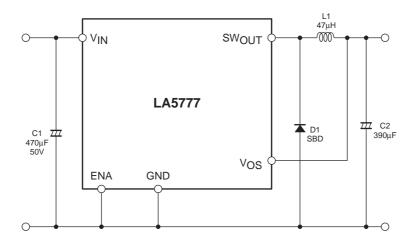
## **Pin Assignment**

(1)  $V_{\mbox{\footnotesize{IN}}}$  (2)  $SW_{\mbox{\footnotesize{OUT}}}$  (3)  $\mbox{\footnotesize{GND}}$  (4)  $\mbox{\footnotesize{V}}_{\mbox{\footnotesize{OS}}}$  (5)  $\mbox{\footnotesize{ENA}}$ 

## **Block Diagram**



## **Application Circuit Example**



Note: ENA pin starts operation with LOW voltage input.

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