





General Description

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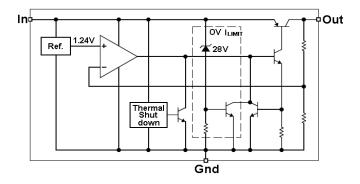
The TS4274 series of fixed-voltage monolithic micro-power voltage regulators is designed for a wide range of applications. This device excellent choice of use in battery-power application. Furthermore, the quiescent current increases on slightly at dropout, which prolongs battery life.

This series of fixed-voltage regulators features very low ground current (200uA Typ.) and very low drop output voltage (600mV at 400mA). This includes a tight initial tolerance of 2% typ., and very low output temperature coefficient.

Features

- Dropout voltage typically 0.6V @lo=400mA
- Output current up to 400mA
- Output voltage trimmed before assembly
- Load dump protection
- Internal current limit
- Thermal shutdown protection

Block Diagram



Ordering Information

Part No.	Package	Packing
TS4274CZ <u>xx</u> C0	TO-220	50pcs / Tube
TS4274CM <u>xx</u> RN	TO-263	800pcs / 13" Reel
TS4274CP <u>xx</u> RO	TO-252	2.5Kpcs / 13" Reel
TS4274CW xx RP	SOT-223	2.5Kpcs / 13" Reel

Note: Where xx denotes voltage option, available are

12= 12V
50= 5.0V
33= 3.3V
25= 2.5V
18= 1.8V

Absolute Maximum Rating

Parameter	Symbol	Limit	Unit
Input Supply Voltage	V _{IN}	-18 ~ +35	V
Operation Input Supply Voltage	V _{OPR}	26	V
Power Dissipation	P _D	Internally Limited	W
Junction Temperature Range	TJ	+150	°C
Operating Temperature Range	T _{OPR}	-40 ~ + 85	°C
Storage Temperature Range	T _{STG}	-65 ~ +150	°C



Electrical Characteristics (V_{IN}=V_{OUT}+1V, I_L=5mA, C_O=10uF, T_A=25°C, unless otherwise noted)

Parameter	Conditions	Min	Тур	Мах	Unit
		2.40	2.5	2.60	V
Output Voltage	$5mA \le I_L \le 500mA$, Vo+1V $\le V_{IN} \le 26V$	3.17	3.3	3.43	
	$V0+1V \le V_{\rm IN} \le 20V$	4.80	5.0	5.20	
Input Supply Voltage				26	V
Output Voltage Temperature Coefficient			50	150	ppm/ °C
Line Regulation	$Vo+1V \le V_{IN} \le 26V$		0.05	0.5	%
Load Regulation	$5\text{mA} \le I_L \le 400\text{mA}$		0.2	2.0	%
	I _L =100mA		100	200	
Dropout Voltage (Note 4)	I _L =250mA		300	400	mV
	I _L =400mA		500	600	
	I _L =100uA		200	300	uA
Quiescent Current (Note 5)	I _L =250mA		10	15	mA
	I _L =400mA		25	35	mA
Short Circuit Current (Note 6)	V _{OUT} =0		600	900	mA
Output Naisa	C _L =2.2uF		500		
Output Noise,	C _L =3.3uF		350		uVrms
10Hz to 100KHz, I _L =10mA	C _L =33uF		120		
Power Supply Ripple Rejection	F=100Hz, Vr=0.5Vss		60		dB

Thermal Performance

Condition	Package type	Тур	Unit
	TO-220	60	
Thermal Resistance	TO-263	80	⁰ 0000
Junction to Ambient	TO-252	150	°C/W
	SOT-223	170	

Note 1: Absolute Maximum Rating is limits beyond which damage to the device may occur. For guaranteed specifications and test conditions see the electrical characteristics.

Note 2: Maximum positive supply voltage of 35V must be limited duration (<100mS) and duty cycle (<1%).

Note 3: The maximum allowable power dissipation is a function of the maximum junction temperature, T_J , the junction to ambient thermal resistance, θ_{JA} , and the ambient temperature, Ta. Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. The effective value of θ_{JA} can be reduced by using a heatsink.

Note 3: Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.

Note 4: Ground pin current is the regulator quiescent current. The total current drawn from the source is the sum of the ground pin current and output load current.

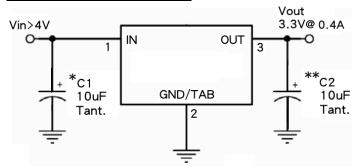
Note 5: Output current will decrease with increasing temperature, but it will be not dropped below 400mA at the maximum specified temperature.



Application Information

The TS4274 series is a high performance with low dropout voltage regulator suitable for moderate to high current and voltage regulator application. Its 600mV(typ) dropout voltage at full load and over temperature makes it especially valuable in battery power systems and as high efficiency noise filters in post regulator applications. Unlike normal NPN transistor design, where the base to emitter voltage drop and collector to emitter saturation voltage limit the minimum dropout voltage, dropout performance of the PNP output of these devices is limited only by low Vce saturation voltage. The TS4274 series is fully protected from damage due to fault conditions. Linear current limiting is provided. Output current during overload conditions is constant. Thermal shutdown the device when the die temperature exceeds the maximum safe operating temperature. Transient protection allows device survival even when the input voltage spikes above and below nominal. The output structure of these regulators allows voltages in excess of the desired output voltage to be applied without reverse current flow.

Typical Application Circuit



Output Capacitor

The TS4274 series requires an output capacitor to maintain stability and improve transient response. Proper capacitor selection is important to ensure proper operation. The output capacitor selection is dependent upon the ESR of the output capacitor the maintain stability. When the output capacitor is 10uF or greater, the output capacitor should have an ESR less than 2 ohm. This will improve transient response as well as promoted stability. Ultra low ESR capacitors (<100mohm), such as ceramic chip capacitors may promote instability. These very low ESR levels may cause an oscillation and/or under damped transient response. A low ESR solid tantalum capacitor works extremely well and provides good transient response and stability over temperature. Aluminum electrolytic can also be used, as long as the ESR of the capacitor is <20hm. The value of the output capacitor can be increased without limit. Higher capacitance values help to improved transient response and ripple rejection and reduce output noise.

Minimum Load Current

The TS4274 series is specified between finite loads. If the output current is too small leakage currents dominate and the output voltage rises. A 1mA minimum load current is necessary for proper regulation.

Input Capacitor

An input capacitor of 1uF or greater is recommended when the device is more that 4 inches away from the bulk AC supply capacitance or when the supply is a battery. Small and surface mount ceramic chip capacitors can be used for bypassing. Larger values will help to improve ripple rejection by bypassing the input to the regulator, further improving the integrity of the output voltage.



TS4274

Application Information (Continue)

Thermal Characteristics

A heatsink may be required depending on the maximum power dissipation and maximum ambient temperature of the application. Under all possible operating conditions, the junction temperature must be within the range specified under absolute maximum ratings. To determine if the heatsink is required, the power dissipated by the regulator, P_D must be calculated.

The below formula shows the voltages and currents for calculating the P_{D} in the regulator:

IIN = IL / IG

 $P_{D} = (V_{IN} - V_{OUT}) * I_{L} + (V_{IN}) * I_{G}$

Ex. P_D = (3.3V-2.5V) * 0.5A + 3.3V * 11mA

= 400mW + 36mW

= 436mW

Remark: I_L is output load current,

I_G is ground current.

V_{IN} is input voltage

V_{OUT} is output voltage

The next parameter which must be calculated is the maximum allowable temperature rise.

TR(max) is calculated by the using to formula:

TR(max) = TJ(max) - TA(max)

Where: TJ(max) is the maximum allowable junction temperature, which is 125° C for commercial grade parts. T_A(max) is the maximum ambient temperature which will be encountered in the application.

Using the calculated values for TR(max) and PD, the maximum allowable value for the junction to ambient thermal resistance, θ_{JA} , can now be found:

 $\theta_{JA} = TR(max) / PD$

IMPORTANT: if the maximum allowable value for is found to be $\ge 60^{\circ}$ C /W for the TO-220 package, $\ge 80^{\circ}$ C/W for the TO-263 package, $\ge 150^{\circ}$ C/W for the TO-252 package, or $\ge 170^{\circ}$ C /W for the SOT-223 package, no heatsink is needed since the package alone will dissipate enough heat to satisfy these requirements. If the calculated value for θ_{JA} falls below these limits, a heatsink is required.



Application Information (Continue)

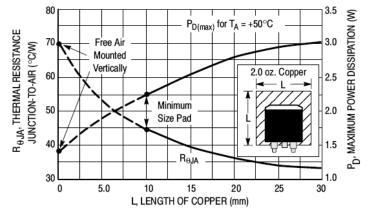


Figure 1 – D²PAK Thermal Resistance and Maximum Power Dissipation vs. P.C.B Copper Length

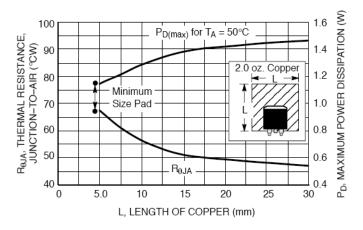


Figure 2 – DPAK Thermal Resistance and Maximum Power Dissipation vs. P.C.B Copper Length

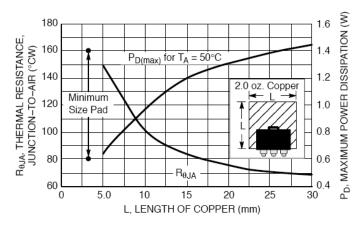
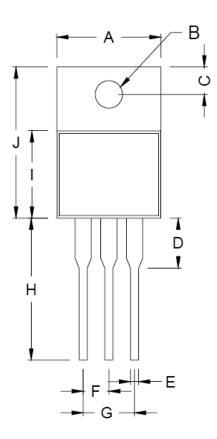
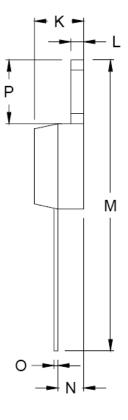


Figure 3 – SOT-223 Thermal Resistance and Maximum Power Dissipation vs. P.C.B Copper Length



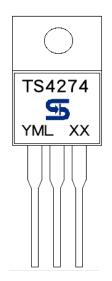
TO-220 Mechanical Drawing





	TO-220 DIMENSION				
DIM	MILLIMETERS		INCHES		
DIIVI	MIN	MAX	MIN	MAX	
Α	10.000	10.500	0.394	0.413	
В	3.740	3.910	0.147	0.154	
С	2.440	2.940	0.096	0.116	
D	-	6.350	-	0.250	
Е	0.381	1.106	0.015	0.040	
F	2.345	2.715	0.092	0.058	
G	4.690	5.430	0.092	0.107	
Н	12.700	14.732	0.500	0.581	
Ι	8.382	9.017	0.330	0.355	
J	14.224	16.510	0.560	0.650	
Κ	3.556	4.826	0.140	0.190	
L	0.508	1.397	0.020	0.055	
М	27.700	29.620	1.060	1.230	
Ν	2.032	2.921	0.080	0.115	
0	0.255	0.610	0.010	0.024	
Р	5.842	6.858	0.230	0.270	

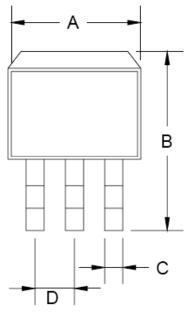
Marking Diagram

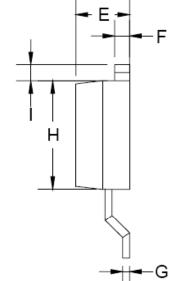


- Y = Year Code
- M = Month Code
 (A=Jan, B=Feb, C=Mar, D=Apl, E=May, F=Jun, G=Jul, H=Aug, I=Sep, J=Oct, K=Nov, L=Dec)
- L = Lot Code
- **XX** = Voltage Code (1.8=1.8V, 2.5=2.5V, 3.3=3.3V, 5.0=5V, 12=12V)



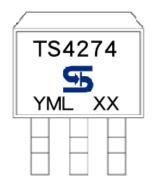
TO-263 Mechanical Drawing





	TO-263 DIMENSION				
DIM	MILLIMETERS		INCHES		
DIN	MIN	MAX	MIN	MAX	
Α	10.000	10.500	0.394	0.413	
В	14.605	15.875	0.575	0.625	
С	0.508	0.991	0.020	0.039	
D	2.420	2.660	0.095	0.105	
Е	4.064	4.830	0.160	0.190	
F	1.118	1.400	0.045	0.055	
G	0.450	0.730	0.018	0.029	
Н	8.280	8.800	0.325	0.346	
Ι	1.140	1.400	0.044	0.055	
J	1.480	1.520	0.058	0.060	

Marking Diagram



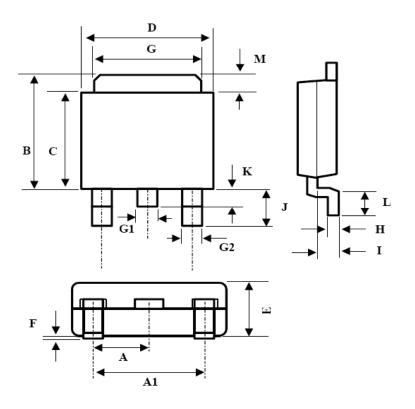
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TO-252 Mechanical Drawing



TO-252 DIMENSION				
	MILLIMETERS		INCHES	
DIM	MIN	MAX	MIN	MAX
А	2.3E	BSC	0.09	BSC
A1	4.6E	BSC	0.18BSC	
В	6.80	7.20	0.268	0.283
С	5.40	5.60	0.213	0.220
D	6.40	6.65	0.252	0.262
Е	2.20	2.40	0.087	0.094
F	0.00	0.20	0.000	0.008
G	5.20	5.40	0.205	0.213
G1	0.75	0.85	0.030	0.033
G2	0.55	0.65	0.022	0.026
Н	0.35	0.65	0.014	0.026
Ι	0.90	1.50	0.035	0.059
J	2.20	2.80	0.087	0.110
К	0.50	1.10	0.020	0.043
L	0.90	1.50	0.035	0.059
М	1.30	1.70	0.051	0.67

Marking Diagram



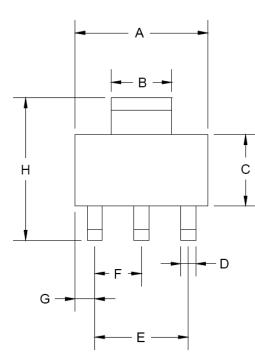
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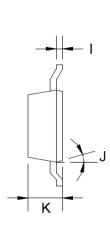
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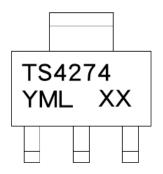
SOT-223 Mechanical Drawing





	SOT-223 DIMENSION					
DIM	MILLIMETERS		INCHES			
DIN	MIN	MAX	MIN	MAX		
А	6.350	6.850	0.250	0.270		
В	2.900	3.100	0.114	0.122		
С	3.450	3.750	0.136	0.148		
D	0.595	0.635	0.023	0.025		
Е	4.550	4.650	0.179	0.183		
F	2.250	2.350	0.088	0.093		
G	0.835	1.035	0.032	0.041		
н	6.700	7.300	0.263	0.287		
Ι	0.250	0.355	0.010	0.014		
J	10°	16°	10°	16°		
K	1.550	1.800	0.061	0.071		

Marking Diagram



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