TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC4520BP,TC4520BF,TC4520BFN

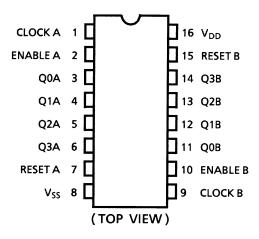
#### TC4520B Dual Binary Up Counter

TC4520B is up counters of 4 bit binary.

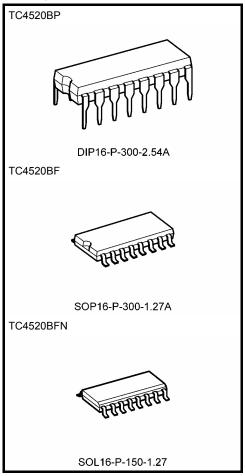
Since both of TC4520B contain two independent circuits of counters with the same functions in one package, counting or frequency division of two BCD digits or eight binary bits can be achived with one IC. The counters can be reset to "0" (Q0 $\sim$ Q3 = "L") by giving "H" level signal to RESET input regardless of other inputs

The counting condition is changed by the rising edge of CLOCK input if ENABLE = "H" or by the falling edge of ENABLE if CLOCK = "L".

#### **Pin Assignment**



Note: xxxFN (JEDEC SOP) is not available in Japan.

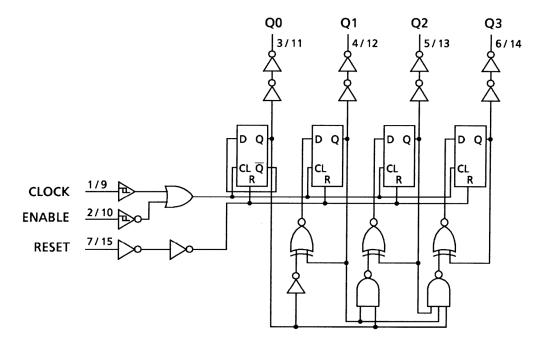


Weight

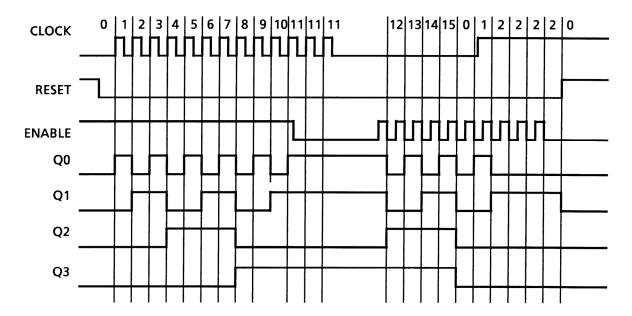
DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

2007-10-01

# **Logic Diagram**



# **Timing Chart**





#### **Absolute Maximum Ratings (Note)**

Characteristics	Symbol	Rating	Unit
DC supply voltage	$V_{DD}$	V <sub>SS</sub> - 0.5~V <sub>SS</sub> + 20	V
Input voltage	V <sub>IN</sub>	V <sub>SS</sub> - 0.5~V <sub>DD</sub> + 0.5	V
Output voltage	V <sub>OUT</sub>	V <sub>SS</sub> - 0.5~V <sub>DD</sub> + 0.5	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40~85	°C
Storage temperature range	T <sub>stg</sub>	−65 <b>~</b> 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Operating Ranges (V<sub>SS</sub> = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	$V_{DD}$	_	3	_	18	V
Input voltage	$V_{IN}$		0	_	$V_{DD}$	V

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Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .



# Static Electrical Characteristics ( $V_{SS} = 0 V$ )

		Sym-	Test Condition		-40°C			25°C			85°C	
Charac	teristics	bol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
		V <sub>OH</sub>	$ I_{OUT}  < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	5	4.95	_	4.95	5.00	_	4.95	_	
High-level output voltage	10			9.95	_	9.95	10.00	_	9.95	_	V	
			VIN - VSS, VDD	15	14.95		14.95	15.00	_	14.95	—	
l			  I <sub>OUT</sub>   < 1 μA	5	_	0.05	_	0.00	0.05	_	0.05	
Low-level voltage	output	V <sub>OL</sub>	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05	_	0.05	V
			VIN = VSS, VDD	15	_	0.05	_	0.00	0.05	_	0.05	
			V <sub>OH</sub> = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_	
			V <sub>OH</sub> = 2.5 V	5	-2.5	_	-2.1	-4.0	_	-1.7	_	mA
Output hig	h current	I <sub>OH</sub>	V <sub>OH</sub> = 9.5 V	10	-1.5	_	-1.3	-2.2	_	-1.1	_	
			V <sub>OH</sub> = 13.5 V	15	-4.0	_	-3.4	-9.0	_	-2.8	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		l <sub>OL</sub>	V <sub>OL</sub> = 0.4 V	5	0.61	_	0.51	1.2	_	0.42	_	mA
Output low	v current		V <sub>OL</sub> = 0.5 V	10	1.5	_	1.3	3.2	_	1.1	_	
Output low	Output low current		V <sub>OL</sub> = 1.5 V	15	4.0	_	3.4	12.0	_	2.8	_	
			$V_{IN} = V_{SS}, V_{DD}$									
		V <sub>IH</sub>	V <sub>OUT</sub> = 0.5 V, 4.5 V	5	3.5		3.5	2.75	_	3.5	_	V
Input high	voltago		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	7.0	_	7.0	5.5	_	7.0	_	
input nign	voitage		V <sub>OUT</sub> = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	_	
			$ I_{OUT}  < 1 \mu A$									
		V	V <sub>OUT</sub> = 0.5 V, 4.5 V	5	_	1.5	_	2.25	1.5	_	1.5	
Input low y	voltago		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	_	3.0	_	4.5	3.0	_	3.0	V
Input low voltage	V <sub>IL</sub>	V <sub>OUT</sub> = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0	V	
			$ I_{OUT}  < 1 \mu A$									
Input	"H" level	l <sub>IH</sub>	V <sub>IH</sub> = 18 V	18		0.1	_	10 <sup>-5</sup>	0.1	_	1.0	μА
current	"L" level	I <sub>Ι</sub> L	V <sub>IL</sub> = 0 V	18		-0.1	_	-10 <sup>-5</sup>	-0.1	_	-1.0	μΑ
			., ., .,	5	_	5	_	0.005	5	_	150	
Quiescent supply current		I <sub>DD</sub>	$V_{IN} = V_{SS}, V_{DD}$	10	_	10	_	0.010	10	_	300	μΑ
			(Note)	15		20	_	0.015	20		600	

Note: All valid input combinations.

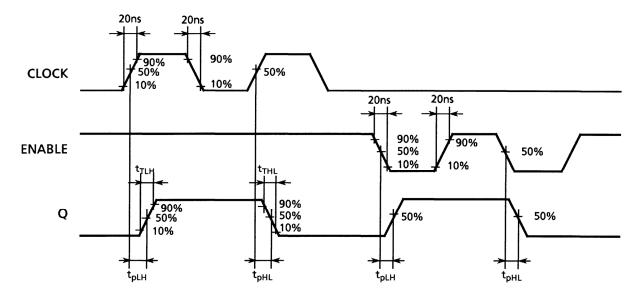


# Dynamic Electrical Characteristics (Ta = 25°C, $V_{SS}$ = 0 V, $C_L$ = 50 pF)

Observatoristis	O make at	Test Condition	N 4 !	_		1.1	
Characteristics	Symbol		V <sub>DD</sub> (V)	Min	Тур.	Max	Unit
Output transition time			5	_	70	200	
(low to high)	t <sub>TLH</sub>	_	10	_	35	100	ns
(IOW to High)			15	_	30	80	
Output transition time			5	_	70	200	
(high to low)	t <sub>THL</sub>	_	10	_	35	100	ns
(night to low)			15	_	30	80	
Propagation delay time	*		5	_	160	560	
	t <sub>pLH</sub>	_	10	_	75	230	ns
(CLOCK, ENABLE-Q)	t <sub>pHL</sub>		15	_	60	160	
Drangation dalay time			5	_	110	560	
Propagation delay time	t <sub>pHL</sub>	_	10	_	55	230	ns
(RESET-Q)			15	_	40	160	
Max clock frequency	t <sub>CL</sub>	_	5	1.5	6	_	
			10	3	14	_	MHz
			15	4	18	_	
	t <sub>rCL</sub>		5	No limit			
Max clock input rise/fall time		_	10				μS
			15				
			5				
Max input rise/fall time	t <sub>r</sub>	_	10	No limit			μS
(ENABLE)	t <sub>f</sub>		15				
	tw	_	5	_	30	200	
Min clock pulse width			10	_	15	100	ns
			15	_	10	70	
A.C. 1 110			5	_	35	250	
Min pulse width	t <sub>W</sub>	_	10	_	20	110	ns
(ENABLE)			15	_	15	80	
Min mula a middle			5	_	45	250	
Min pulse width	t <sub>WH</sub>	_	10	_	20	110	ns
(RESET)			15	_	15	80	
Min managed Aires	t <sub>rem</sub>		5	_	_	0	
Min removal time		_	10	_	_	0	ns
(RESET-CLOCK, ENABLE)			15	_	_	0	
Input capacitance	C <sub>IN</sub>	_	ı	_	5	7.5	pF

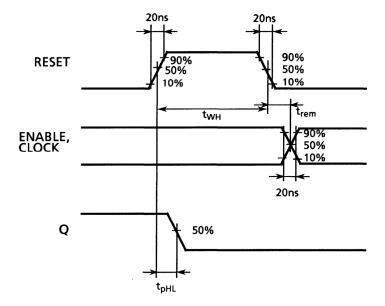
# **Waveforms for Measurement of Dynamic Characteristics**

#### Waveform 1

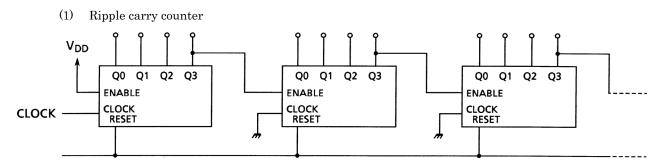


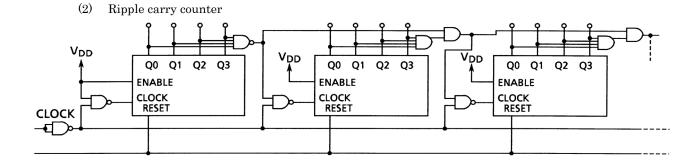
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#### Waveform 2



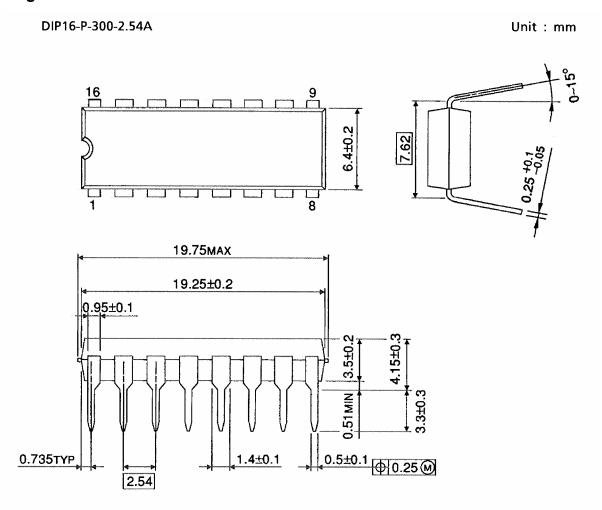
# **Application Circuit**







# **Package Dimensions**

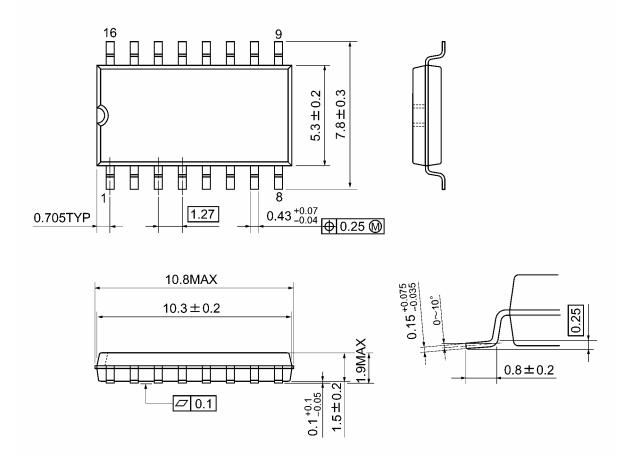


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Weight: 1.00 g (typ.)

# **Package Dimensions**

SOP16-P-300-1.27A Unit: mm

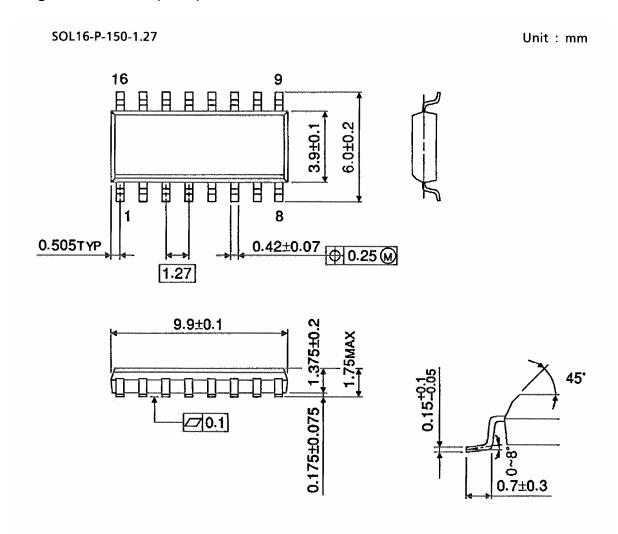


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Weight: 0.18 g (typ.)



# **Package Dimensions (Note)**



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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