TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

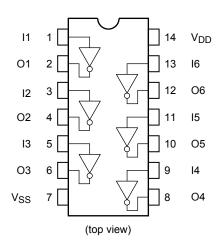
TC4069UBP,TC4069UBF,TC4069UBFN,TC4069UBFT

TC4069UB Hex Inverter

TC4069UB contains six circuits of inverters. Since the internal circuit is composed of a single stage inverter, this is suitable for the applications of CR oscillator circuits, crystal oscillator circuits and linear amplifiers in addition to its application as inverters.

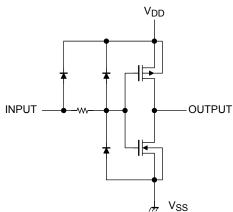
Because of one stage gate configuration, the propagation time has been reduced.

Pin Assignment

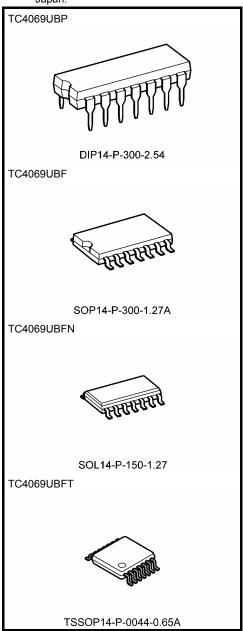


Circuit Diagram

1/6 TC4069UB



Note: xxxFN (JEDEC SOP) is not available in Japan.



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Weight DIP14-P-300-2.54 SOP14-P-300-1.27A SOL14-P-150-1.27 : 0.96 g (typ.) : 0.18 g (typ.) 0.12 g (typ.) TSSOP14-P-0044-0.65A : 0.06 g (typ.)



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V _{SS} - 0.5 to V _{SS} + 20	V
Input voltage	V _{IN}	V _{SS} - 0.5 to V _{DD} + 0.5	V
Output voltage	V _{OUT}	V _{SS} - 0.5 to V _{DD} + 0.5	V
DC input current	I _{IN}	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	−40 to 85	°C
Storage temperature range	T _{stg}	−65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0	1	V_{DD}	V

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Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .



Static Electrical Characteristics ($V_{SS} = 0 V$)

Characteristics Symbo			Test Condition		-40°C		25°C			85°C		
		Symbol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
High-level output voltage	V _{OH}	l _{OUT} < 1 μA	5 10	4.95 9.95	_	4.95 9.95	5.00 10.00	_	4.95 9.95	_	V	
		$V_{IN} = V_{SS}, V_{DD}$	15	14.95	_	14.95	15.00	_	14.95	_		
			Havel 4 UA	5	_	0.05	_	0.00	0.05	_	0.05	
Low-level output voltage	V_{OL}	$ I_{OUT} < 1 \mu A$ $V_{IN} = V_{SS}, V_{DD}$	10	_	0.05	_	0.00	0.05	_	0.05	V	
			V 40V	15		0.05	<u> </u>	0.00	0.05		0.05	
			$V_{OH} = 4.6 \text{ V}$ $V_{OH} = 2.5 \text{ V}$	5 5	-0.61	_	-0.51	-1.0	_	-0.42	_	
Output h	nigh	Гон	$V_{OH} = 2.5 \text{ V}$ $V_{OH} = 9.5 \text{ V}$	10	-2.50 -1.50		-2.10 -1.30	-4.0 -2.2	_	-1.70 -1.10	_	mA
current		ΙΟΗ	V _{OH} = 13.5 V	15	-4.00		-3.40	-2.2 -9.0		-2.80		
			V _{IN} = V _{SS}	10	4.00		0.40	0.0		2.00		
		I _{OL}	V _{OL} = 0.4 V	5	0.61	_	0.51	1.2	_	0.42	_	mA
Output lo	ow		V _{OL} = 0.5 V	10	1.50	_	1.30	3.2	_	1.10	_	
current			V _{OL} = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	
			$V_{IN} = V_{DD}$									
		V _{IH}	V _{OUT} = 0.5 V, 4.5 V	5	4.0	_	4.0	_	_	4.0	_	mA
Input hig	gh		V _{OUT} = 1.0 V, 9.0 V	10	8.0	_	8.0	_	_	8.0	_	
voltage			V _{OUT} = 1.5 V, 13.5 V	15	12.0	_	12.0	_	_	12.0	_	
		$ I_{OUT} < 1 \mu A$										
		V _{IL}	V _{OUT} = 0.5 V, 4.5 V	5	_	1.0	_	_	1.0	_	1.0	mA
Input low	v		V _{OUT} = 1.0 V, 9.0 V	10	_	2.0	_	_	2.0	_	2.0	
voltage			V _{OUT} = 1.5 V, 13.5 V	15	_	3.0	_	_	3.0	_	3.0	
			I _{OUT} < 1 μA									
Input	"H" level	I _{IH}	V _{IL} = 18 V	18	_	0.1	_	10 ⁻⁵	0.1	_	1.0	μА
current	"L" level	I _{IL}	V _{IL} = 0 V	18	_	-0.1	_	-10 ⁻⁵	-0.1	_	-1.0	
o :			$V_{IN} = V_{SS}, V_{DD}$	5	_	0.25	_	0.001	0.25	_	7.5	
Quiesce supply c		I_{DD}	(Note)	10	_	0.50	_	0.001	0.50	_	15.0	μΑ
11 3			(Note)	15	_	1.00		0.002	1.00	_	30.0	

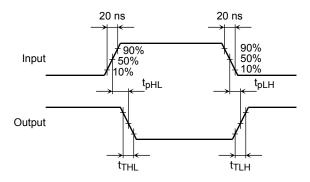
Note: All valid input combinations.



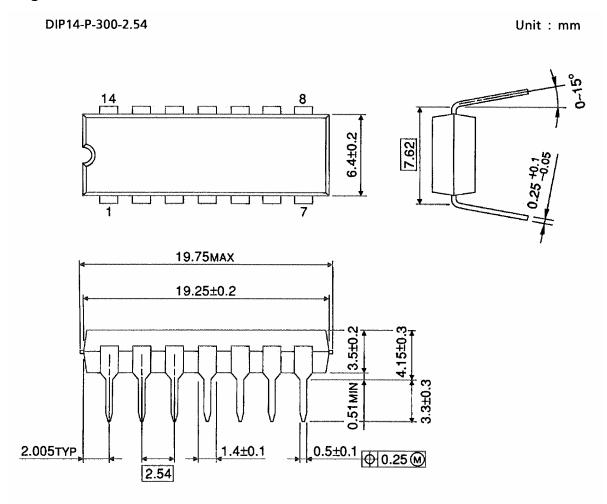
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Characteristics	Зупівої		V _{DD} (V)	IVIII I	ιyp.	IVIAX	Offic
Output transition time			5	_	70	200	
(low to high)	t _{TLH}	_	10	_	35	100	ns
(low to riigir)			15		30	80	
Output transition time			5		70	200	
Output transition time (high to low)	t_{THL}	_	10	_	35	100	ns
(High to low)			15		30	80	
Propagation delay time			5	_	55	110	
(low to high)	t _{pLH}	_	10	_	30	60	ns
(low to riigh)			15		25	50	
Propagation delay time			5	_	55	110	
(high to low)	t_{pHL}	_	10	_	30	60	ns
(night to low)			15		25	50	
Input capacitance	C _{IN}	_		_	7.5	15	pF

Waveform for Measurement of Dynamic Characteristics



Package Dimensions

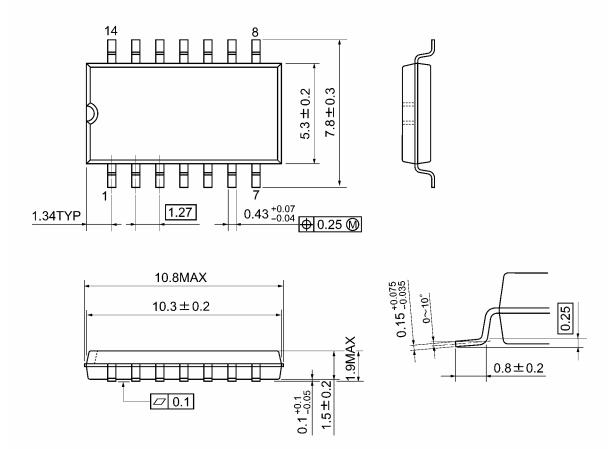


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Weight: 0.96 g (typ.)

Package Dimensions

SOP14-P-300-1.27A Unit: mm

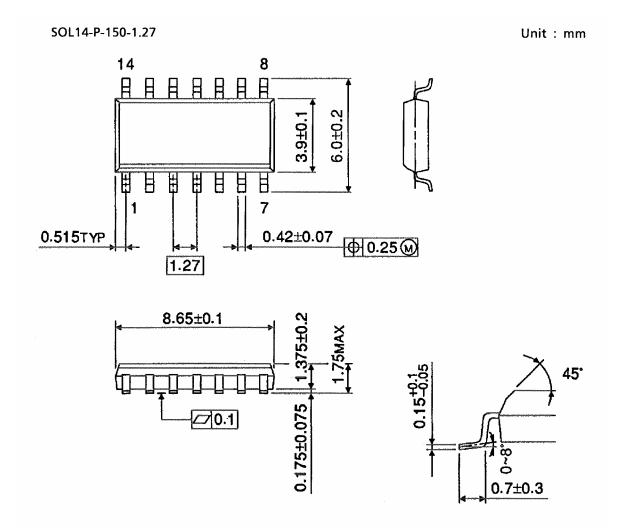


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Weight: 0.18 g (typ.)



Package Dimensions (Note)

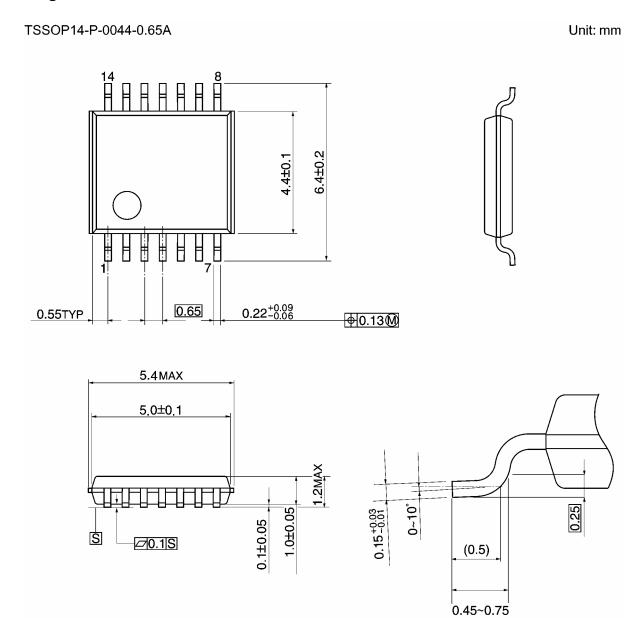


Note: This package is not available in Japan.

Weight: 0.12 g (typ.)



Package Dimensions



Weight: 0.06 g (typ.)

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20070701-EN GENERAL

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