TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

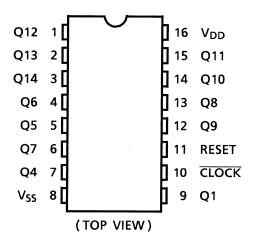
# TC4020BP,TC4020BF,TC4020BFN

TC4020B 14 Stage Ripple-Carry Binary Counter/Dividers

TC4020B is 14 stage ripple carry binary counter having asynchronous clear function. The counter advances its counting stage by falling edge of  $\overline{\text{CLOCK}}$  input. When RESET input is placed "H", all the circuits are reset regardless of  $\overline{\text{CLOCK}}$  input making all the outputs (Q1, Q4~Q14) to be "L".

This is most suitable for frequency dividers, control circuits and timing circuits.

### **Pin Assignment**



### Truth Table

$\overline{CLOCK} \Delta$	RESET	Output State
*	Н	All Outputs = "L"
	L	No Change
	L	Advance to Next State

 $\Delta$ : Level change

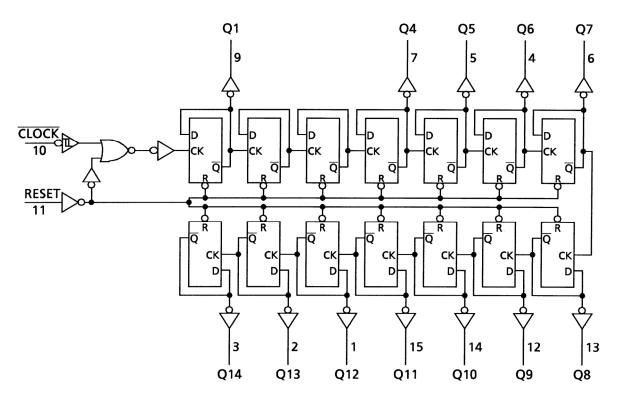
\*: Don't care

Japan. TC4020BP DIP16-P-300-2.54A TC4020BF SOP16-P-300-1.27A TC4020BFN SOL16-P-150-1.27 Weight DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.) SOL16-P-150-1.27 : 0.13 g (typ.)

Note: xxxFN (JEDEC SOP) is not available in

## <u>TOSHIBA</u>

### Logic Diagram



### Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V <sub>DD</sub>	$V_{SS} - 0.5  V_{SS} + 20$	V
Input voltage	V <sub>IN</sub>	$V_{SS} - 0.5$ ~ $V_{DD} + 0.5$	V
Output voltage	V <sub>OUT</sub>	$V_{SS} - 0.5$ ~ $V_{DD} + 0.5$	V
DC input current	I <sub>IN</sub>	±10	mA
Power dissipation	PD	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T <sub>opr</sub>	-40~85	°C
Storage temperature range	T <sub>stg</sub>	-65~150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### **Operating Ranges (V<sub>SS</sub> = 0 V) (Note)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V <sub>DD</sub>	—	3	_	18	V
Input voltage	V <sub>IN</sub>	—	0		V <sub>DD</sub>	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either  $V_{DD}$  or  $V_{SS}$ .

### Static Electrical Characteristics ( $V_{SS} = 0 V$ )

		Sym-	Test Condition	-40°C		25°C			85°C			
Charac	teristics	bol		V <sub>DD</sub> (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit
			5	4.95	_	4.95	5.00	_	4.95	_		
High-level voltage	output	V <sub>OH</sub>	I <sub>OUT</sub>   < 1 μΑ	10	9.95	_	9.95	10.00	_	9.95	—	V
			$V_{IN} = V_{SS}, V_{DD}$	15	14.95	—	14.95	15.00	—	14.95	—	
			I <sub>OUT</sub>   < 1 μΑ	5		0.05	_	0.00	0.05		0.05	
Low-level voltage	output	V <sub>OL</sub>	$V_{IN} = V_{SS}, V_{DD}$	10	—	0.05	—	0.00	0.05		0.05	V
0			VIN – VSS, VDD	15	_	0.05	—	0.00	0.05	—	0.05	
			V <sub>OH</sub> = 4.6 V	5	-0.61	—	-0.51	-1.0	_	-0.42	—	mA
			$V_{OH} = 2.5 V$	5	-2.50	_	-2.10	-4.0	_	-1.70	—	
Output hig	h current	IOH	V <sub>OH</sub> = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	—	
			V <sub>OH</sub> = 13.5 V	15	-4.00	_	-3.40	-9.0	—	-2.80	—	
			$V_{IN}=V_{SS},V_{DD}$									
		I <sub>OL</sub>	$V_{OL} = 0.4 V$	5	0.61	—	0.51	1.2	_	0.42	—	mA
	vcurrent		$V_{OL} = 0.5 V$	10	1.50	_	1.30	3.2	_	1.10	—	
Output low current	IOL	V <sub>OL</sub> = 1.5 V	15	4.00	—	3.40	12.0	—	2.80	—	IIIA	
		$V_{IN}=V_{SS},V_{DD}$										
			$V_{OUT} = 0.5 V, 4.5 V$	5	3.5	—	3.5	2.75	_	3.5	—	
Input high	voltage	VIH	V <sub>OUT</sub> = 1.0 V, 9.0 V	10	7.0	_	7.0	5.50	_	7.0	—	v
input nigh	voltage		V <sub>OUT</sub> = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.0	—	
			$ I_{OUT}  < 1 \ \mu A$									
		VIL	$V_{OUT} = 0.5 V, 4.5 V$	5		1.5	_	2.25	1.5		1.5	v
Input low y	voltage		V <sub>OUT</sub> = 1.0 V, 9.0 V	10	—	3.0	—	4.50	3.0		3.0	
input iow v	Input low voltage		$V_{OUT} = 1.5 V, 13.5 V$	15	_	4.0	—	6.75	4.0	—	4.0	
			$ I_{OUT}  < 1 \ \mu A$									
Input	"H" level	IIН	V <sub>IH</sub> = 18 V	18		0.1	_	10 <sup>-5</sup>	0.1		1.0	μA
current	"L" level	١ <sub>١L</sub>	$V_{IL} = 0 \ V$	18	_	-0.1		-10 <sup>-5</sup>	-0.1		-1.0	μΛ
				5	_	5	—	0.005	5		150	
Quiescent current	Quiescent supply current		V <sub>IN</sub> = V <sub>SS</sub> , V <sub>DD</sub> (Note)	10	—	10	—	0.010	10		300	μA
			(NOLE)	15	<u> </u>	600						

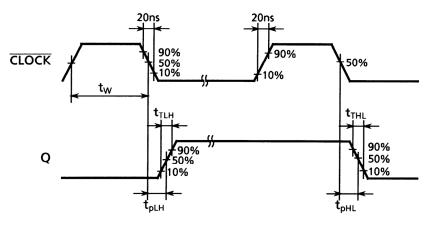
Note: All valid input combinations.

### Dynamic Electrical Characteristics (Ta = $25^{\circ}$ C, V<sub>SS</sub> = 0 V, C<sub>L</sub> = 50 pF)

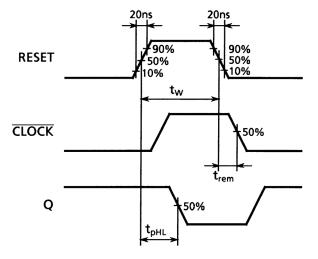
Characteristics	Symbol	Test Condition	[	Min	Тур.	Max	Unit
			V <sub>DD</sub> (V)				
Output transition time			5	—	70	200	
(low to high)	ttlh	—	10	—	35	100	ns
			15	—	30	80	
Output transition time			5	—	70	200	
(high to low)	t <sub>THL</sub>	—	10	—	35	100	ns
(g ,			15	—	30	80	
Propagation delay time			5		160	360	
(CLOCK -Q1)	t <sub>pLH</sub>	—	10	—	80	160	ns
(010011 (1))			15		65	130	
Propagation delay time			5	—	160	360	
(CLOCK -Q1)	tpHL	—	10		80	160	ns
			15	—	65	130	
Propagation delay time	t <sub>pLH</sub>	_	5	—	1000	2000	ns
$(\overline{\text{CLOCK}} - \text{Q14})$			10	—	500	1000	
			15	_	400	800	
Propagation delay time			5	—	1000	2000	
(CLOCK -Q14)	t <sub>pHL</sub>	—	10	—	500	1000	ns
			15	—	400	800	
Propagation delay time			5	—	150	280	
(RESET-Q)	t <sub>pHL</sub>	—	10	—	70	120	ns
(RE3E1-Q)			15	—	50	100	
			5	3.5	10	_	
Max clock frequency	f <sub>CL</sub>	—	10	8.0	20		MHz
			15	12.0	25	—	
Min clock nulse width			5		50	140	
Min clock pulse width	t <sub>VV</sub>	_	10	—	20	60	ns
(RESET)			15	—	15	40	
			5		100	200	
Min pulse width	tw	_	10	—	40	80	ns
			15	—	30	60	
			5	—		350	
Min removal time	t <sub>rem</sub>	_	10	—	—	150	ns
(RESET- CLOCK)			15	—		100	
			5				
Max clock input rise time	t <sub>rCL</sub>	_	10	No limit			μs
Max clock input fall time	t <sub>fCL</sub>		15				
Input capacitance	C <sub>IN</sub>	—	•		5	7.5	pF

### **Operating Supply Current Test Circuit**

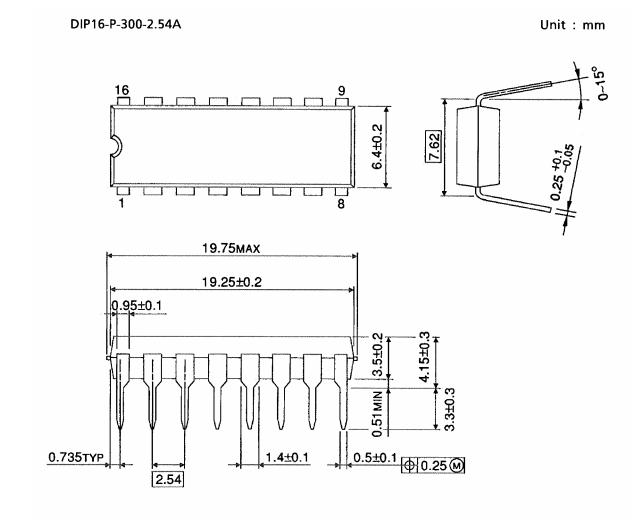
### Waveform 1



#### Waveform 2



### **Package Dimensions**



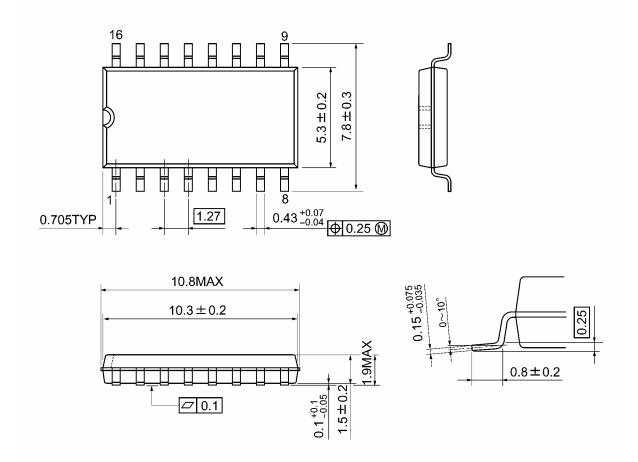
Weight: 1.00 g (typ.)



### **Package Dimensions**

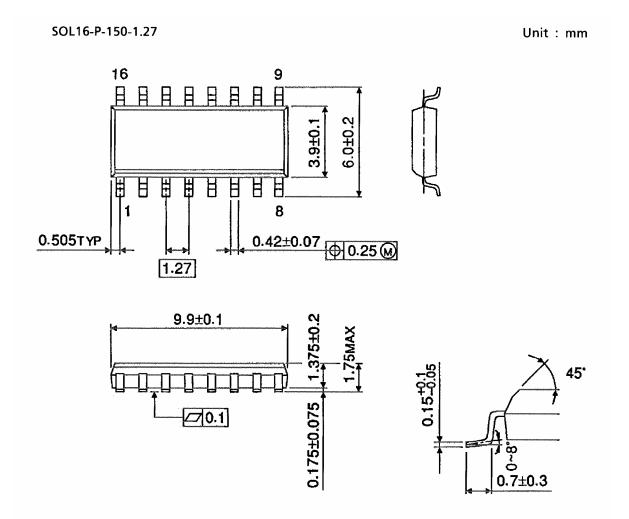
SOP16-P-300-1.27A

Unit: mm



Weight: 0.18 g (typ.)

### Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.13 g (typ.)

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20070701-EN GENERAL

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