TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH34FE

NON-INVERT BUFFER

Features

• Super high speed operation :tpD = 3.8 ns (typ.)

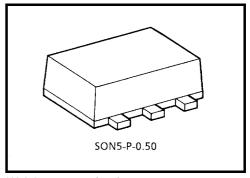
$$@V_{CC} = 5 V$$

• Low power dissipation : $I_{CC} = 2 \mu A$ (Max.)

$$@$$
 Ta = 25°C

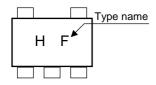
• High noise immunity : $V_{NIH} = V_{NIH}$

- 5.5V tolerant input.
- Wide operation voltage range : VCC (opr) = 2~5.5 V

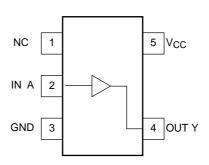


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)

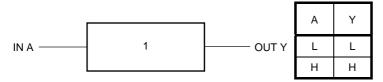


Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7	V
DC input voltage	V _{IN}	-0.5~7	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65~150	°C

Logic Diagram

Truth Table



Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~ V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	0~100 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V	
input rise and fair time	ui/uv	0~20 (V_{CC} = 5 V ± 0.5 V)		

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Circuit		Test	st . Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
		rest Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
High-level input					2.0	1.5	_	_	1.5	_	
voltage	V _{IH}	_	_		3.0~5.5	V _{CC} × 0.7			V _{CC} × 0.7		V
Low-level input				2.0	_		0.5	_	0.5		
voltage	V _{IL}	_	_		3.0~5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V
					2.0	1.9	2.0		1.9	_	
High-level voltage	_	V _{IN} = V _{IH}	I _{OH} = -50 μA	3.0	2.9	3.0		2.9		٧	
				4.5	4.4	4.5		4.4			
				$I_{OH} = -4 \text{ mA}$	3.0	2.58			2.48	_	
				$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
Low-level output voltage		$V_{IN} = V_{IL}$	I _{OL} = 50 μA	2.0	—	0	0.1		0.1	V	
				3.0	_	0	0.1	_	0.1		
	_			4.5	—	0	0.1		0.1		
			$I_{OL} = 4 \text{ mA}$	3.0	—	_	0.36		0.44		
				$I_{OL} = 8 \text{ mA}$	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	_	V _{IN} = 5.5 V	or GND	0~5.5	_	ı	±0.1	_	±1.0	μА
Quiescent supply current	I _{CC}	_	$V_{IN} = V_{CC} \alpha$	or GND	5.5	_		2.0	_	20.0	μА

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AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40~85°C		- Unit
			V _{CC} (V)	C _{L (} pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	tPLH tPHL	3.3 ± 0.3 5.0 ± 0.5	33+03	15	_	5.0	7.1	1.0	8.5	
			50	_	7.5	10.6	1.0	12.0	ns	
			15	_	3.8	5.5	1.0	6.5		
			50	_	5.3	7.5	1.0	8.5		
Input capacitance	C _{IN}				_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)		_	13	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

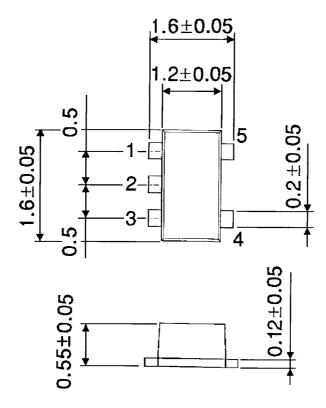
Average operating current can be obtained by the equation.

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

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Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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