TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SG04FU

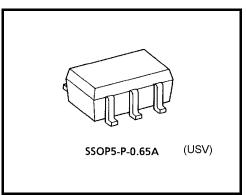
Inverter

Features

- High-level output current: $I_{OH}/I_{OL} = \pm 8 \text{ mA (min)}$ at V_{CC} = 3.0 V
- High-speed operation: t_{pd} = 2.3 ns (typ.)

at V_{CC} = 3.3 V,15pF

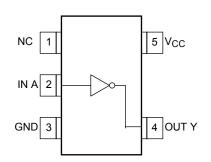
- Operating voltage range: V_{CC} = 0.9~3.6 V
- 5.5-V tolerant input.
- 3.6-V power down protection output.



Weight: 0.006 g (typ.)

Marking

W 5^K



Pin Assignment (top view)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	-0.5~4.6	V	
DC input voltage	V _{IN}	-0.5~7.0	V	
		-0.5~4.6 (Note 1)	V	
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5 (Note 2)		
Input diode current	IIK	-20	mA	
Output diode current	I _{OK}	-20 (Note 3)	mA	
DC output current	IOUT	±25	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	-65~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

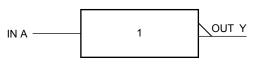
Note 1: $V_{CC} = 0V$

Note 3: V_{OUT} < GND

Note 2: High or Low State. I_{OUT} abusolute maximum rating must be observed.

<u>TOSHIBA</u>

IEC Logic Symbol



А	Y
L	Н
Н	L

Truth Table

Operating Ranges

Characteristics	Symbol	Value	Unit		
Power supply voltage	V _{CC}	0.9~3.6	V		
Input voltage	V _{IN}	0~5.5	V		
Output voltage	Vout	0~3.6 (Note 4)	V		
	VOUT	0~V _{CC} (Note 5)	v		
Output Current		±8.0 (Note 6)			
		±4.0 (Note 7)			
	1 /1	±3.0 (Note 8)	mA		
	I _{OH} /I _{OL}	±1.7 (Note 9)	ma		
		±0.3 (Note 10)			
		±0.02 (Note 11)			
Operating temperature	T _{opr}	-40~85	°C		
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V		

Note 4: $V_{CC} = 0V$

Note 5: High or Low state.

Note 6: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 7: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 8: $V_{CC} = 1.65 \sim 1.95 \text{ V}$

Note 9: $V_{CC} = 1.4 \sim 1.6 \text{ V}$

Note 10: $V_{CC} = 1.1 \sim 1.3 \text{ V}$

Note 11: $V_{CC} = 0.9 \ V$

Note 12: $V_{IN} = 0.8 \sim 2.0 \text{ V}, \text{ V}_{CC} = 3.0 \text{ V}$

DC Electrical Characteristics

Characteristics Symbol Test Condition			Condition		Ta = 25°C			Ta = -40~85°C		Unit
		Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
					V _{CC}	_	_	V _{CC}	_	-
					V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
High-level V _{IH} input voltage			1.4~1.6	$\begin{array}{c} V_{CC} \\ \times \ 0.65 \end{array}$	_	_	V _{CC} × 0.65	_	V	
			1.65~1.95	$\begin{array}{c} V_{CC} \\ \times \ 0.65 \end{array}$			V _{CC} × 0.65			
			2.3~2.7	1.7	_	_	1.7	_		
				3.0~3.6	2.0			2.0		
				0.9	_		GND		GND	
			1.1~1.3		_	$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	_	$V_{CC} \times 0.3$	V	
Low-level	VIL		_				$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$	_		$\begin{array}{c} V_{CC} \\ \times \ 0.35 \end{array}$
input voltage				1.65~1.95			V _{CC} × 0.35			V _{CC} × 0.35
				2.3~2.7	_	_	0.7			0.7
				3.0~3.6			0.8			0.8
		VIN = VIL	I _{OH} =-0.02 mA	0.9	0.75			0.75		V
			I _{OH} = -0.3 mA	1.1~1.3	V _{CC} × 0.75			V _{CC} × 0.75		
High-level Vон	V _{ОН}		I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75			V _{CC} × 0.75		
output voltage			I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45			V _{CC} -0.45		
			I _{OH} = -4.0 mA	2.3~2.7	2.0			2.0		
			I _{OH} = -8.0 mA	3.0~3.6	2.48			2.48		
			I _{OL} = 0.02 mA	0.9	_	_	0.1	—	0.1	
Low-level V _{OL}		I _{OL} = 0.3 mA	1.1~1.3	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$		
	V _{OL}	OL VIN = VIH	I _{OL} = 1.7 mA	1.4~1.6	_	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	_	$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	V
			I _{OL} = 3.0 mA	1.65~ 1.95			0.45	_	0.45	
			$I_{OL} = 4.0 \text{ mA}$	2.3~2.7	_	_	0.4	—	0.4	
			I _{OL} = 8.0 mA	3.0~3.6	_		0.4	—	0.4	
Input leakage current	I _{IN}	V _{IN} = 0~5.5V		0~3.6			±0.1	_	±1.0	μA
Power off leakage current	I _{OFF}	$V_{IN} = 0 \sim 5.5 V$ $V_{OUT} = 0 \sim 3.6 V$		0	_	_	1.0	_	10.0	μΑ
Quiescent supply current	ICC	$V_{IN} = V_{CC}$	or GND	3.6	_	_	1.0	—	10.0	μA

AC Electrical Characteristics (input $t_r = t_f = 3 \text{ ns}$)

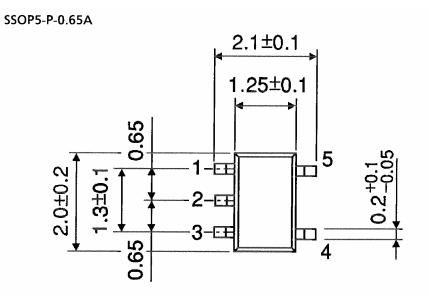
Observation	Oursela d	Taat Oan dition		Ta = 25°C			Ta = −40~85°C		
Characteristics Symb		Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		$C_L = 10 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	18.6		_	_	
			1.1~1.3	_	8.7	18.4	1.0	34.2	
			1.4~1.6	_	4.9	8.5	1.0	10.0	
			1.65~ 1.95		3.8	6.2	1.0	6.7	
			2.3~2.7	_	2.6	3.9	1.0	4.4	
Propagation delay time			3.0~3.6	_	2.1	3.1	1.0	3.7	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	21.0		_	_	
	tplh tphl		1.1~1.3	_	9.8	21.5	1.0	37.1	ns
			1.4~1.6		5.4	9.3	1.0	11.2	
			1.65~ 1.95		4.2	6.9	1.0	7.1	
			2.3~2.7	_	2.8	4.4	1.0	5.0	
			3.0~3.6	_	2.3	3.4	1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	31.2	_	_	_	
			1.1~1.3		13.8	29.6	1.0	56.0	
			1.4~1.6		7.4	13.1	1.0	15.9	
			1.65~ 1.95	_	5.6	9.2	1.0	9.6	
			2.3~2.7	_	3.7	5.7	1.0	6.1	
			3.0~3.6		2.9	4.4	1.0	4.8	
Input capacitance	C _{IN}	—	3.6		3			—	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9~3.6		6		_		pF

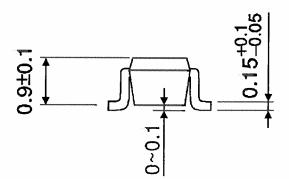
Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation: $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions





Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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