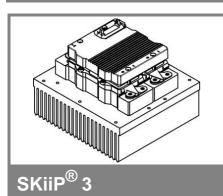
SKiiP 1213GB123-2DL



2-pack-integrated intelligent Power System

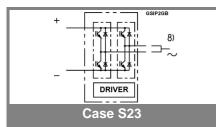
Power section

SKiiP 1213GB123-2DL

Data

Power section features

- SKiiP technology inside
- Trench IGBTs
- CAL HD diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP[®] 3 System)
- IEC 60068-1 (climate) 40/125/56)
- UL recognized file no. E63532
- with assembly of suitable MKP capacitor per terminal
- AC connection busbars must be connected by the user; copper busbars available on request



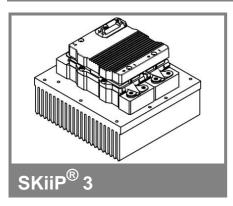
Absolute	Maximum Ratings	$\Gamma_{s} = 25^{\circ}C$ unless otherwise specified						
Symbol Conditions		Values	Units					
IGBT								
V _{CES} V _{CC} ¹⁾		1200	V					
	Operating DC link voltage	900	V					
V _{GES}		± 20	V					
I _C	T _s = 25 (70) °C	1200 (900)	А					
Inverse o	Inverse diode							
I _F = - I _C	T _s = 25 (70) °C	930 (700)	А					
I _{FSM}	$T_{j} = 150 \text{ °C}, t_{p} = 10 \text{ ms}; \text{ sin.}$	6900	А					
I²t (Diode)	Diode, T _j = 150 °C, 10 ms	238	kA²s					
T _j , (T _{stg})		- 40 + 150 (125)	°C					
V _{isol}	rms, AC, 1 min, main terminals to heat sink	3000	V					
I _{AC-terminal}	per AC terminal, rms, T _s = 70 °C,	400	А					
	T _{terminal} <115 °C							

Charact	eristics	T _s = 25	°C unless	otherwise	specifie
Symbol	/mbol Conditions		typ.	max.	Units
IGBT					
V _{CEsat}	I_{C} = 600 A, T_{j} = 25 (125) °C; measured at terminal		1,7 (1,9)	2,1	V
V _{CEO}	T _i = 25 (125) °C; at terminal		0,9 (0,8)	1,1 (1)	v
r _{CE}	T _i = 25 (125) °C; at terminal		1,3 (1,8)	1,7 (2,2)	mΩ
I _{CES}	V _{GE} = 0 V, V _{CE} = V _{CES} , T _i = 25 (125) °C		2,4 (72)		mA
E _{on} + E _{off}	$I_{\rm C} = 600 \text{ A}, V_{\rm CC} = 600 \text{ V}$		221		
	T _i = 125 °C, V _{CC} = 900 V		390		mJ
R _{CC+EE}	terminal chip, T _i = 25 °C		0,25		mΩ
L _{CE}	top, bottom		6		nH
C _{CHC}	per phase, AC-side		3,4		nF
Inverse	diode	•			
V _F = V _{EC}	I _F = 600 A, T _j = 25 (125) °C measured at terminal		1,5 (1,5)	1,8	V
V _{TO}	T _i = 25 (125) °C		0,9 (0,7)	1,1 (0,9)	v
r _T	T _i = 25 (125) °C		1 (1,3)	1,1 (1,5)	mΩ
E _{rr}	$I_{\rm C}$ = 600 A, V _{CC} = 600 V		42		mJ
	T _j = 125 °C, V _{CC} = 900 V		56		mJ
Mechan	ical data				•
M _{dc}	DC terminals, SI Units	6		8	Nm
M _{ac}	AC terminals, SI Units	13		15	Nm
W	SKiiP [®] 3 System w/o heat sink		1,7		kg
w	heat sink		5,4		kg

60747-15)									
R _{th(j-s)I}	per IGB	Т					0,03	K/W	
R _{th(j-s)D}	per diod	е					0,058	K/W	
Z _{th}	R _i (mK/\	R _i (mK/W) (max. values)				tau _i (s)			
	1	2	3	4	1	2	3	4	
Z _{th(j-r)I}	9,8	16,4	3,8	0	0,37	0,06	0,01	1	
Z _{th(j-r)D}	10	24	24	36	50	5	0,25	0,04	
Z _{th(r-a)}	4,3	20,3	7,1	2,3	160	53	9	0,4	

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SKiiP 1213GB123-2DL



2-pack-integrated intelligent Power System

2-pack integrated gate driver SKiiP 1213GB123-2DL

Data

Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlock of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute	• Maximum Ratings	$a = 25^{\circ}$ C unless otherwise specified		
Symbol	Conditions	Values	Units	
V _{S2}	unstabilized 24 V power supply	30	V	
V _i	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/µs	
V _{isollO}	input / output (AC, rms, 2s)	3000	V	
VisoIPD	partial discharge extinction voltage, rms, $Q_{PD} \leq 10 \text{ pC}$;	1170	V	
V _{isol12}	output 1 / output 2 (AC, rms, 2s)	1500	V	
f _{sw}	switching frequency	15	kHz	
f _{out}	output frequency for I _{peak(1)} =I _C	15	kHz	
$T_{op} (T_{stg})$	operating / storage temperature	- 40 + 85	°C	

Characte	(T _a			= 25 °C)	
Symbol	Conditions	min.	typ.	max.	Units
V _{S2}	supply voltage non stabilized	13	24	30	V
I _{S2}	V _{S2} = 24 V	278+25*f/	′kHz+0,0002	22*(I _{AC} /A) ²	mA
V _{iT+}	input threshold voltage (High)			12,3	V
V _{iT-}	input threshold voltage (Low)	4,6			V
R _{IN}	input resistance		10		kΩ
C _{IN}	input capacitance		1		nF
t _{d(on)IO}	input-output turn-on propagation time		1,3		μs
t _{d(off)IO}	input-output turn-off propagation time		1,3		μs
t _{pERRRESET}	error memory reset time	9			μs
t _{TD}	top / bottom switch interlock time		3,3		μs
I _{analogOUT}	max. 5mA; 8 V corresponds to 15 V supply voltage for external components		1200		A
I _{s1out}	max. load current			50	mA
I _{TRIPSC}	over current trip level				
	(I _{analog} OUT = 10 V)		1500		A
T _{tp}	over temperature protection	110		120	°C
UDCTRIP	U _{DC} -protection (U _{analog OUT} = 9 V);	i	not implemente	d	V
	(option for GB types)				

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