TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK2998

Chopper Regulator, DC-DC Converter Applications

Unit: mm

•	Low drain-source ON res	istance	$: RDS (ON) = 11.5 \Omega (typ.)$
•	High forward transfer adr	mittance	$ Y_{fs} = 0.4 \text{ S (typ.)}$
•	Low leakage current	$: I_{DSS} = 1$	$00 \mu\text{A} (\text{max}) (\text{V}_{ ext{DS}} = 500 \text{V})$
•	Enhancement mode	$: V_{th} = 2.0$	$0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	500	V	
Drain-gate voltage (Ro	_{SS} = 20 kΩ)	V_{DGR}	500	V	
Gate-source voltage		V_{GSS}	±30	V	
Drain current	DC (Note 1)	I _D	0.5	Α	
Dialii Cuiteiii	Pulse (Note 1)	I _{DP}	1.5	Α	
Drain power dissipation	١	P_{D}	0.9	W	
Channel temperature	nnel temperature		150	°C	
Storage temperature ra	ange	T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5.1 max 0.75 max 1.0 max 10.5 min 0.8 max 0.6 max 1.27 1.27 2.54 l max 1 2 3 1.SOURCE 2.DRAIN 3.GATE **JEDEC** TO-92MOD JEITA TOSHIBA 2-5J1C

Weight: 0.36 g (typ.)

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch-a)}	138	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

This transistor is an electrostatic-sensitive device.

Please handle with caution.

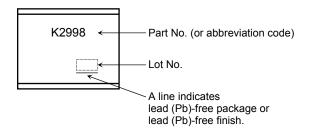
Electrical Characteristics (Ta = 25°C)

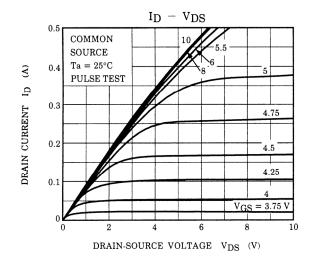
Charac	cteristics	Symbol	Test Condition		Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μΑ
Gate-source breakdown voltage		V (BR) GSS	I _D = ±10 mA, V _{GS} = 0 V		_	_	V
Drain cut-off current		I _{DSS}	V _{DS} = 500 V, V _{GS} = 0 V	_	_	100	μΑ
Drain-source breakdown voltage		V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	500	_	_	V
Gate threshold v	voltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source ON resistance		R _{DS} (ON)	V _{GS} = 10 V, I _D = 0.25 A		11.5	18	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 0.25 A	0.2	0.4	_	S
Input capacitance		C _{iss}			75	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		7	_	pF
Output capacitance		Coss			25	_	
	Rise time	t _r	V_{GS} V_{OV} V_{OUT} V_{OUT} V_{DD} V_{DD} V_{DD} V_{DD}	_	11	_	
Switching time	Turn-on time	t _{on}		ı	18		ns
Switching time	Fall time	t _f		_	54	_	115
	Turn-off time	t _{off}		_	95	_	
Total gate charge (gate-source plus gate-drain)		Qg			3.8		
Gate-source charge		Q _{gs}	$V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 0.5 \text{ A}$		1.9	_	nC
Gate-drain ("miller") charge		Q _{gd}			1.9	_	

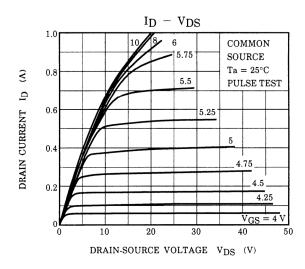
Source-Drain Ratings and Characteristics (Ta = 25°C)

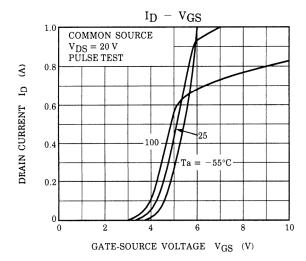
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	0.5	Α
Pulse drain reverse current (Note 1)	I _{DRP}	-	_	_	1.5	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 0.5 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 0.5 A, V _{GS} = 0 V		190	_	ns
Reverse recovery charge	Q _{rr}	dI_{DR} / $dt = 100 \text{ Å}$ / μs		380	_	nC

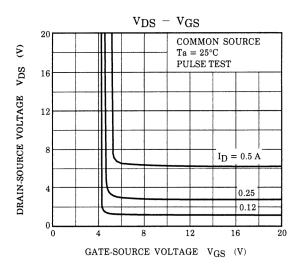
Marking

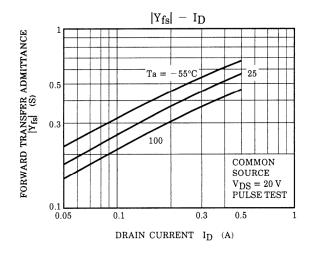


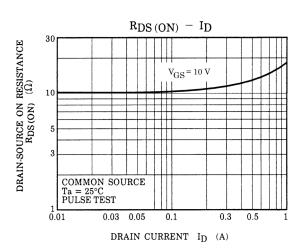


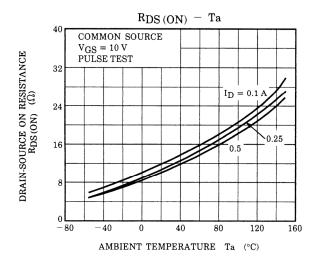


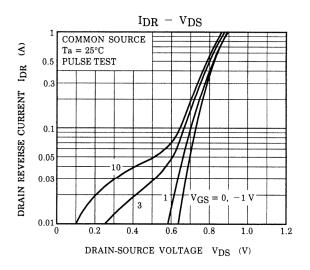


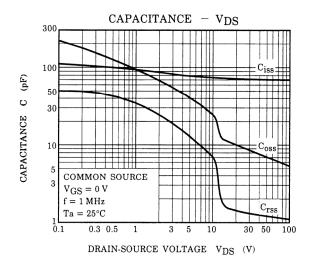


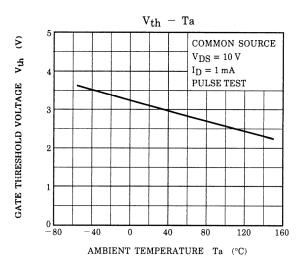


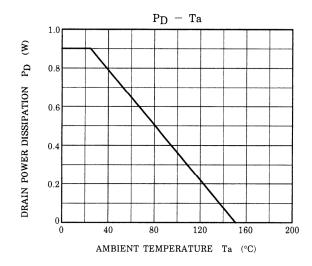


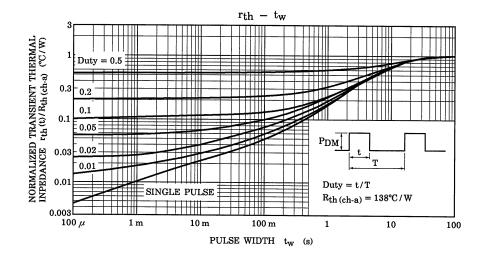


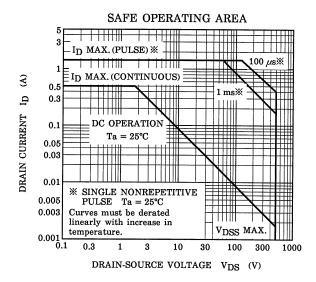












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