Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L^2 - π -MOSV)

2SK2846

Chopper Regulator, DC-DC Converter and Motor Drive Applications

 $\begin{array}{ll} \bullet & Low \ drain-source \ ON \ resistance & : R_{DS} \ (oN) = 4.2 \ \Omega \ (typ.) \\ \bullet & High \ forward \ transfer \ admittance & : |Y_{fs}| = 1.7 \ S \ (typ.) \\ \bullet & Low \ leakage \ current & : I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 600 \ V) \\ \bullet & Enhancement \ mode & : V_{th} = 2.0 \sim 4.0 \ V \ (V_{DS} = 10 \ V, \ I_{D} = 1 \ mA) \\ \end{array}$

Absolute Maximum Ratings (Ta = 25°C)

Charac	eteristics	Symbol	Rating	Unit	
Drain-source volta	ge	V_{DSS}	600	V	
Drain-gate voltage	(R _{GS} = 20 kΩ)	V_{DGR}	600	V	
Gate-source voltage	ge	V _{GSS}	±30	V	
Drain current	DC (Note 1)	ID	2	Α	
	Pulse (t = 1 ms) (Note 1)	I _{DP}	5	А	
	Pulse (t = 100 µs) (Note 1)	I _{DP}	8	А	
Drain power dissipa	ation	P_{D}	1.3	W	
Single pulse avalanche energy (Note 2)		E _{AS}	93	mJ	
Avalanche current		I _{AR}	2	Α	
Repetitive avalanch	he energy (Note 3)	E _{AR}	0.13	mJ	
Channel temperatu	ire	T _{ch}	150	°C	
Storage temperatu	re range	T _{stg}	-55~150	°C	

1.4±0.1 1.05±0.1 1.05±0.1 1.55±0.5 1.50URCE 2.5±0.5 1.50URCE 2.DRAIN 3.GATE

2-8M1B

Weight: 0.54 g (typ.)

JEDEC JEITA TOSHIBA

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient	R _{th (ch-a)}	96.1	°C/W

Note 1: Ensure that the channel temperature does not exceed 150 $^{\circ}$ C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 41 mH, R_G = 25 Ω , I_{AR} = 2 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device.

Please handle with caution.



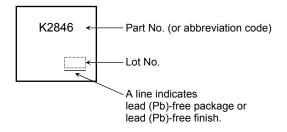
Electrical Characteristics (Ta = 25°C)

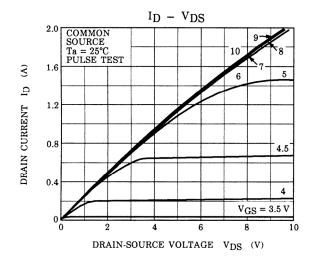
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	irrent	I _{GSS}	V _{GS} = ±25 V, V _{DS} = 0 V	_	_	±10	μA
Gate-source bre	eakdown voltage	V (BR) GSS	I _D = ±10 μA, V _{GS} = 0 V	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	600	_	_	V
Gate threshold v	voltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 1 A		4.2	5.0	Ω
Forward transfer	r admittance	Y _{fs}	V _{DS} = 10 V, I _D = 1 A	0.8	1.7	_	S
Input capacitano	e	C _{iss}			380	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	40	_	pF
Output capacitance		Coss			120	_	
Switching time	Rise time	t _r	V_{GS} $0V$ $R_{L}=200\Omega$ $V_{DD}=200V$	_	15	_	ns ns
	Turn-on time	t _{on}		_	25	_	
	Fall time	t _f		_	20	_	
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_{\mathbf{w}} = 10 \mu \text{s}$	_	80	_	
Total gate charge (gate-source plus gate-drain)		Qg			9		
Gate-source charge		Q _{gs}	$V_{DD} \approx 480 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 2 \text{ A}$		5	_	nC
Gate-drain ("miller") Charge		Q_{gd}		_	4	_	

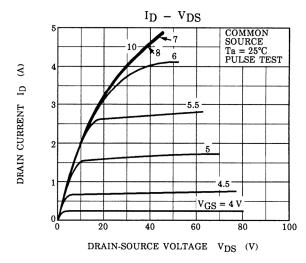
Source-Drain Ratings and Characteristics (Ta = 25°C)

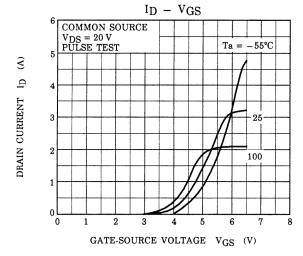
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	2	Α
Pulse drain reverse current (Note 1)	I _{DRP}	t = 1 ms	_	_	5	Α
	I _{DRP}	t = 100 μs	_	_	8	Α
Forward voltage (diode)	V_{DSF}	I _{DR} = 2 A, V _{GS} = 0 V	_	_	-1.5	V
Reverse recovery time	t _{rr}	I _{DR} = 2 A, V _{GS} = 0 V	_	1000	_	ns
Reverse recovery charge	Qrr	dI _{DR} / dt = 100 A / μs	_	3.5	_	μC

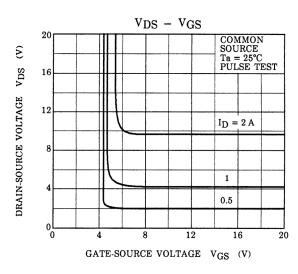
Marking

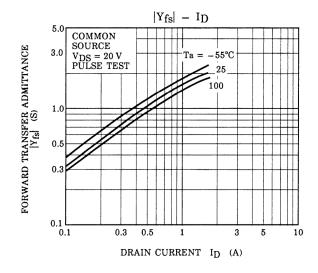


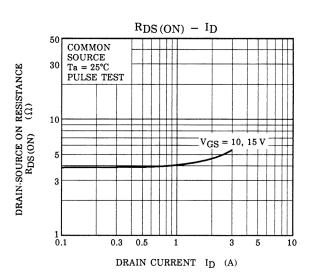


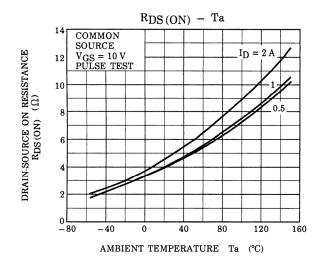


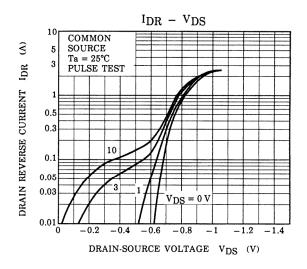


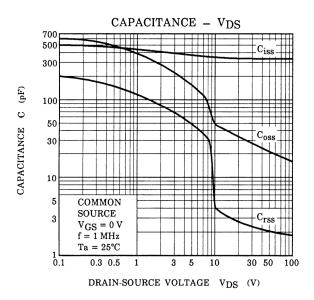


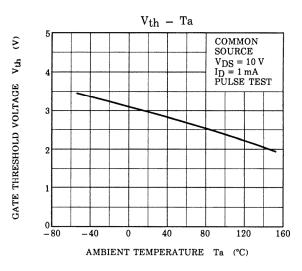


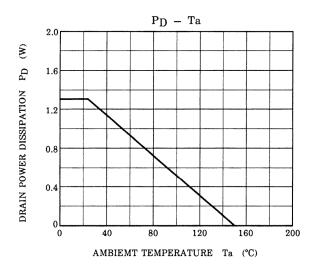


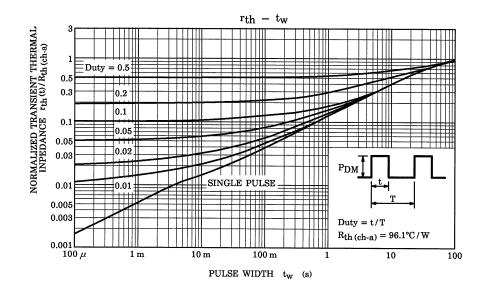


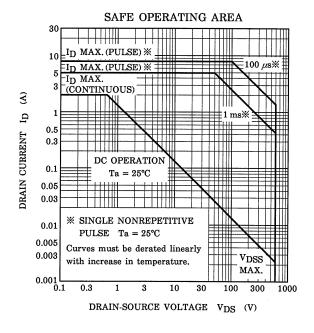


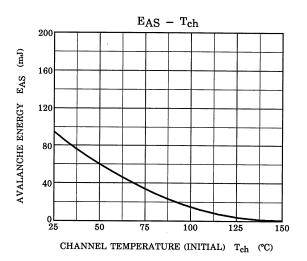


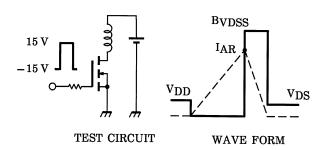












$$R_G = 25 \Omega$$

 $V_{DD} = 90 \text{ V}, L = 41 \text{ mH}$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{B_{VDSS}}{B_{VDSS} - V_{DD}} \right)$$

RESTRICTIONS ON PRODUCT USE

20070701-EN

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