Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

# 2SK2467

#### **High-Power Amplifier Application**

• High breakdown voltage: VDSS = 180 V

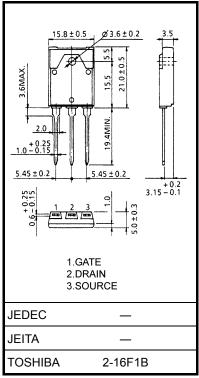
• High forward transfer admittance:  $|Y_{fs}| = 4.0 \text{ S (typ.)}$ 

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	$V_{DSS}$	180	V
Gate-source voltage	$V_{GSS}$	±20	V
Drain current (Note 1)	ΙD	9	Α
Drain power dissipation (Tc = 25°C)	$P_{D}$	80	W
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 5.8 g (typ.)

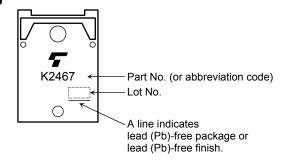
#### **Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 180 V, V <sub>GS</sub> = 0	_	_	1.0	mA
Gate leakage current	I <sub>GSS</sub>	V <sub>DS</sub> = 0, V <sub>GS</sub> = ±20 V	_	_	±0.5	μΑ
Drain-source breakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0	180	_	_	V
Drain-source saturation voltage	V <sub>DS</sub> (ON)	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A	_	2.5	5.0	V
Gate-source cut-off voltage (Note 3)	V <sub>GS</sub> (OFF)	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.1 A	1.4	_	2.8	V
Forward transfer admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 3 A	_	4.0	_	S
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0, f = 1 MHz	_	700	_	pF
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0, f = 1 MHz	_	150	_	pF
Reverse capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0, f = 1 MHz	_	90	_	pF

Note 3: V<sub>GS (OFF)</sub> classification Y: 1.4 to 2.8

This transistor is an electrostatic-sensitive device. Plese handle with caution.

## Marking



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